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THE AGS BOOSTER BEAM POSITION MONITOR SYSTEM*

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Abstract

To accelerate both protons and heavy ions, the AGS Booster requires a broadband (multi-octave) beam position monitoring system with a dynamic range spanning several orders of magnitude (2 x 10^{10} to 1.5×10^{13} particles per pulse). System requirements include the ability to acquire single turn trajectory and average orbit information with ± 0.1 mm resolution. The design goal of ± 0.5 mm corrected accuracy requires that the detectors have repeatable linear performance after periodic bakeout at 300°C. The system design and capabilities of the Booster Beam Position Monitor will be described, and initial results presented.

Introduction

The measurement parameters for this system are [1]: Absolute accuracy: + 0.5 mm

Absolute accuracy.	_	0.5	
Precision:	±	0.1	mm
Circular aperture:	±	30	mm





Figure 1 is a block diagram of the AGS Booster BPM system. There are 46 BPM stations within the Booster ring, each responsible for average orbit measurement in a single plane



Fig. 1 Booster BPM System Block Diagram.

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(22 horizontal and 24 vertical). Six additional stations are located in the Booster To AGS (BTA) transfer line. At each station there is a beam sensor located at the appropriate betamax and locked to a quadrupole on the central orbit. Signals from the detector go to processing and acquisition hardware, located on the floor of the Booster just below each quadrupole (Figure 2). Digitized information is retrieved from each station up to six times per Booster cycle. The system provides average orbit information (over up to 80 turns) or first turn trajectory. In addition, analog real-time outputs of either the bunch signals or their sum and difference are available from any location for use by other systems.

The Detector

The beam sensor is the split cylinder electrostatic type, chosen for its flat low frequency response and linear induced electrode signal vs. beam position. Each detector has been tested prior to its installation in the Booster, and has a verified linearity and resolution of greater than \pm 0.1 mm over the 60 mm circular measurement aperture [2]. A built-in calibration ring couples equally to both pick up electrodes (PUEs), simulating a centered beam when a test signal is applied from the electronics. This can be used to obtain a value for the total system offset, referenced to beampipe center. There are four rf N-type vacuum feedthrus which bring signals into and out of the detector; two for the calibration ring (one of which is terminated with 50 ohms), and one for each PUE of the split cylinder.

Signal Cable Assemblies

A 3 meter length of standard 93 ohm coaxial cable, modified to have a solid shield, is used to carry the PUE signals from the detector to the processing electronics (Fig. 3).



Fig. 3 Mock-up of PUE Signal Cable Assembly.

The length of the cable is a compromise between several factors. The main objective was to locate the processing electronics far enough away from the beamline to minimize radiation damage. The length must be limited, however, to reduce signal loss due to capacitive loading of the PUEs, and to prevent excessive peaking at the top end of the bunch signal response. A 200 ohm resistor (at the PUE end of the assembly) helps to flatten this peaking at the high end of the processing bandwidth (approximately 25 MHz). RG-62 coaxial cable was chosen for its low cost, moderate capacitance per unit length (46 pF/m) and reasonable radiation survivability (semi-solid polyethylene). The cable is enclosed by standard 3/8 inch copper tubing, which is electrically connected to the

shield at both ends of the assembly. Each assembly is measured on a network analyzer, and units are paired by matching their responses to within \pm 0.01 dB (to 30 MHz).

The use of Thermal Isolators at each of the four vacuum feedthrus on the detector allows signal cables to remain connected during bakeout at 300°C. The isolators are effectively rf coaxial air lines, 14 cm in length, with male N type connectors on either end. This length is negligible as compared to the processing bandwidth of the electronics, and adds only 8 pF to the total capacitance load on each PUE.



Fig. 4 Two versions of the Thermal Isolator.

Stainless steel construction provides a temperature drop (at the center conductor, 25° C ambient) from 200°C to approximately 60°C. In those areas with mechanical obstructions near the beampipe, an angled version of the Thermal Isolator is used instead. Capacitance variations between assemblies are less than \pm 0.1 pF.

Local BPM Signal Processing Electronics

The Local Signal Processing crate is built into a Eurocard style subrack for modularity (Figure 5). The body of the subrack is isolated from ground and allowed to float at beampipe



Fig. 5 Local BPM Processing Electronics.

potential for purposes of increased noise immunity. The top right slot is reserved for a dual analog fiberoptic transmitter module, used to isolate the electronics from any external systems requiring beam position information. A Test Access Panel (TAP) is included in each crate as a maintainence aide. This card allows accessibility to backplane signals, and has provision for switching the crate to local control.

The local processing electronics are divided into three major sections [3]; Front End Processing (FEP), Acquisition (ACQ) and Calibration and Local Timing (CLT). The FEP module provides appropriate PUE signal scaling and frequency compensation. Both channels of the FEP are matched to within 0.02 dB (to 20 MHz) in all three gain modes, resulting in a position accuracy for this module of better than 0.3 mm. A ± 1 dB flatness preserves the temporal response of the PUE signals for bunch widths down to 50 ns (half max) [4]. Each module has a secondary set of outputs which provides either buffered PUE signals or their sum and difference. These can be used for diagnostic purposes, or as real-time position information by other systems.

Two Acquisition (ACQ) modules and the Calibration and Local Timing (CLT) module form the next PUE signal processing step. Sum and difference signals from the FEP are baseline restored (the capacitively coupled PUE signals have zero average value), integrated then digitized by the ACQ. Local timing and control signals are generated by the CLT module. This module receives a properly phased pulse burst from the Sequencer and generates the BLR, integrate gating and reset pulses for each ACQ. The number of beam bunches over which to integrate can be selected from 1 to 255 (up to 85 turns), and the resulting integrals are digitized to 13 bits. In addition, the CLT module can generate pulses to drive the calibration ring of the detector at five selectable frequencies. This can be used to check system connections and to measure total system offset errors [5].

The Controller Interface Electronics (CIE) card is the local interface between the crate and the BPM Instrument Controller. This board passes digital data, control and status signals to and from the Controller via a fiberoptic version of DATACON which eliminates ground loops and allows the crate to float at beampipe potential. The CIE card also serves as the fiberoptic interface between the Sequencer and the CLT module.

Sequencer

The Sequencer is located remote from the Booster ring and connected to each of the 52 BPM stations via digital fiberoptic link [6]. The Booster Low Level RF (LLRF) system supplies the Sequencer with f_{rf} and $48xf_{rev}$, both of which are phase locked to the beam [7] (beam phase information is fed back to the LLRF system from the secondary sum output of an FEP module in the ring). These two signals are used to generate a burst (of specified duration) of BLR pulses for use by the Calibration and Local Timing module at each BPM station. The BLR pulses are phased such that they always occur between bunches, independent of harmonic number. The LLRF system also supplies the Sequencer with triggers which indicate the start of the first and last turns in the Booster cycle. Since the BPM stations in the extraction line (BTA) always see three bunches at the end of the acceleration cycle, no baseline restoration of the sum and difference signals are necessary before integration. For the six BPM stations in the BTA line, the Sequencer always outputs a burst of three BLR pulses.

High Level Software Control

The BPM Instrument Controller interfaces the host computer (Apollo system) with the Sequencer and the local processing electronics, and provides reports to the software of PUE sum and difference data from one or more samples during the Booster cycle.

The "booster_orbit" program is the high level applications software for the Beam Position Monitor, running on the Apollo computer system. Its primary function is to graphically represent the equilibrium orbit of the Booster ring at any time during the cycle. The program allows for the acquisition of data with varying parameters such as number of turns (over which to integrate), number of BPM Booster groups to acquire information from, acquisition times and gain settings. The program also allows for a variety of displays using saved, current or difference orbits with varying modes of averaging and plotting parameters. Statistical display options are also available.

In addition to orbit acquisition and display, the scope of the program extends to diagnostic testing of the hardware, full calibration of each of the local BPM stations and display/update of static and dynamic BPM parameters (e.g. values of differential sensitivity, system offsets and operational status) used in the position calculations.

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