

A NOVEL CURRENT MONITOR FOR DC AND MODULATED MAGNETS IN THE PROTON STORAGE RING*

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The zero flux dc current transformer has been described before in literature.¹⁻² This paper endeavors to present a more analytical approach. The method of demodulating the dc channel is novel, using a time-domain scheme rather than the usual second-harmonic method. The current monitor measures bipolar currents up to 2 000 A without physical contact of the single-turn primary. Choosing the secondary current as 1 A sets the turns ratio at 1:2000. The magnetic material used is of the molybdenum Permalloy variety with its high (30 000) initial permeability and almost square hysteresis loop. Its saturation flux density is about 0.74 T. The toroidal geometry of the tape-wound transformer insures almost unity coupling of the windings.

As depicted in Fig. 1, the ac channel consists of one toroidal transformer with bifilar wound secondaries. R_2 and the input impedance of the differential input amplifier must be very large; otherwise they would rob ampere-turns from $N_2 I_f$, and a serious error would appear in the output voltage across R_0 . As seen in the figure, the ac channel has a 3-db bandwidth from a low-frequency pole to infinity. The amplifier extends the transformer output response to lower frequencies by a factor of almost A_F ; and the mutual inductance of the windings takes over at higher frequencies above the bandwidth of the amplifier.

At frequencies above ~ 500 kHz the resonances in the transformer's secondary windings become active and a simple low pass R-C filter could be used to eliminate them. The output voltage across R_0 is not within the feedback network. Therefore R_0 must be a very high-quality resistor in terms of temperature stability and accuracy.

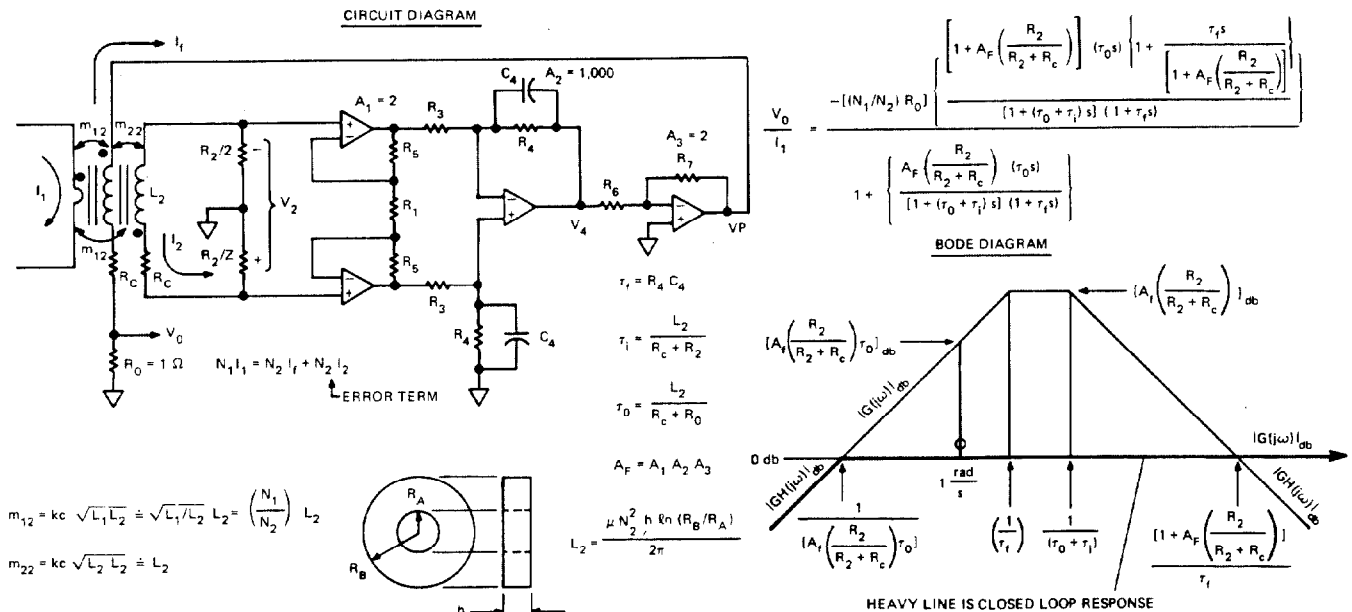
Figure 2 shows a dc channel. Two toroidal transformers are used that are identical to the one employed in the ac channel. This circuit is obviously a sampled data or discrete system. The state of the

net error current, $I_E = (N_1/N_2)I_1 - I_f$, is determined every period, T_s , of the square wave switching voltage V_{DR} . If the net error current I_E is not zero, a correction is made by I_f to bring it toward zero. I_f also flows through R_0 producing an output voltage, V_0 , proportional to the input current I_1 .

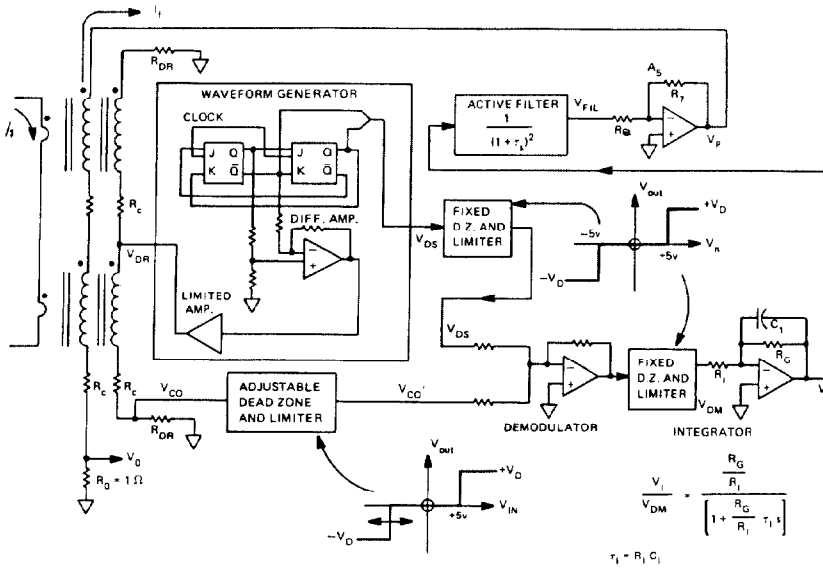
The best way to understand this dc channel is to study the actual waveforms in Fig. 2 that are shown for two conditions: with $I_E = 0$ and with $I_E > 0$. I_1 is greater than zero in both cases and steady. The toroids, flux biased by I_E , are both switched by V_{DR} from one saturated state to the other in such a way that, except for a very short interval proportional to I_E , no net voltage is induced in the primary or the secondary windings in series with R_0 . Thus the secondaries in series present only $2R_C$ as a load to the power amplifier V_p . The toroids must be selected and matched by their switching time under a constant voltage low-impedance source. The signal derived from R_{DR} in series with the switching windings is squared up by an adjustable dead zone and limiter circuit.³ The purpose of this adjustment is to zero the output voltage with $I_1 = 0$. Note the finite slope of the signal across R_{DR} as the toroids go into saturation.

The voltage switching signal V_{DR} and the demodulator signal V_{DS} must be in synchronism and are derived from a two-stage Johnson counter. These unipolar, logic level signals are level shifted by nonsaturating, differential input amplifiers to provide the 11.5-V bipolar signals that feed the dead-zone and limiter circuits. The actual demodulator is a simple summing amplifier circuit that adds and inverts the demodulating signal and the squared signal across R_{DR} followed by another dead-zone and limiter circuit. Thus the demodulator provides a bipolar, impulse-like signal of strength V_{DT_0} when $I_E = 0$. T_0 is made short by adjusting the frequency $1/T_s$ of the clock that drives the Johnson counter so that the toroids saturate, with $I_E = 0$, about 50 μ s before $T_s/4$.

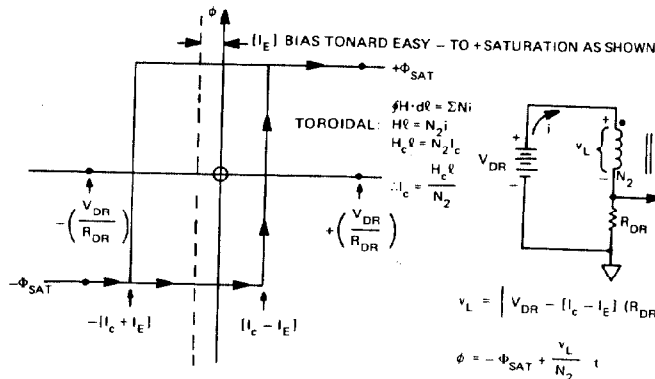
*Work performed under the auspices of U.S. Department of Energy.



CIRCUIT DIAGRAM



BIASED HYSTERESIS MODEL



$$\phi(t_{SAT}) = +\phi_{SAT} - \phi_{SAT} + \left(\frac{V_L}{N_2}\right) t_{SAT}; \text{ OR } t_{SAT} = \frac{2 N_2 \phi_{SAT}}{V_L}$$

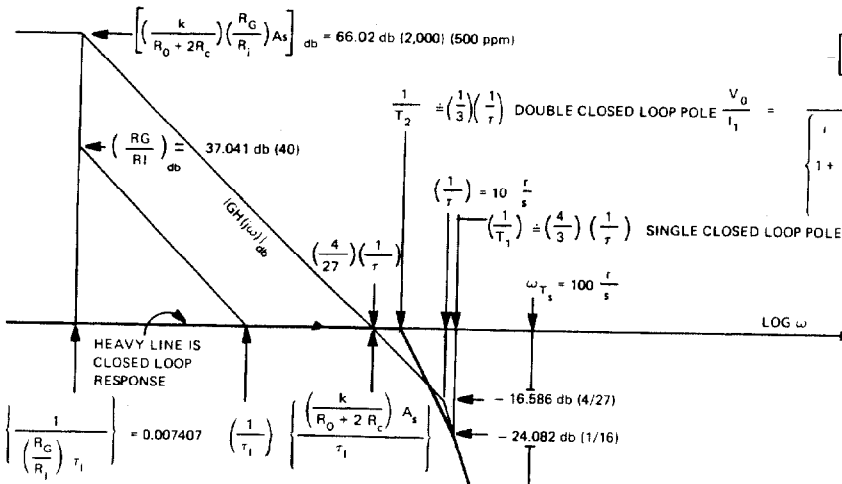
$$\frac{d t_{SAT}}{d I_E} = \frac{-2 N_2 \phi_{SAT} R_{DR}}{(V_L)^2}; \quad m = \left| \frac{d t_{SAT}}{d I_E} \right|_{I_E=0} = \frac{2 N_2 \phi_{SAT} R_{DR}}{(V_{DR} - I_c R_{DR})^2}$$

AT $I_E = 0$ BECAUSE I_E IS SMALL

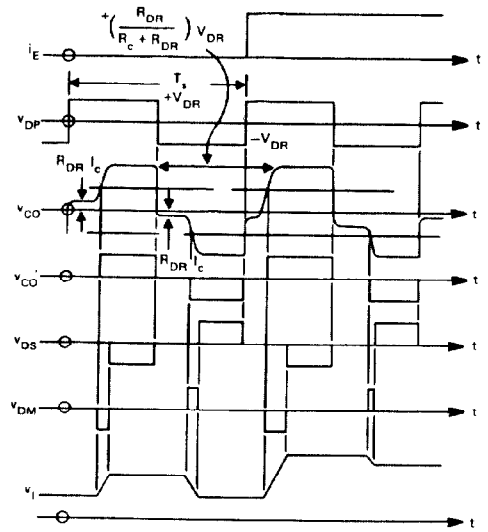
$$k = \frac{2m V_D}{T_s} = \frac{V_D R_{DR}}{2(V_{DR} - I_c R_{DR})} = \left(\frac{V_D R_{DR}}{2 V_{DR}}\right) \left[\frac{1}{1 - \frac{I_c R_{DR}}{V_{DR}}} \right] \approx \left(\frac{V_D R_{DR}}{2 V_{DR}}\right)$$

$$T_s \approx (4) \left| t_{SAT} \right|_{I_E=0}$$

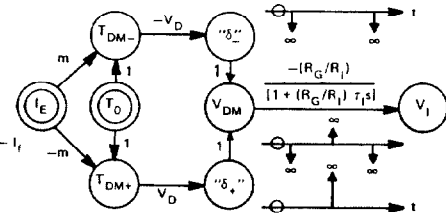
BODE DIAGRAM



ACTUAL WAVEFORMS



SAMPLED DATA SIGNAL FLOW GRAPH



$$V_{DM-} = -V_D T_{DM-} = -V_D T_D - V_{DM} I_E$$

$$V_{DM+} = V_D T_{DM+} = V_D T_D - V_{DM} I_E$$

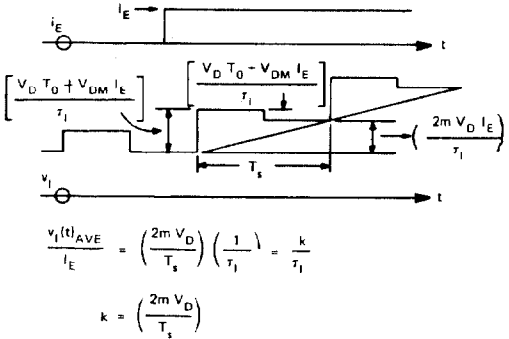


Fig. 2.

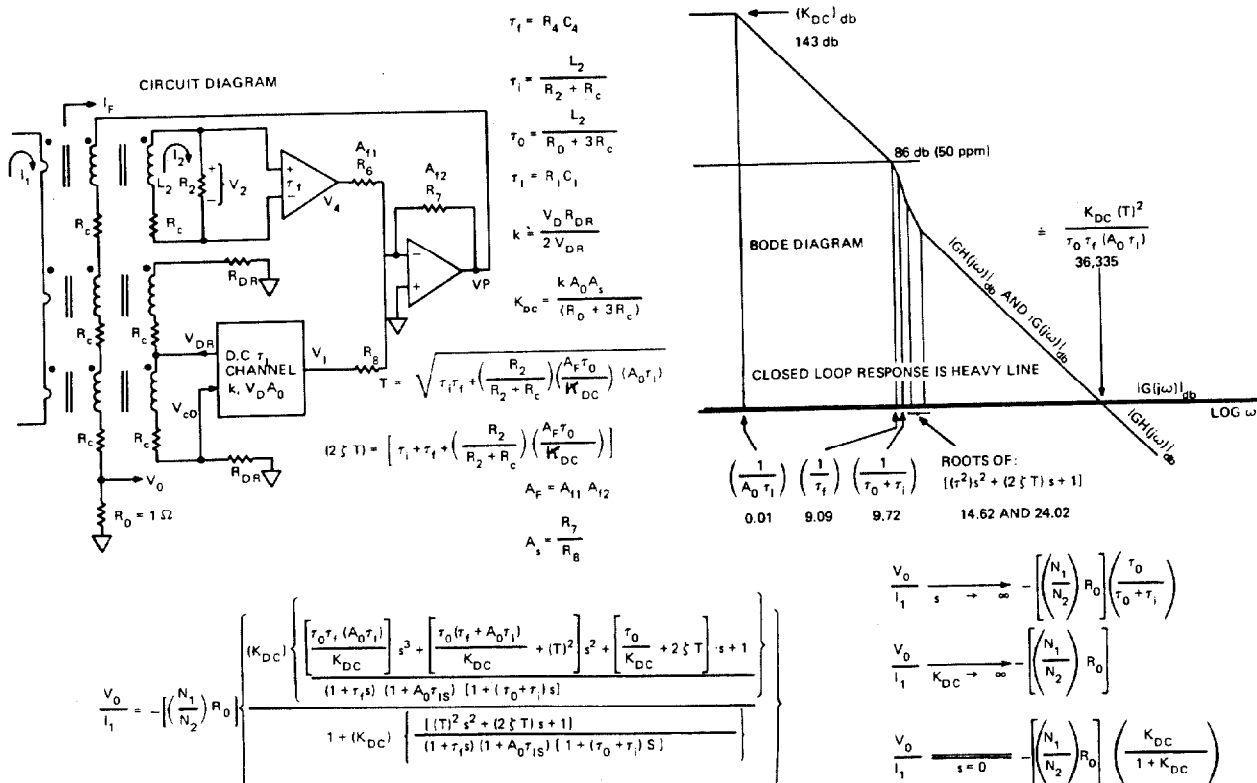


Fig. 3.

The integrator following the demodulator is purposely degraded by R_6 to lower the open loop gain. However like all op-amp integrators it displays a constant gain, R_6/R_1 , below the radian frequency $1/(R_6/R_1)(R_1C_1)$. The integrator converts the bipolar "impulses" from the demodulator to a "square" wave of voltage riding on a dc level proportional to the input current I_1 . The following active low-pass filter allows this dc level to proceed through the dc channel, eventually determining the current I_F that flows through R_0 .

Figure 2 also shows a signal flow graph of the sampled data portion of the dc channel. I_E and T_0 are inputs to this graph with V_I the integrator output. The biasing effect of I_E on the switching time of the toroids is indicated by m (sA^{-1}). V_D is the limiting voltage of the demodulator. The figure also shows the idealized time domain output of the integrator with a step of I_E applied. From this diagram it is easy to characterize the sampled data portion of the dc channel with a single parameter $k(\Omega)$ for all frequencies, an octave or lower, below the switching frequency $1/T_s$.

It remains to relate m and k to other circuit parameters. For this a simple flux-biased hysteresis model is used and shown in Fig. 2. The significant approximation here is that T_s is four times the toroid saturation time t_s with $I_E = 0$. This is justified because I_E is the net error current and therefore very small. The Bode plot is shown in Fig. 2. Using the criterion of no ringing in the output of this third-order system, the gain K_{DC} is adjusted so that two of the closed loop poles merge on the negative real axis.

To have current monitor with 50-ppm resolution at dc and still have a wide bandwidth, the ac and dc

channels must be combined. The ac channel described above would continually drift toward flux saturation because of small offsets in its channel. The dc channel has short-interval large-amplitude switching spikes proportional to I_E in its output signal. A combination of the two channels eliminates both these catastrophes. The ac channel detects the spikes as an error signal and also serves as the active filter. The dc channel senses the drift as an error signal. Thus the two channels combined are a perfect marriage.

Figure 3 shows a summing amplifier as the method of combination. There are now three toroids. A standard, nondegraded integrator is used to achieve maximum open loop gain at dc. All of the design criterion of the individual channels remain in effect. The system transfer function is indicated along with Bode plot. Several different Bode plots are possible with this system, but large open loop gain at dc, large R_2 , and no ringing in the output lead to the one shown.

In conclusion, a very accurate wide-band current monitor is achieved by the combination of an ac and a dc channel with toroidal transformers. The ac channel and transformers handle the fast components of the input current while the dc channel eliminates the drift and yields a precise reading at dc.

References

1. K. Unser, "Beam Current Transformer with dc to 200-MHz Range," IEEE Trans. Nucl. Sci., NS-16, 9340938.
2. M Groenenboom and J. Lissers, "Accurate Measurement of dc and ac by Transformer," Electronics and Power, January 1977, 52-55.
3. J. Smith, "Modern Operational Circuit Design," Wiley-Interscience, 1971.