

DESIGN OF AN ACCURATE LLRF SYSTEM FOR AN ARRAY OF TWO-GAP RESONATORS

D. Liakin[†], S. Barabin, A. Orlov, T. Kulevoy, NRC KI ITEP, Moskva, Russian Federation

Abstract

A particle accelerator based on an array of two-gap resonators requires a control system, which is responsible for precise setup and stabilization of the phase and magnitude of the electromagnetic field in resonators. We develop a cost-effective LLRF system for the array of more than 80 resonators and three different operating frequencies. The design is based on proved solution used for 5-resonators accelerator HILAC (project NICA, Dubna). This paper gives an overview of the basic structure and some specific features of the developing LLRF control system.

STRUCTURE OF THE LLRF

The low-level radio-frequency control system is a part of more general system controlling the whole linear accelerator as it is shown in Fig. 1. Components of the system are distributed over the large area along the accelerating structure and inside of service rooms. The communication network based on Ethernet technology unites the modules into single fast and stable system. Some control tasks are encapsulated into subsystems supported by virtual subnet networking. The accelerating section is built as a row of similar two-gap resonators (see Fig. 2)

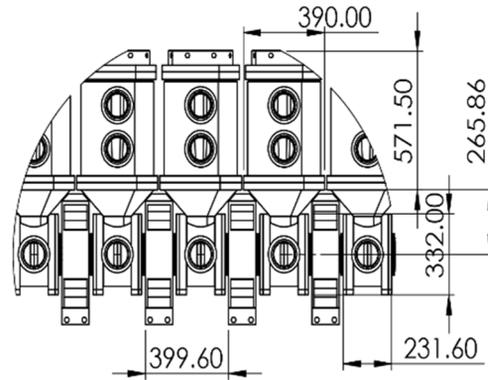


Figure 2: Two-gap resonators in a row.

The structure of the accelerator also determines the structure of the system for monitoring and stabilizing high-frequency fields in resonators. Such a structure is shown in Fig. 3. The carefully synchronized multi-channel reference generator REF is mounted in a single housing and installed in the radio equipment room together with powerful high-frequency amplifiers A. Control measurements are the responsibility of many distributed control unit modules (CU) that serve each accelerating resonator separately.

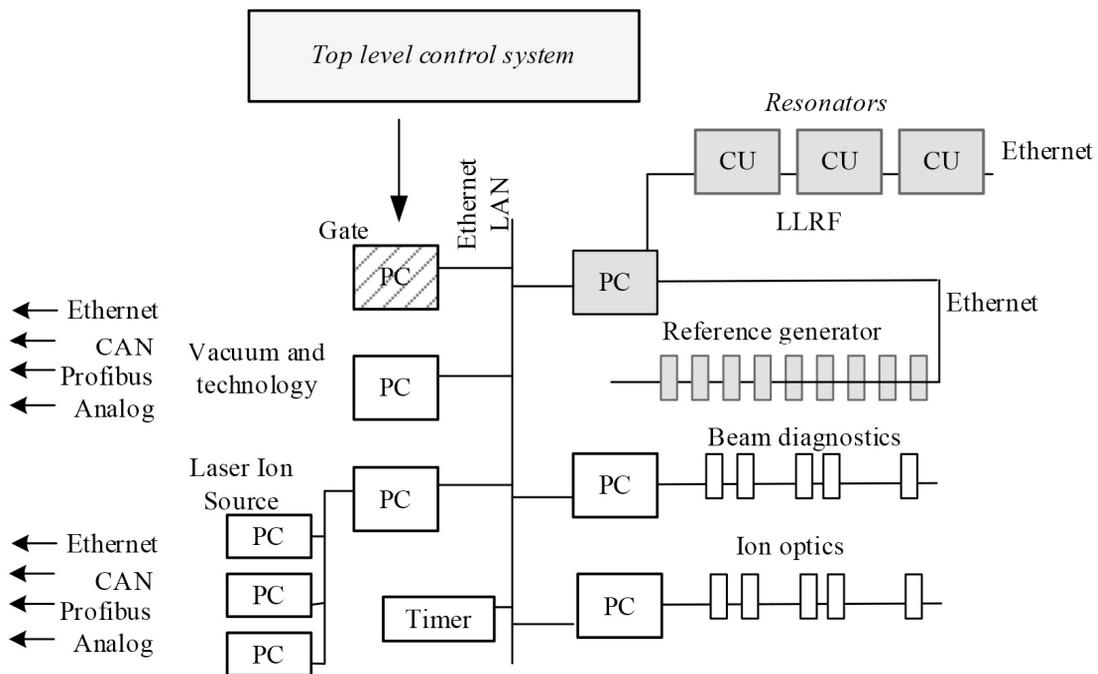


Figure 1: The general structure of the linac control system.

[†] liakin@itep.ru

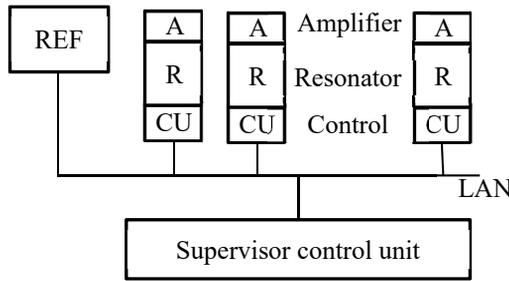


Figure 3: The general structure of the LLRF.

The LLRF system of the linac deal with resonators operating on different frequencies. There are 40.5, 81 and 162 MHz resonators in use. A large number of similar resonators encourages the development of a small standard circuit, which is located in the near of the controlled cavity. Such a device closes the local control loop, covering the measuring and mechanical parts of the automatic frequency control system.

THE RESONATOR CONTROL UNIT

Figure 4 shows the general signal structure of the local control unit of an accelerating resonator. The unit mainly consists of a multichannel analog-to-digital converter with an FPGA- based digital signal processor, and this is combined to another multicore processor for general purposes use.

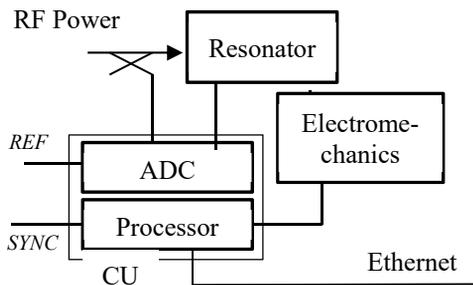


Figure 4: A control unit of a resonator.

The controller involves three signals to see the input RF power level and the resonator frequency mismatching. It also performs full control over the mechanics of the resonator tuner. Nevertheless, the measured data is transmitted to supervisor unit which is also responsible for issuing of the movement commands.

It is also important to keep exact phase relation between resonators in the long resonators array. One of the functions of the control unit is a precise control of the phase advance between electromagnetic field in resonator from one hand and a stable dedicated reference signal from another.

Signals from the reflectometer are received without rectification. They are numerically converted from sinusoidal to the complex vector form in cartesian or polar coordinates.

Programming model of the control unit is presented in Fig. 5. The DSP implements an agile but straightforward data transformation algorithm using expensive FPGA resources. The memory blocks of the FPGA are filled by decimated RF pulse data in a burst mode. This takes about one millisecond after receiving of the synchronisation pulse. Then the classical ARM processor has enough time to re-write the data into the buffer in the low-cost dynamic memory.

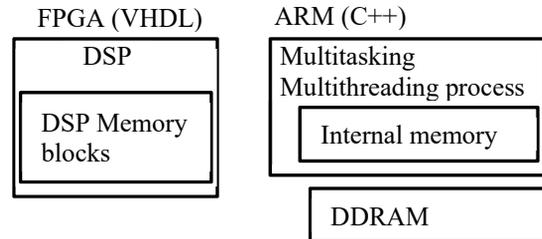


Figure 5: Programming model of the control unit.

THE SIGNAL PROCESSOR

The RF signals are sampled directly by the ADC. The signal strength and spectra allow eliminate analog electronics in front of the ADC. The downstream digital data processing is quite conservative (see Fig. 6). The signal A is shifted in the frequency domain toward the low-frequency region. The mirror component of the signal is suppressed by a low-pass filter. The decimator rarefied the data reducing the data rate down to value which is more adequate to the bandwidth of the input signal. More detailed description of the used algorithms could be found here [1].

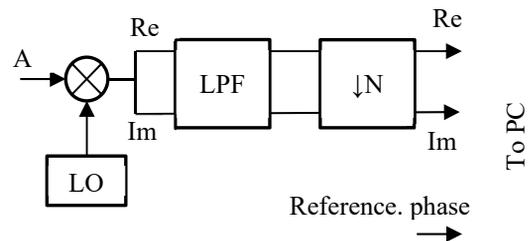


Figure 6: The base algorithm for the digital data processor.

There is a small difference in the processing of the reference signal. The reference signal is constantly present at the measurement input and the digital PLL circuit with the IIR filter makes the most of this feature.

The effective and proven choice for the AD-converter is an 8-channel device with 14-bit resolution. This device operates in undersampling mode reducing the raw data rate and, therefore, does not required expensive interface to the FPGA chip. Eight AD channels option is a good opportunity for improving the cost factor of the control unit. Single module could implement two virtual control units as it is shown in Fig. 7.

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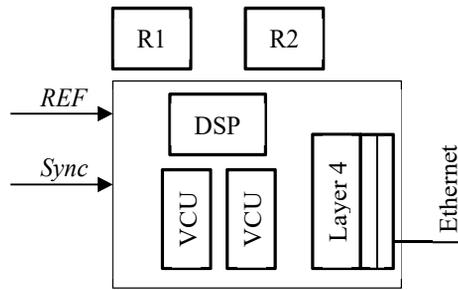


Figure 7: Virtualization of the remote control units.

Such a module serves two resonators. A single signal processor processes data in streaming format for both virtual machines. Virtual units share also synchronization and reference signals as well as resources assigned to the communication functionality. The programming model used in control units and in the supervisor's, software considers such virtual elements as real existing components.

THE REFERENCE PHASE SIGNAL

The reference signal allows us to track the actual phase difference between the resonators. The distribution scheme is shown in Fig. 8.

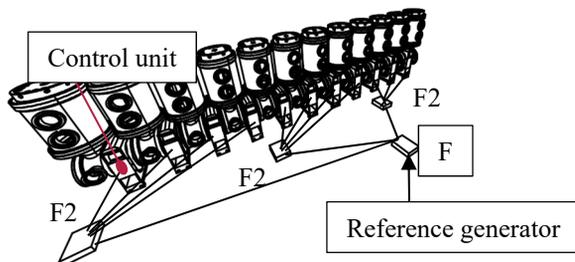


Figure 8: The distribution scheme of the reference phase signal.

The resonator's array is divided to clusters with eight to twelve resonators in cluster. Each resonator in the cluster is supplied by the common phase signal using a precise, low-jitter and stable delay fan-out scheme F. Clusters are connected to the common fan-out module by mean cables with equal length. The common module is located in the middle part of the accelerator. Accelerating resonators use three different frequencies. The reference signal has a lowest frequency in the system.

THE REFERENCE GENERATOR

Accurate multichannel signal generator is important for the LLRF system. As it is shown in Fig. 3 each resonator supplied by separate RF power amplifier. Amplifiers operate in linear A or AB mode and, therefore, the output phase and amplitude of amplifiers may be precisely controlled by the phase and magnitude of the low-level reference signal.

The multichannel reference signal generator is built based on Direct Digital Synthesizer (DDS) devices. The

accuracy and resolution of the output signal are provided by 14-bit giga sample per second DAC. The phase resolution is better than 16 bits depending on selected operational mode.

A beam load effect compensation is performed as an arbitrary phase and amplitude modulation as it is shown in Fig. 9.

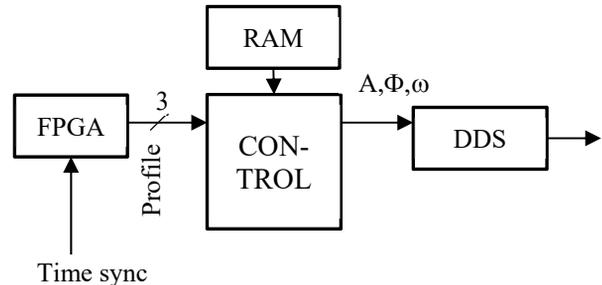


Figure 9: It is envisaged that each channel of the reference generator has means to compensate of the beam loading effect.

The compensation pattern is stored in the RAM memory and the on-off beam state is switched by dedicated profile pins.

The multichannel generator is implemented as a crate uniting few tens of equal single board modules. Every module is a four-channel sinusoidal signal generator. Generally, the frequency, magnitude and phase are independent at each channel's output. At the synchronized state modules issue coherent signals of three operating frequencies. A common clock signal is used to synchronise all modules with each other's.

CONCLUSION

Proposed low-level RF control system solves general problems such, as frequency generation, stabilisation of the accurate phase and magnitude distributions over multiple resonators, stabilisation of the resonant frequency for each resonator in the system.

The DDS-based multichannel reference generator provides precise phase and magnitude manipulation. The distributed system of control units is a high-quality cost-effective system which corresponds to the distributed nature of the array of accelerating resonators.

REFERENCES

- [1] D. Liakin, S. Barabin, and A. Orlov, "Digital signal processing algorithms for linac low-level RF systems", *Proc. 24th Russian Particle Accelerator Conf. (RuPAC'14)*, Obninsk, Russia, Oct. 2014, paper THPSC33, pp. 392-394.