

#### High-performance reflection-based Serialiser

no interface description language (IDL), instead using reflection based approach

ROUTER PUB Radio REST

Client

flexible

Web

lient protocol

- self-documenting serialisation format and data structures
- integration of mp-units physical units library to prevent scaling or unit errors
- high performance using Unsafe in java and constexpr in c++

Client

1	struct CppDomainObjectExample {	memcpy	-	numeric payload 28x8
2	Annotated <float, thermodynamic_temperature<kelvin="">, "device specific temperature"&gt; temperature</float,>	= 23.2F; OpenCMW		c++
3	Annotated <float, electric_current<ampere="">, "this is the current from"&gt; current</float,>			java
÷.	Annotated <float, energy<electronvolt="">, "SIS18 energy at injection before being captured"&gt; injectionEnerg std:string</float,>			
6	// []	CMW		
7	); )	FlatBuffers		
8	// refl-cpp-based: targeted and becomes obsolete with the next C++ standard	Cap'nProto		
9	ENABLE_REFLECTION_FOR(CppDomainObjectExample, temperature, current, injectionEnergy, notAnnotated)	protobuf		1
1	<pre>public class JavaDomainObjectExample { @NetaInfo(description = "device specific temperature", unit = "K")</pre>	OpenCMW-json		
ŝ.,	public float temperature = 23.2f;	Jsoniter	-	
4	dMetaInfo(description = "this is the current from", unit = "A")	Jackson	-	
5	public float current = 42.f;	fastison		
6	<pre>@MetaInfo(description = "SIS18 energy at injection before being captured", unit = "eV")</pre>	Gson		
7	<pre>public float injectionEmergy = 8.44e6f; public String notAnnotated = "Hello World!";</pre>	Gson		
0	public string notamotated - nello world';		1 10 10	0 1.000
10	)		1 10 10	1,000
				round-trip [us]
СС	de example: c++ and Java domain objects.		serialisation and de-seri ng different serialisers.	alisation of

#### **Open and Lean Development**

Event Store:

e.g. los-level driver aprigr (HQ handler

development as open source on public GitHub accepting PRs



- test suite, CI/CD, code quality tooling to allow high quality contributions
- small code footprint: e.g. serialiser (without tests)
  - Java 8431 LOCs
  - C++ 1344 LOCs

low number of dependencies by leveraging standard library functionality

#### References:

Majordomo Broker ROUTER PUB Radio REST

> [1] A. Krimm and R. Steinhagen, "FAIR Common Specification - Modular Open Common Middle-Ware Library for Equipment- and Beam-Based Control Systems of the FAIR Accelerators," FAIR, Tech. Rep., 2020. Available: https://edms.cern.ch/document/2444348

Cvert Store

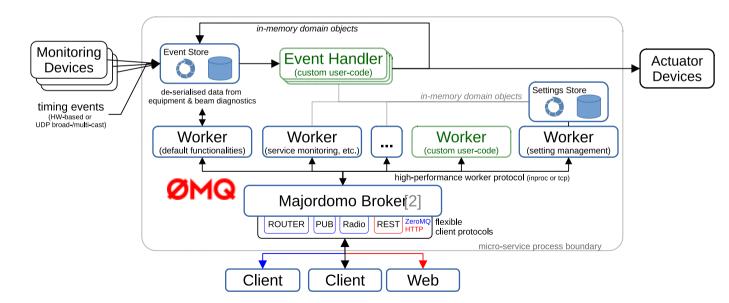
[2] P. Hintjens, "Majordomo Protocol RFC," The ZeroMQ Project, Tech. Rep., 2012, Available: https://rfc.zeroma.org/spec/18/

[3] R. J. Steinhagen et al. "Generic Digitization of Analog Signals at FAIR - First Prototype Results at GSI," in Proc. IPAC'19, Melbourne, Australia, 19-24 May 2019, https://doi.org/10.18429/JACoW-IPAC2019-WEPGW021

[4] M. Pusz. (2020, Jan) P1935R2 - A C++ Approach to Physical Units. [Online]. Available:http://www.openstd.org/itc1/sc22/wg21/docs/papers/2020/p1935r2.html

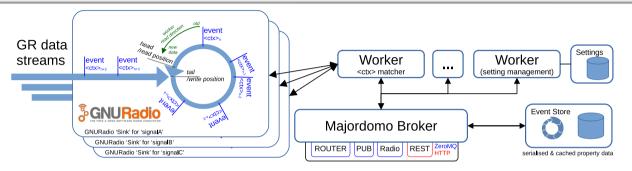
## **Goals and Architecture**

- FAIR ES S
- multi language modular event-driven microservice middle-ware framework based on modern language standards [1]
- backwards compatibility through implementations of established protocols

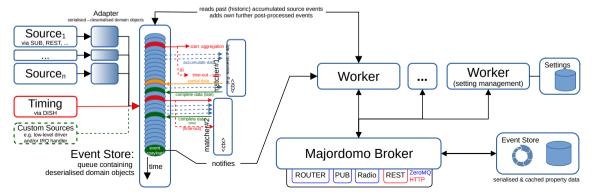




## **Data Aggregation and Event Processing**



Aggregation and processing of data is performed either using GNURadio based continuous streams[3] or event based processing and aggregation. The majordomo framework facilitates event processing using predefined or custom workers defined by handlercallbacks and input/output domain-objects.



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code example: c++ and Java domain objects.

benchmark results: serialisation and de-serialisation of domain-objects using different serialisers.

# **Open and Lean Development**



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[2] P. Hintjens, "Majordomo Protocol RFC," The ZeroMQ Project, Tech. Rep., 2012. Available: https://rfc.zeromq.org/spec/18/

[3] R. J. Steinhagen, R. Bär, A. Franke, A. Krimm, K. Lüghausen, D. Ondreka, A. Schwinn, and M. Thieme, "Generic Digitization of Analog Signals at FAIR – First Prototype Results at GSI," in Proc. IPAC'19, Melbourne, Australia, 19-24 May 2019, https://doi.org/10.18429/JACoW-IPAC2019-WEPGW021

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