

**PROJECT ON PC BASED ACCELERATOR CONTROL KERNEL
(COACK-II)**

I. Abe, KEK High Energy Accelerator Research Org; J.-I. Kishiro, KEK High Energy Accelerator Research Org; K. Nigorikawa, KEK High Energy Accelerator Research Org; T. Kosuge, KEK High Energy Accelerator Research Org; A. Shirakawa, KEK High Energy Accelerator Research Org; M. Mutoh, KEK High Energy Accelerator Research Org; M. Kitamura, KEK High Energy Accelerator Research Org

A PC system has been applied partially in each layer of the Top (GUI),middle, subordinate position, and the device layers. In fiscal year 1999,further enhancement of the device controller PCs in the device layer isscheduled. Through enhancements and accelerator domain analysis on therequirements and usage of various accelerator control kernel systems, it hasbeen found that the current KEK Linac PC based system (COACK) is ideallysuited to any PC based system. COACK (Component-ware Oriented PC basedAccelerator Control Kernel) is brushing up in KEK Linac. The development of COACK phase-II and the realization of this research willbe presented in this paper. This project has received support from JST(Japan Science & Technology Corporation) foundation.