

A HIGH PRECISION PULSE WIDTH MODULATOR SOURCE*

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A novel high-resolution pulse width modulator (PWM) is being developed for a new digital regulator for the APS power converters. The circuit features 130-ps settability over an 80- μ s range. This is equivalent to better than 19 bits. The circuit is presently implemented as a VME module and is an integral part of the digital regulator prototype. The design concept and performance results will be presented.

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