HIGH POWER SWITCH-MODE POWER CONVERTERS FOR THE 1.4 GEV PS-BOOSTER BEAM TRANSFER LINES AT CERN

S.Dewan (DPS), R.Holmes (IE), K.Reiniger (TRIUMF), F.Voelker (CERN), J.Zeng (UOT)

Abstract

For preparing the proton injector chain for the Large-Hadron-Collider (LHC) at CERN, the PS-Booster has been upgraded from 1 to 1.4 GeV. New switch-mode, zero current soft switching (ZCS) power converters, with DC output ratings ranging between 15 and 250 kW (550V and 500A) have been developed for the 1.4 GeV beam transfer line magnets. The ZCS technique is the preferred approach for the high power switch-mode converters employing Insulated Gate Bipolar Transistors (IGBT) to minimize switching losses and EMI. The switch mode power converters have been selected because of the high dynamic response, low output ripple, and low input current harmonics. In this paper, the circuit topology, function of system components, key system specifications and experimental results for a 100 kW switch mode converter are described in detail. The design and development process employed is based on virtual electrical simulation of the system. This technique has been essential for the successful development of this unit.

1 INTRODUCTION

This paper deals with the description, specification, experimental results and the development process of 100 kW switch mode converters for DC and pulse magnet power supplies. In section 2, the basic converter topology including control philosophy is described. The key specifications and experimental results are outlined in section 3. The performance of the switch mode converter has been compared with phase control converter in section 4. The simulation/design/ development process is discussed in section 5.

2 BASIC CONVERTER SYSTEM

Figure 1 shows the essential elements of the 100 kW switch mode converter system.

- EMI filter (EMI1) reduces conducted EMI to input source.
- Reactors (Ls, Lp) and capacitor (Cp) provide required reduction of the input harmonic currents.
- Diodes (D1 to D6) convert three-phase AC voltage to DC voltage.
- Two stage LC filters (Lf1, Lf3, Cf1, Cf3), with a corner frequency of approximately 40 Hz, reduce the line harmonic voltage from the converter output.

- Damping capacitor (Cf2) and resistor (Rd2) reduce DC voltage change during the step change in the input source voltage.
- High frequency filter (Lhf, Chf) ensures that switching frequency currents generated by the inverter do not flow back to the line.
- IGBTs (S1 to S4) are connected in a full bridge configuration and switched at 18 kHz to convert the DC to high frequency (18 kHz) AC.
- 18 kHz high frequency transformer (TR1) provides the (isolated) desired output voltage.
- High frequency diodes (Do1, Do2) convert the 18 kHz AC voltage to DC voltage.
- Free wheeling diode (DF) provides a path for the current when Do1 and Do2 are not conducting.
- Two stage output filters (Lo1, Co1, Lo2, Co2), with a corner frequency about 2 kHz, reduce the switching frequency voltage ripple in the output.
- Output damping capacitor (Co3) and resistor (Ro3) improve output transient performance for the pulsing applications.
- The control strategy employs an outer magnet current (I_p) loop with an inner unfiltered voltage (V_r) loop. The inner voltage loop has also another inner primary current (I_p) loop. This I_p loop ensures that transformer TR1 does not saturate due to transients and asymmetry in switching properties of IGBT switches.
- Parameters for 100 kW switch mode converter in figure1 are:

Vab=400V, Vo=200V, Io=500A; Ls=475 μ H, Lp=700 μ H, Cp=192 μ F; Lf1=Lf3=1.2 mH, Cf1=Cf3=12,000 μ F; Cf2=36,000 μ F, Rd2=0.365 Ω ; Lhf=10 μ H, Chf=40 μ F; Turn Ratio of TR1 N=0.5; Lo1=Lo2=100 μ H, Co1=100 μ F, Co2=60 μ F; Co3=500 μ F, Ro3=0.28 Ω .

3 KEY SYSTEM SPECIFICATIONS AND EXPERIMENTAL RESULTS

This section identifies the significant performance requirements/results for input and output of the 100 kW switch mode converter. Both simulation and experiments have verified these results.

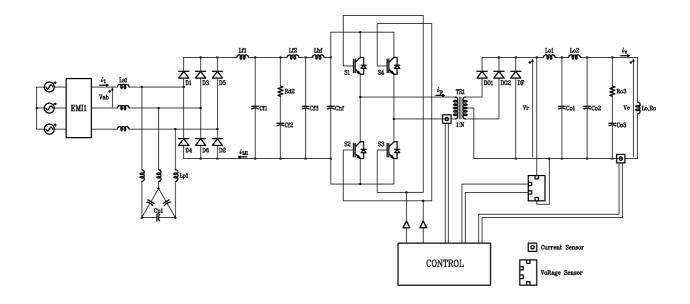


Figure 1: Basic 100 kW Switch Mode Converter System (ZCS)

3.1 Input

| Voltage RMS | | 400V±10% | |
|-----------------------------|-------------------|----------|--|
| Current RMS | | 164A | |
| Current Harmonic Distortion | | <8A(5%) | |
| Efficiency at Full Load | | > 90% | |
| Efficiency at 50% Load | | > 85% | |
| EMI | EN-5501/Gr-1CI-A, | | |
| | VDE-08715 Grade N | | |

Input line current at rated power is shown in figure 2. Also, EMI results are presented in table 1.

3.2 Output

| DC Voltage, Continuous | 0 to 200V |
|-------------------------|--------------------------|
| DC Current, Continuous | 0 to 500A |
| Unfiltered Voltage Loop | |
| Bandwidth | >2,000Hz |
| Closed Current Loop | |
| Bandwidth | > 300 Hz |
| Low Frequency Output | |
| Voltage Ripple up to 1 | $< 20 \text{ mV}_{P-P}$ |
| kHz | |
| High Frequency Output | |
| Voltage Ripple greater | $< 200 \text{ mV}_{P-P}$ |
| than 1 kHz | |

Figures 3 and 4 show the worst case low frequency and high frequency output voltage ripple, respectively.

Table 1: Worst Case EMI Test Data of 100kW Switch

| Mode Converter | | | | | | | | |
|----------------|--------|------------|--------|--------|---------------|--|--|--|
| | RF | Receiver | QP | AVG | | | | |
| Freq | Level | Detector | Limit | Limit | Margin | | | |
| (MHz) | (dBuV) | (P/QP/AVG) | (dBuV) | (dBuV) | (dB) | | | |
| 0.16 | 54.9 | QP | 79.0 | 66.0 | -24.1 | | | |
| 0.16 | 54.7 | AVG | 79.0 | 66.0 | -11.3 | | | |
| 0.19 | 54.3 | QP | 79.0 | 66.0 | -24.7 | | | |
| 0.19 | 54.3 | AVG | 79.0 | 66.0 | -11.7 | | | |
| 0.23 | 55.5 | QP | 79.0 | 66.0 | -23.5 | | | |
| 0.23 | 55.4 | AVG | 79.0 | 66.0 | -10.6 | | | |
| 0.26 | 56.3 | QP | 79.0 | 66.0 | -22.7 | | | |
| 0.26 | 56.2 | AVG | 79.0 | 66.0 | -9.80 | | | |
| 0.30 | 51.8 | QP | 79.0 | 66.0 | -27.2 | | | |
| 0.30 | 51.8 | AVG | 79.0 | 66.0 | -14.2 | | | |
| Note: | | | | | | | | |

Note:

The emissions were scanned from 150 kHz to 30 MHz at AC mains via a LISN, and all emissions less than 30 dB below the limits were recorded.

4 COMPARISON OF PHASE CONTROL AND SWITCH MODE

With new developments in IGBT technology, system modelling and EMI filter technologies, switch mode has became a viable option for magnet power supplies for both DC and pulsed applications. When compared with phase control, the advantages of the switch mode supplies are;

- At least 10 times greater voltage and current loop bandwidths
- Lower output voltage ripple especially with pulse application
- Lower output current ripple

- Better tracking performance in pulse applications
- Better settling performance in time and accuracy in pulse applications
- High input power factor and negligible input current harmonics
- Much lower audible noise
- Less physical space for similar requirement

The main disadvantage of the switch mode converters i higher losses (approximately 50% higher due to thre stages of conversion) when compared with 12-pulse phase control design.

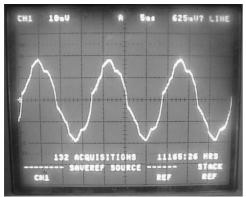


Figure 2 Input Line Current (i₁) at output of 200V, 500A (100A/div, 5ms/div)

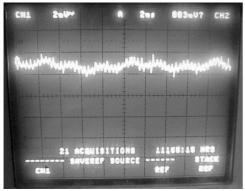


Figure 3 Low Frequency Output Voltage (v_o) Ripple (20 mV/div, 2ms/div)

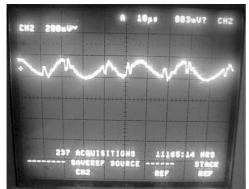


Figure 4 High Frequency Output Voltage (v_{o}) Ripple (200 mV/div, 10 $\mu s/div$)

4 DESIGN/DEVELOPMENT PROCESS

During the process of development of the 100 kW power supply, electrical simulation employed extensively from beginning of the development. A commercial software package was used as the simulation engine. This software package was enhanced by developing an interactive design software package, which accomplished set the following tasks:

- Processed the data from simulation
- Identified optimum power circuit and control configurations
- Verified the transient and steady state system input and output parameters
- Established the major component specifications, power losses and EMI filter requirements
- Verified essentially all experimental results

This virtual electrical prototyping by simulation has been extremely useful in the reduction/elimination of errors early in design. Essentially no component was ordered till the virtual prototyping process was completed. The simulation results for all the relevant performance, including system losses and EMI were close to the experimental results. We believe that this design process also provide the following;

- Increases product reliability
- Reduces design cost
- Maintains control of project schedule
- Allows feasibility/cost evaluation of other ratings of similar design

The major difficulty in the development of the 100 kW power converters was that, for some components, suppliers did not meet the required specifications

5 CONCLUSIONS

This paper has discussed the salient features of a 100 kW switch mode power converter. This converter was designed and tested using a virtual prototyping simulation process. The performance of this power converter is significantly better than the phase control design especially for pulsing applications. A 250 kW power converter is currently being developed and the results will be reported in a subsequent paper.

For any questions, please send emails to Shashi Dewan (sdewan@inverpower.com) or Bob Holmes (iepower@iepower.com) or Klaus Reiniger (Reiniger@triumf.ca) or Fredrich Voelker (Friedrich.Voelker@cern.ch) or Jiaqi Zeng (j.zeng@utoronto.ca)