

ROBUST HIGH-AVERAGE-POWER MODULATOR*

I. Roth, N. Butler, M. Gaudreau, M. Kempkes, Diversified Technologies, Inc., 35 Wiggins Ave, Bedford, MA 01730, USA

Abstract

Diversified Technologies Inc. (DTI) is developing a long-pulse modulator which meets the requirements of the Spallation Neutron Source (SNS) at Oak Ridge National Laboratory and the European Spallation Source (ESS). This modulator design will deliver pulses at up to 100 kV and 50 A with a pulse width of 3.5 ms and a droop much less than 1%. The initial modulator will be delivered to SNS for conditioning klystron tubes.

INTRODUCTION

Diversified Technologies, Inc. (DTI) is developing this new long-pulse modulator under DOE SBIR grant. This modulator has two objectives: to condition klystron tubes at the Spallation Neutron Source (SNS), and to demonstrate the modulator specifications needed for the European Spallation Source (ESS) (Table 1). The key technical problem addressed is that the millisecond-long pulses produce a droop voltage of about 10% with a reasonably-sized capacitor bank—much larger than the 1% droop required. To eliminate the droop without a large and expensive capacitor bank, the modulator has a non-dissipative regulator, which provides a flat pulse at essentially zero additional power.

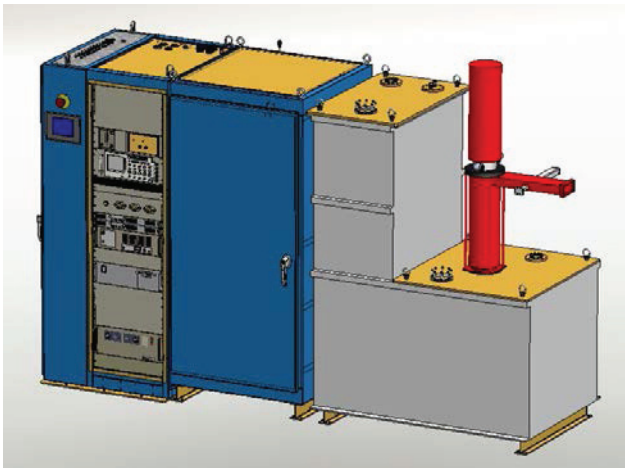


Figure 1: Mechanical layout of the system. The components are (from left to right) blue cabinet for PLC and AC power, gray rack-mount cabinet with power supplies and control boxes, power-conditioning cabinet (which contains the capacitor bank, switch, and regulator), and oil-filled transformer tank, which also contains the RF tube (red) and the heater transformer.

Table 1: SNS and ESS Modulator Specifications. (Frequency and Ripple are not Significant for Klystron Conditioning.)

	SNS	ESS
Voltage (kV)	85	100
Current (A)	13.8	50
Pulse width (ms)	1.5	3.5
Frequency (Hz)	-	14
Voltage Flatness (%)	1	1
Ripple (VRMS)	-	50

OVERVIEW

DTI’s standard design for this class of modulator would use a 100 kV solid-state switch in a compact oil tank. The ESS program, however, has a strong preference for air-insulated electronics at voltages below ~20 kV. Therefore, we are building a pulse transformer (hybrid) modulator for this application. The mechanical layout of the modulator under development at DTI is shown in Fig. 1, and a simplified schematic diagram is shown in Fig. 2. The major elements of the system described in this paper are the switch, regulator, capacitor, and transformer.

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Switch

The switch is made with series-connected IGBTs, which is a well-established technology at DTI (Fig. 3). A series connection allows redundancy—since IGBTs fail short, if one module shorts, the others will continue to hold off the voltage across the switch. The switch consists of seven modules in series, two of these are redundant.

The IGBTs in the switch stack are monitored, so that in the unlikely event of a failure, repairs can be

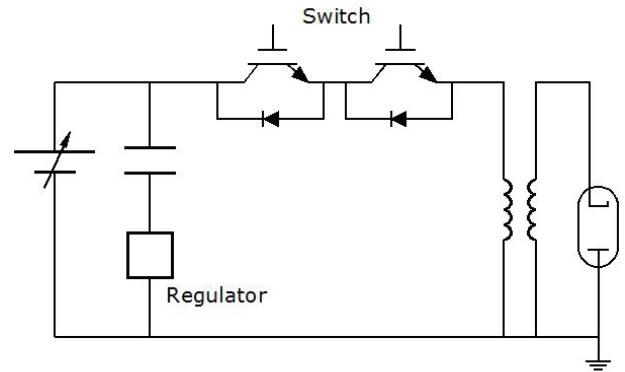


Figure 2: Simplified system diagram. The actual switch is made with seven IGBTs.

appropriately scheduled.

The IGBTs are mounted on a cold plate to keep their temperatures low, giving high reliability.



Figure 3: Standard DTI 3 kV switch module.

Regulator

The regulator is the key development for this program. It keeps the output voltage constant as the capacitor voltage droops. Since the regulator and capacitor also present a constant voltage to the power supply, this allows the system to draw constant power, eliminating flicker as a potential concern. The regulator is non-dissipative, and supplies no average power to the system.

The regulator is based on a full-bridge circuit (Fig. 4). A full bridge has the advantage that it transfers all the energy in the filter inductor to the filter capacitor, rather than returning some of it to the bus capacitor. Because of this, a full bridge can operate at a lower current (with correspondingly lower losses) than a half-bridge.

A key element of the regulator operation is the switching sequence. The switches are operated so that there is no discontinuity in the switch duty during the pulse, keeping the feedback signal continuous. This eliminates any voltage ripple that would have been caused by a mode transition in the middle of the pulse (Fig. 5).

The switches in the bridge operate at 100 kHz during

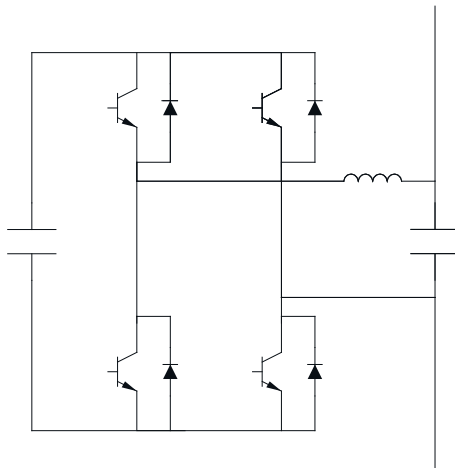


Figure 4: Full bridge circuit.

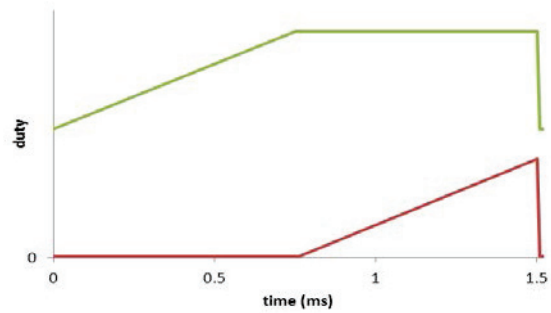


Figure 5: Duty cycle of two switches in the revised regulator control scheme. The switch duty varies smoothly during pulse, so there is no mid-pulse transient voltage.

pulsing, and 5 kHz during charging. The regulator is made with two full bridges in parallel, connected to the same filter capacitor. Their switching is staggered, making an effective switching frequency of 200 kHz during pulsing. With this high-switching frequency, and a filter capacitor that has very low inductance, we calculate a ripple voltage of 40 mV peak-to-peak. (The actual ripple voltage will be somewhat higher than this, limited by electro-magnetic pickup – it will, however, be very small in comparison to the 100 kV output.)

The basic voltage waveforms for the system are shown in Fig. 6. The capacitor voltage (blue) droops during the pulse, then rises during charging between pulses. The regulator (green) aims to complement these changes in voltage to provide a sum load voltage (purple) which is flat during both the pulse and charging. When regulation is precise enough, the capacitor will act as an ideal voltage source.

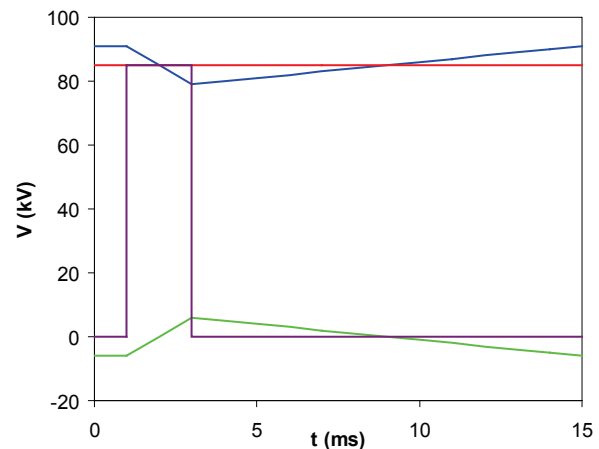


Figure 6: Voltage waveforms for the modulator system. Traces are as follows: Capacitor (blue); Droop Corrector (green); Load (purple); Main power supply (red).

Figure 7 below presents the results of modelling a 3.5 ms, 100 kV pulse. Note that the bottom plot presents the same data as the top plot, but at approximately a 1000x zoom. Voltage regulation is extremely tight.

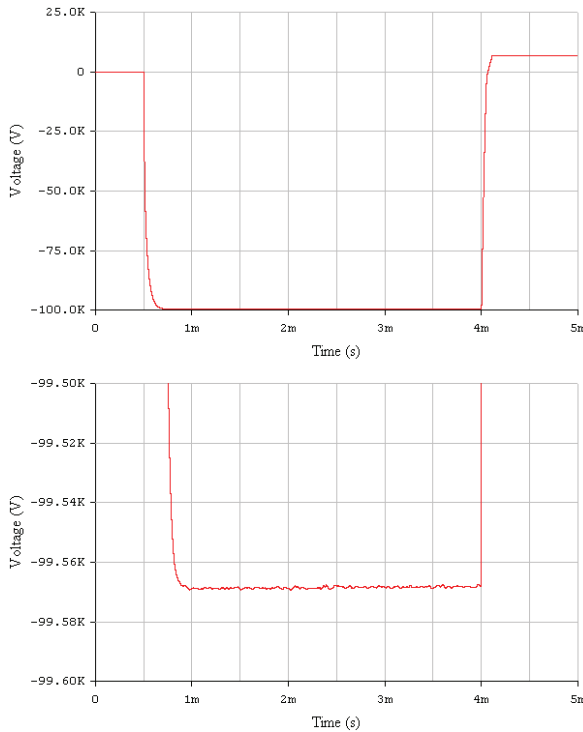


Figure 7: Simulated output voltage of a 3.5 ms pulse. The top graph displays the pulse over the full 100 kV range, while the bottom graph visualizes the ripple by constraining voltage range to the final 100 V.

Capacitor

The capacitors at 6 kV will be made with metallized-film, also referred to as “self-healing”, a technology which is well-established at this voltage. Metallized-film capacitors can tolerate arcs in the film, and so can operate at a relatively-high electric field. This is because when a metallized-film capacitor arcs, the metallization near the arc blows off, isolating the shorted section from the rest of the capacitor. The only impact of this is a small reduction in capacitance. In contrast, a film-foil capacitor cannot tolerate any arcs, and so must operate at a lower electric field.

The total bank capacitance is 4 mF, which stores 82 kJ at a voltage of 6.4 kV (the additional 400 V above 6 kV allows for droop). The high electric field of the metallized-film capacitors gives a high energy density, and so a relatively-small capacitor bank. The bank is being built with three 1333- μ F capacitors with dimensions approximately 14” x 15” x 25” tall. This bank is roughly one-quarter of the volume and cost of a capacitor bank made with film-foil technology.

Transformer

A topology involving a pulse transformer was selected because of a customer belief that frequent servicing is needed for new modulators. Because of this, the customer preferred an air-insulated system to one that is insulated with oil. (We chose a design in accord with this preference, though DTI’s modulators rarely require maintenance, and when it is required, it can be done in only a few hours.) Because air does not insulate as well as oil, the switch voltage was chosen to be low, necessitating a pulse transformer to produce the high-voltage output.

A key issue for the transformer is producing a pulse with a fast rise time to minimize the energy wasted. The rise time is largely determined by the series leakage inductance of the transformer (the leakage inductance), so it is desirable to minimize this inductance. Since the pulse is so slow (3.5 ms) the transformer design will resemble a power transformer rather than the design used for a microsecond pulse.

Future Work

As a reliable, low-cost system, the droop control provided by this modulator for long pulses make it an attractive choice for applications which would otherwise require prohibitively large and expensive capacitor banks. DTI’s future efforts will include exploration of ways to improve the system, including investigation of directly-switched, oil-cooled systems, and extension of this regulator technology to other long-pulse modulator design.