

# Open Hardware Workshop Overview

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BE-CO Hardware and Timing section  
CERN, Geneva, Switzerland

San Francisco, 7 October 2013

# Outline

- 1 Session 1
- 2 Session 2
- 3 Session 3
- 4 Conclusions



# Introduction and experience in the Institutes

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- Soleil's development of a series of SPI-capable boards (controller, motion control, ADC/DAC).
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- OpenPET project in LBNL.

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## But some labs do not enjoy as much backing from their TT groups

- Trend to focus on direct profit only.
- OH is new. Past experience with FOSS helps.

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## Two very good remote talks with live Q&A

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- John Ackermann, father of TAPR OHL, on legal considerations.

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## All audio/video and networking gear worked flawlessly

Many thanks to the Local Organizing Committee and the personnel at the Hyatt!

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- KiCad, a PCB design tool.
- Icarus, an HDL simulator.
- Libre-FDATool, a tool to easily design HDL filters.
- VPCle, a PCIe endpoint virtualization framework.

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## Discussion session

Mainly focused on how to join efforts to finance EDA tool development.

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All slides and some audio/video will be available at  
<http://www.ohwr.org/projects/ohr-meta/wiki/OHW2013>