

Direct (Under) Sampling vs. Analog Downconversion for BPM Electronics

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CERN

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- Introduction
- BPM Signals
 - broadband BPM pickups (button & stripline BPMs)
 - resonant BPM pickups (cavity BPMs)
- BPM Signal Processing
 - Objectives
 - Pure analog BPM example: Heterodyne receiver
 - Analog/digital processing of BPM electrode signals
 - Examples and performance
- Summary

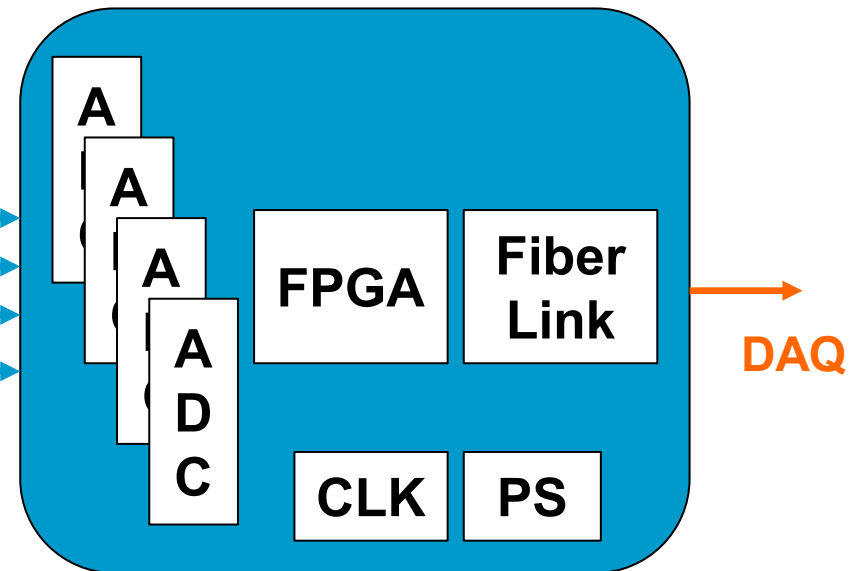
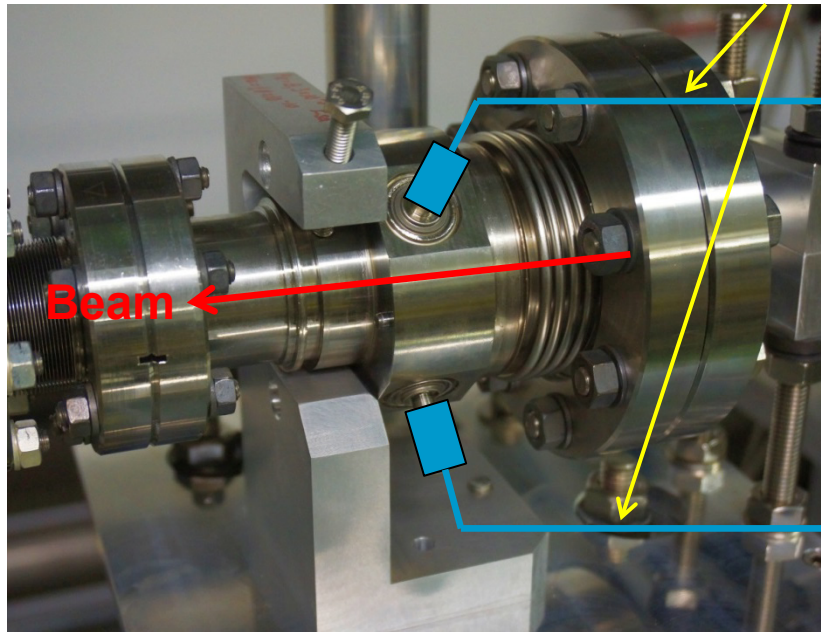
The Ideal BPM Read-out Electronics!?



**BPM pickup
(e.g. button, stripline)**

**Very short
coaxial cables**

**Digital BPM electronics
(rad-hard, of course!)**



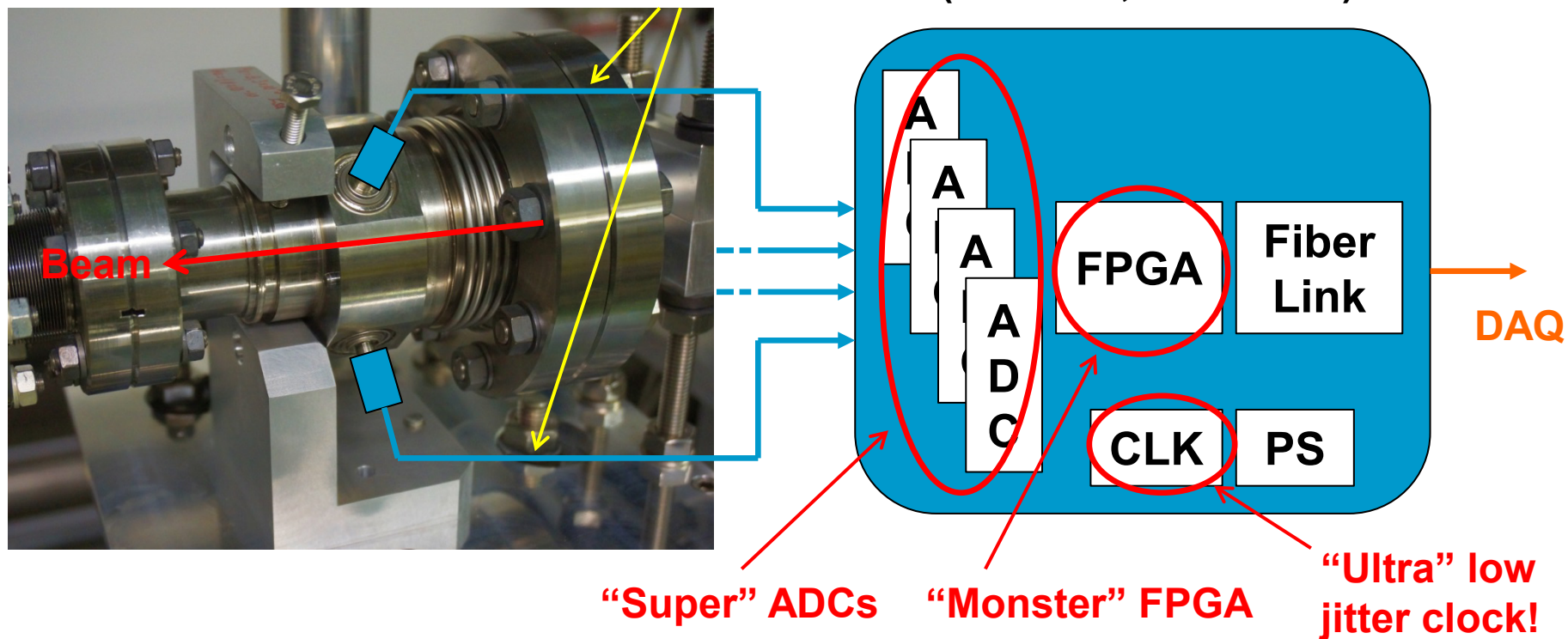
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- Time multiplexing of the BPM electrode signals:
 - Interleaving BPM electrode signals by different cable delays
 - Requires only a single read-out channel!

BPM Pickup

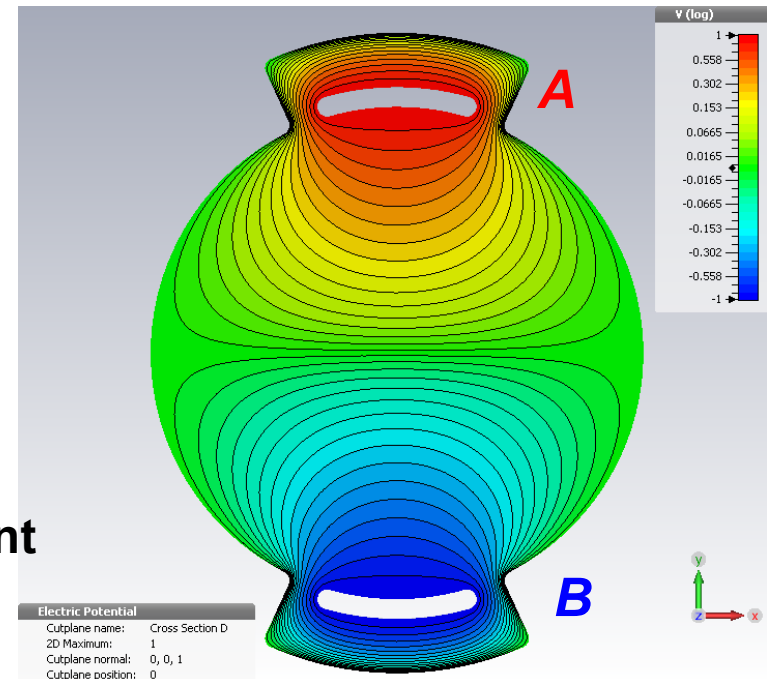
- The BPM pickup detects the beam positions by means of:

- **identifying asymmetries** of the signal amplitudes from symmetrically arranged electrodes *A* & *B*: norm. beam position $\propto \frac{A - B}{A + B}$
broadband pickups, e.g. buttons, striplines
- Detecting dipole-like eigenmodes of a beam excited, passive resonator:
narrowband pickups, e.g. cavity BPM

- BPM electrode transfer impedance:**

$$V_{elec}(x, y, \omega) = s(x, y, \omega) Z(\omega) I_{beam}(\omega)$$

- The beam displacement or sensitivity function $s(x, y)$ is frequency independent for broadband pickups



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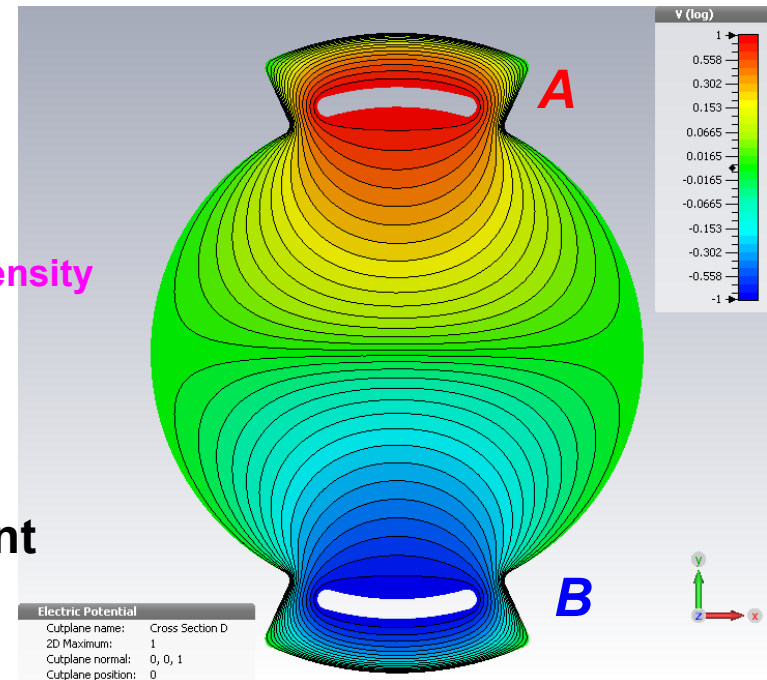
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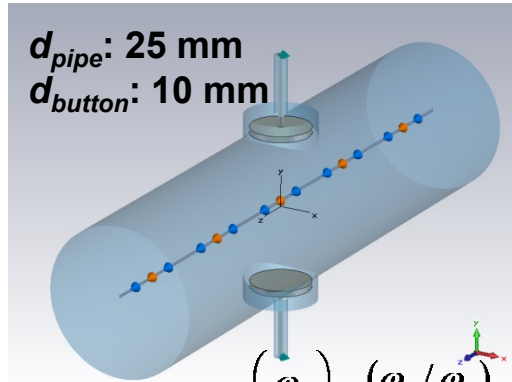
- BPM electrode transfer impedance:

$$V_{elec}(x, y, \omega) = s(\underbrace{x, y}_{\text{beam position}}, \underbrace{\omega}_{\text{only for resonant pickups}}) \underbrace{Z(\omega)}_{\text{Frequency depending coupling impedance of the BPM electrode}} \underbrace{I_{beam}(\omega)}_{\text{beam intensity}}$$

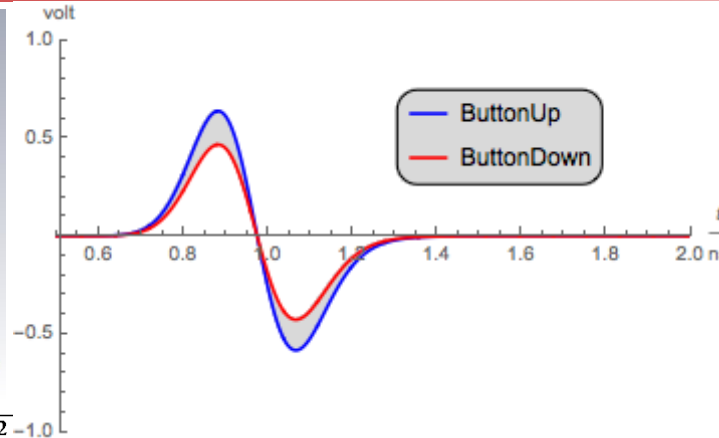
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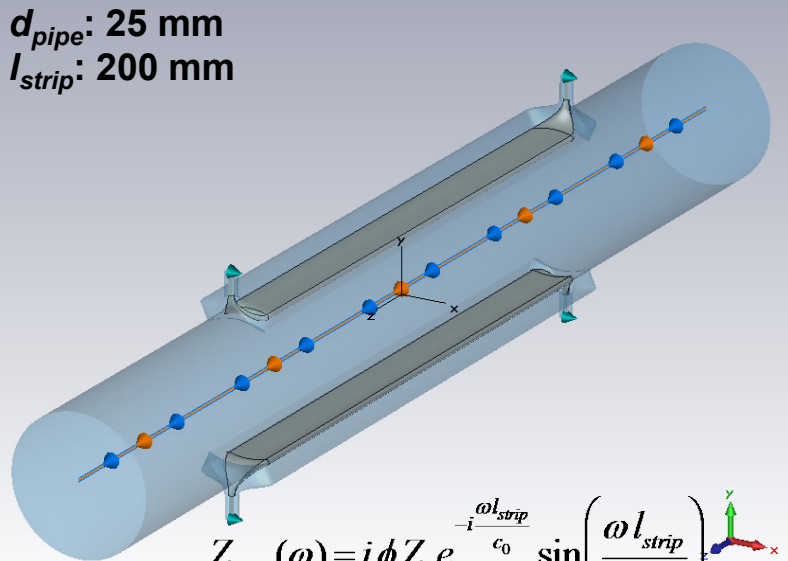
Broadband BPM Signals



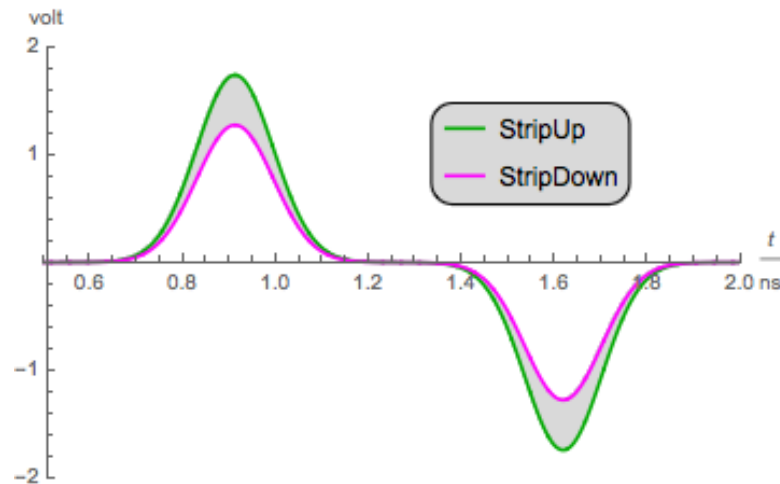
$$Z_{button}(\omega) = \phi R_{load} \left(\frac{\omega_1}{\omega_2} \right) \frac{(\omega_1 / \omega_2)}{1 + (\omega_1 / \omega_2)^2}$$



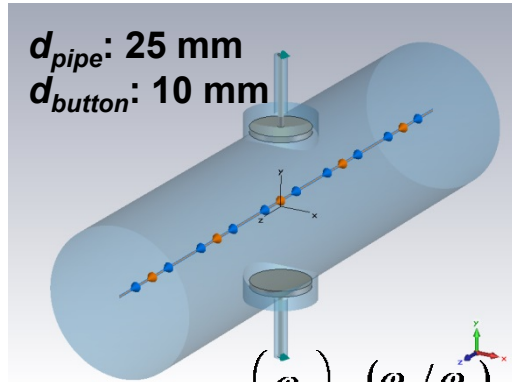
- **Gaussian bunch:**
 - $n=1E10$, $\sigma=25\text{mm}$, $v=c_0$
 - vertical offset=1mm
- **BPM sensitivity:**
 - e.g.: 2.7 dB / mm



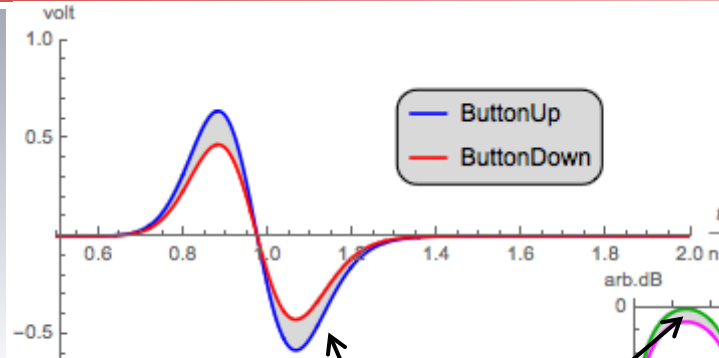
$$Z_{strip}(\omega) = i \phi Z_0 e^{-i \frac{\omega l_{strip}}{c_0}} \sin \left(\frac{\omega l_{strip}}{c_0} \right)$$



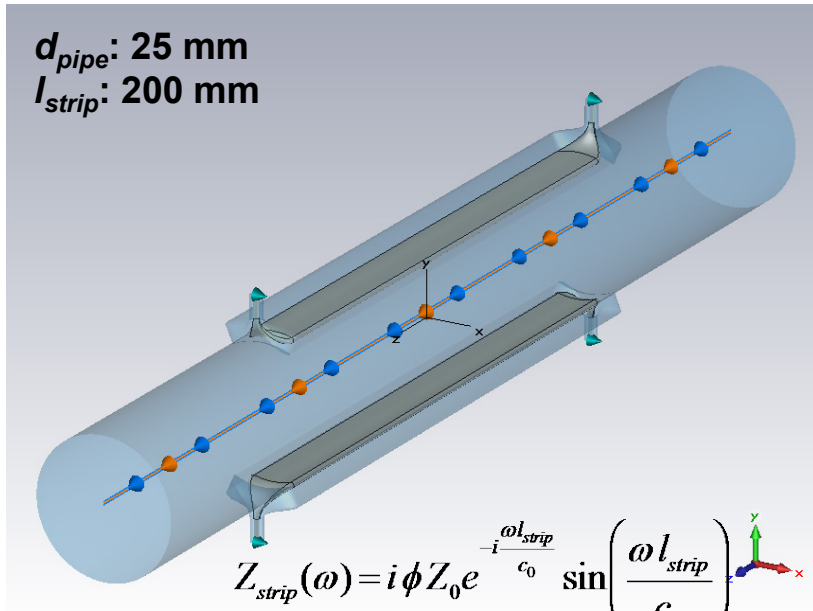
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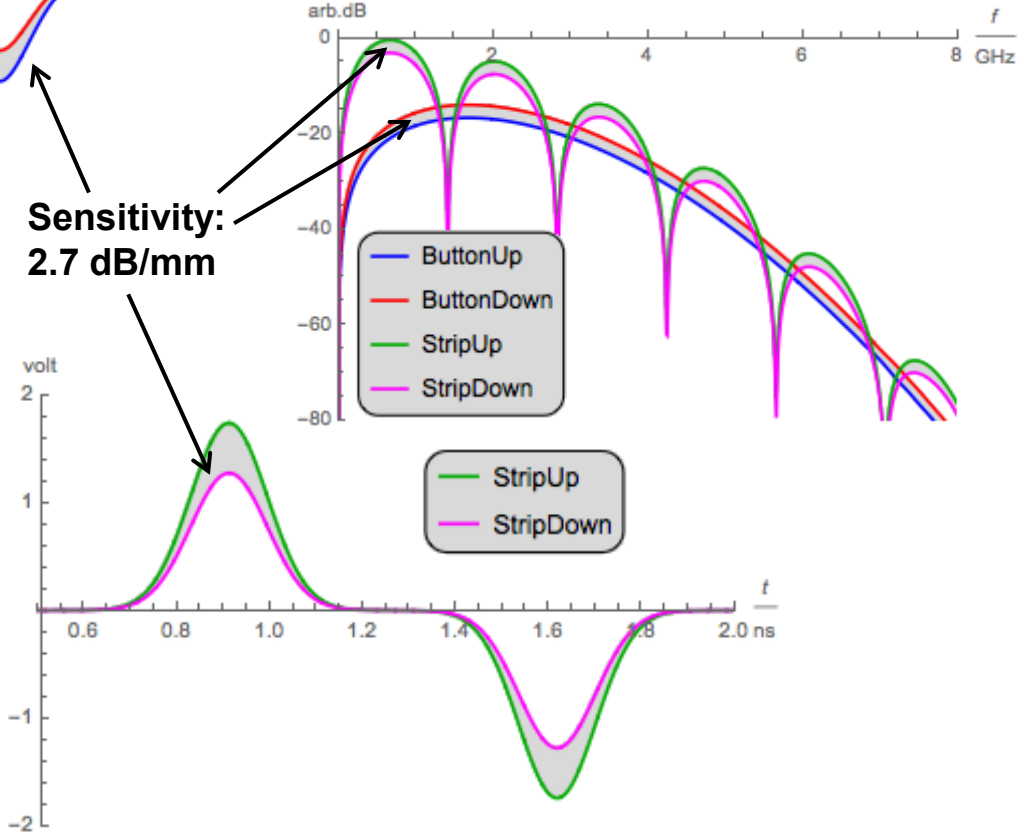


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Sensitivity:
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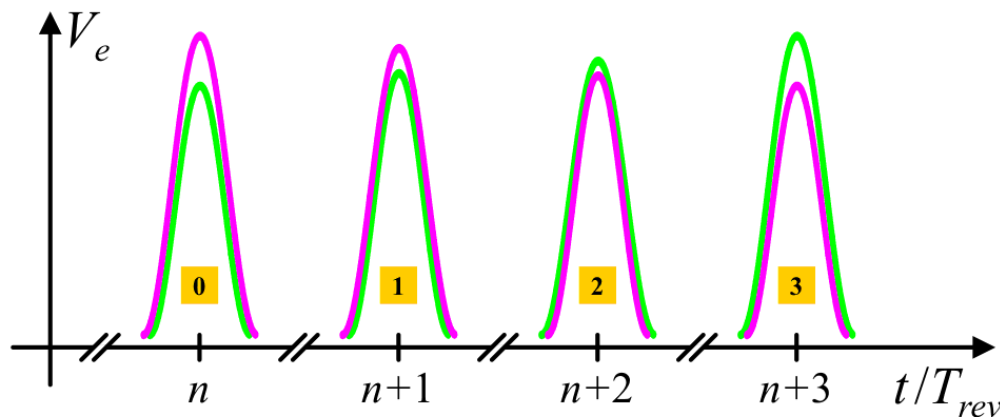


BPM Signals (cont.)

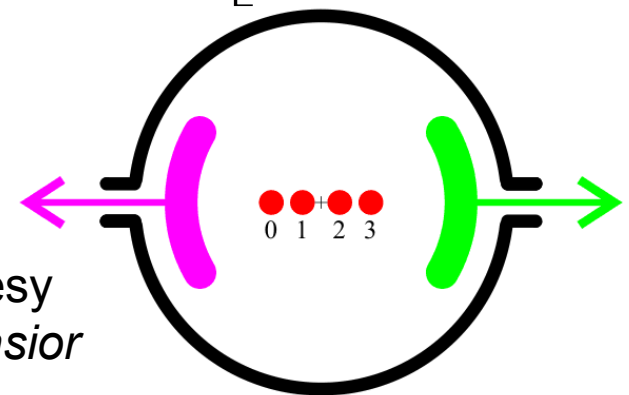
- Raw broadband BPM bunch signals are short in time
 - Single bunch response n sec or sub- n sec pulse signals
 - The beam position information is amplitude modulated (AM) on the large (common mode) beam intensity signal!
- In ring accelerators, the beam position varies on a turn-by-turn basis, the signal spectrum is related to important machine parameters
 - Dipole moment spectrum of a single bunch: (simplistic case)

$$Z(\omega)I_{beam}(\omega) = D(\omega) = \omega_{rev} A_0 Q \sum_{n=-\infty}^{+\infty} \delta\left[\omega - \left(n\omega_{rev} + \underbrace{\omega_{\beta}}_{\text{betatron frequency}}\right)\right] \exp\left[-\frac{\left(\omega - \omega_{\beta} - \omega_{\xi}\right)^2 \sigma_{\tau}^2}{2}\right]$$

courtesy R. Siemann

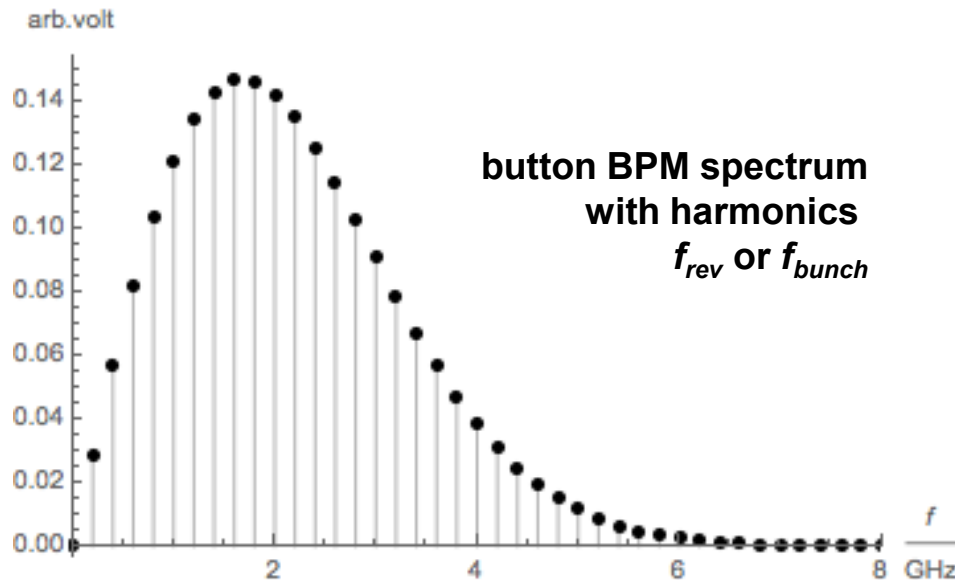


courtesy
M. Gasior



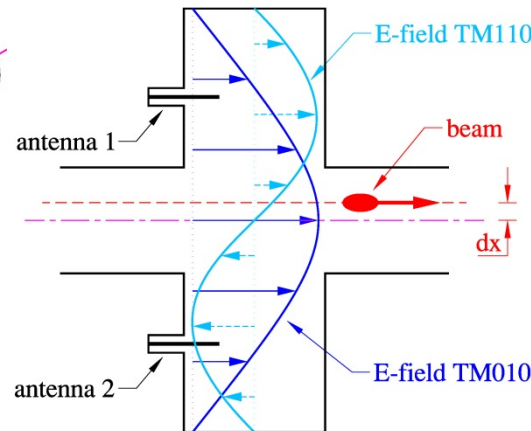
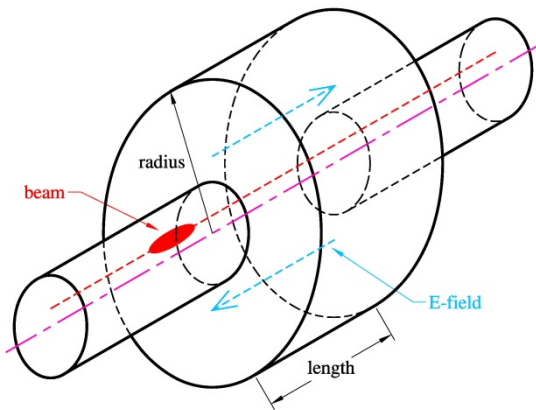
BPM Signals (cont.)

- The beam formatting defines the signal spectrum
 - E.g. f_{rev} , f_{bunch} in circular or linear accelerators



- The position information of broadband BPMs in frequency independent!
 - The broad spectral response of the BPM can be band limited without compromising the position detection!

Resonant BPM Pickups



- “Pill-box” has eigenmodes at:

$$f_{mnp} = \frac{1}{2\pi\sqrt{\mu_0\epsilon_0}} \sqrt{\left(\frac{j_{mn}}{R}\right)^2 + \left(\frac{p\pi}{l}\right)^2}$$

- Beam couples to:

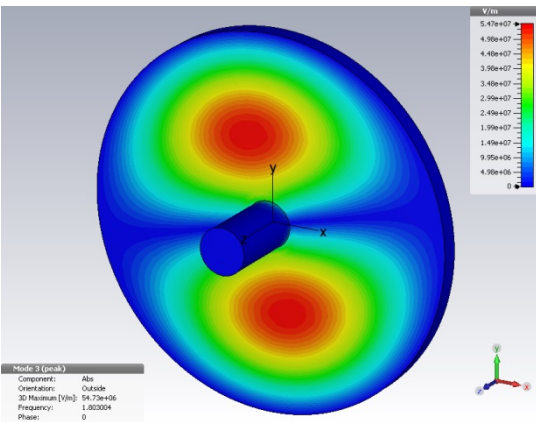
$$E_z = C J_1\left(\frac{j_{11}r}{R}\right) e^{i\omega t} \cos\varphi$$

dipole (TM_{110}) and
monopole (TM_{010})
& other modes

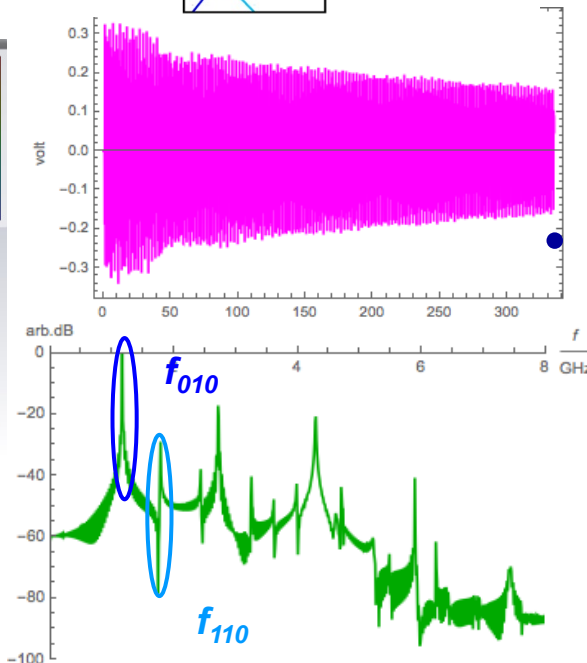
Common mode (TM_{010})
frequency discrimination

Decaying RF signal response

- Position signal: TM_{110}
 - Requires normalization
- Intensity signal: TM_{010}



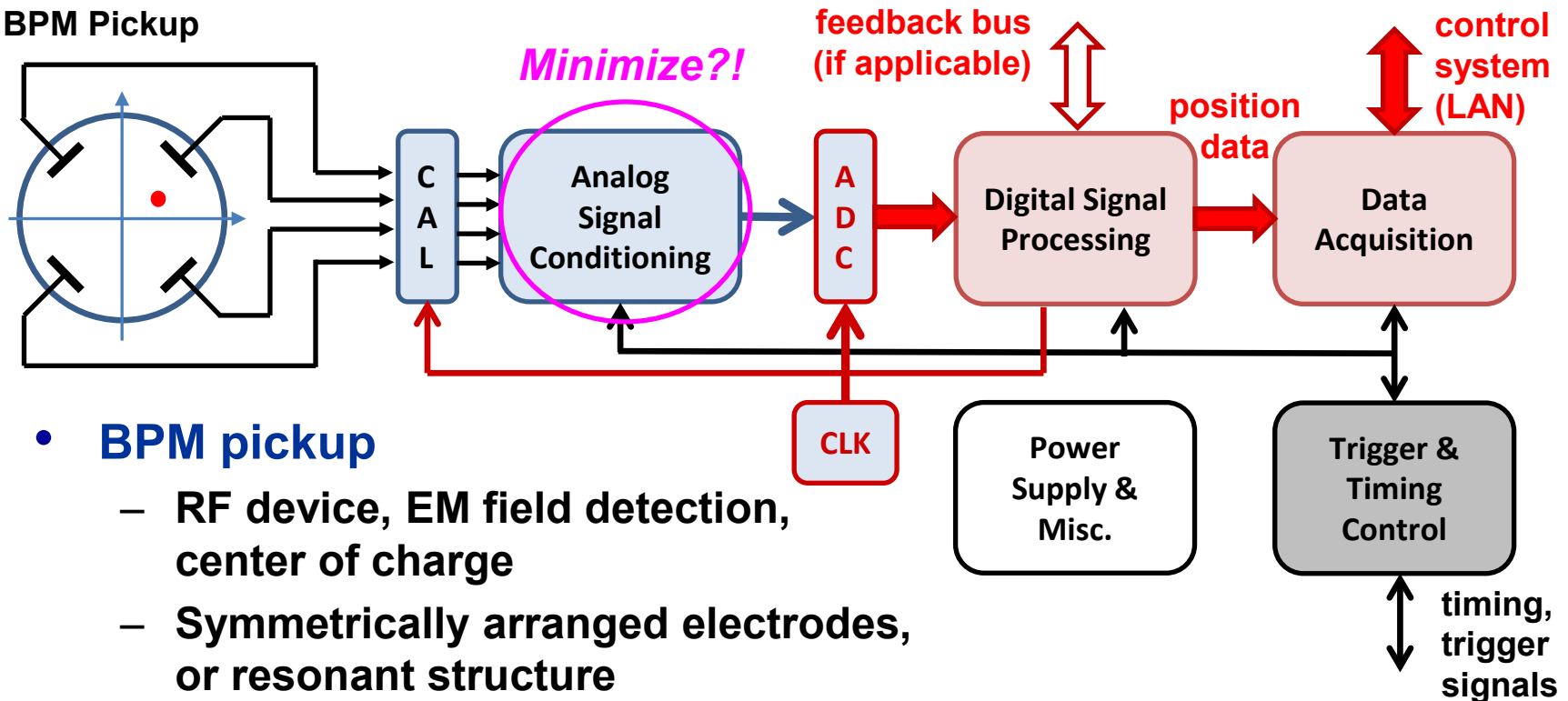
d_{pipe} : 25 mm
 d_{cav} : 200 mm
 l_{cav} : 10 mm



- **Extract the beam position information from the electrode signals: Normalization**
 - Analog using Δ - Σ or 90⁰-hybrids, followed by filters, amplifiers mixers and other elements, or logarithmic amplifiers.
 - Digital, performing the math on individual digitized electrode signals.
- **Decimation / processing of broadband signals**
 - BPM data often is not required on a bunch-by-bunch basis
 - **Exception: Fast feedback processors**
 - **Default: Turn-by-turn and “narrowband” beam positions**
 - Filters, amplifiers, mixers and demodulators in analog and digital to decimate broadband signals to the necessary level.
- **Other aspects**
 - Generate calibration / test signals
 - Correct for non-linearities of the beam position response of the BPM
 - Synchronization of turn-by-turn data
 - Optimization on the BPM system level to minimize cable expenses.
 - BPM signals keep other very useful information other than that based on the beam displacement, e.g.
 - **Beam intensity, beam phase (timing)**

BPM Building Blocks

BPM Pickup



- **BPM pickup**

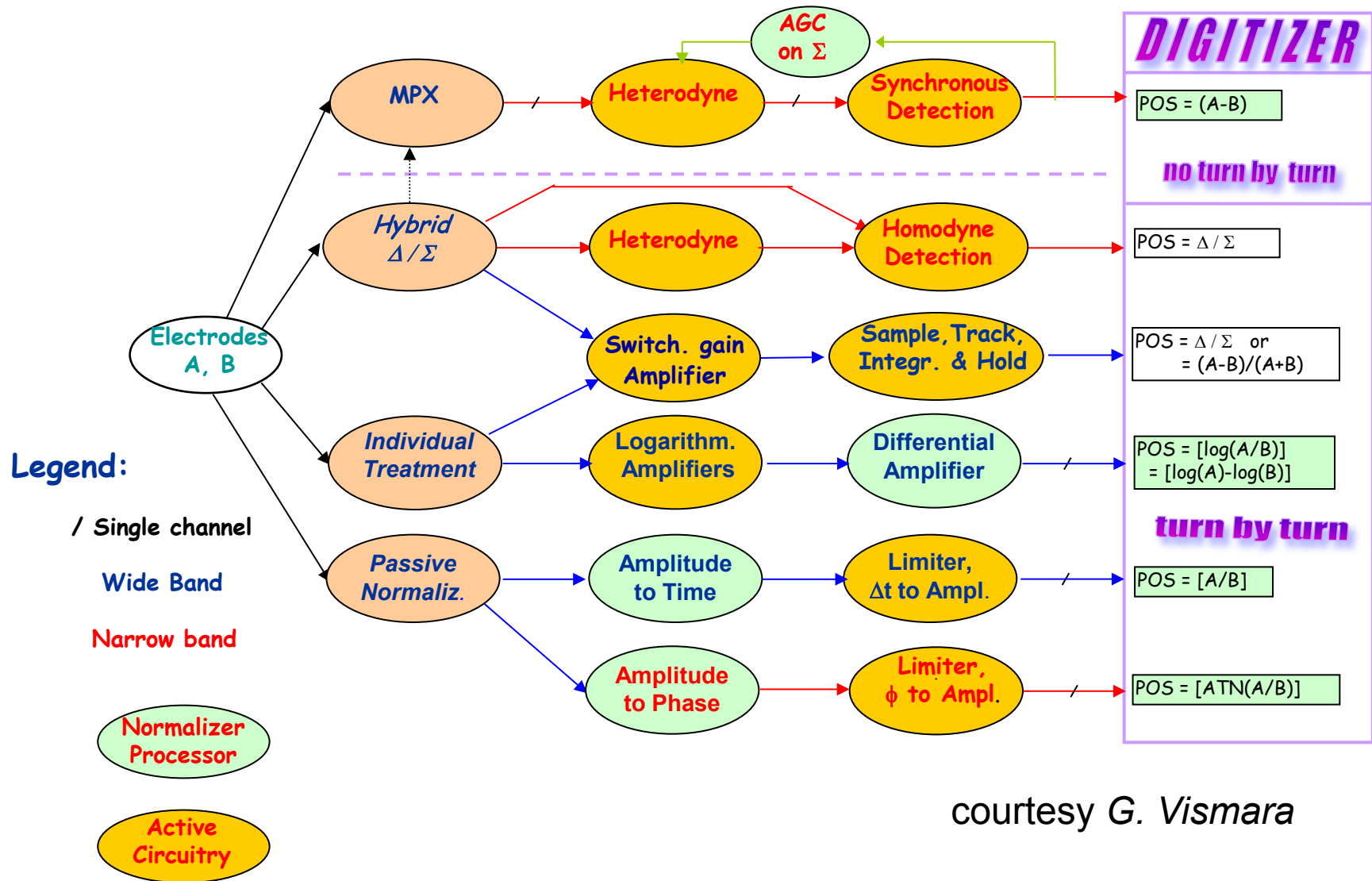
- RF device, EM field detection, center of charge
- Symmetrically arranged electrodes, or resonant structure

- **Read-out electronics**

- Analog signal conditioning
- Signal sampling (ADC)
- Digital signal processing

- Data acquisition and control system interface
- Trigger, CLK & timing signals
- Provides calibration signals or other drift compensation methods

Analog Signal Processing Options

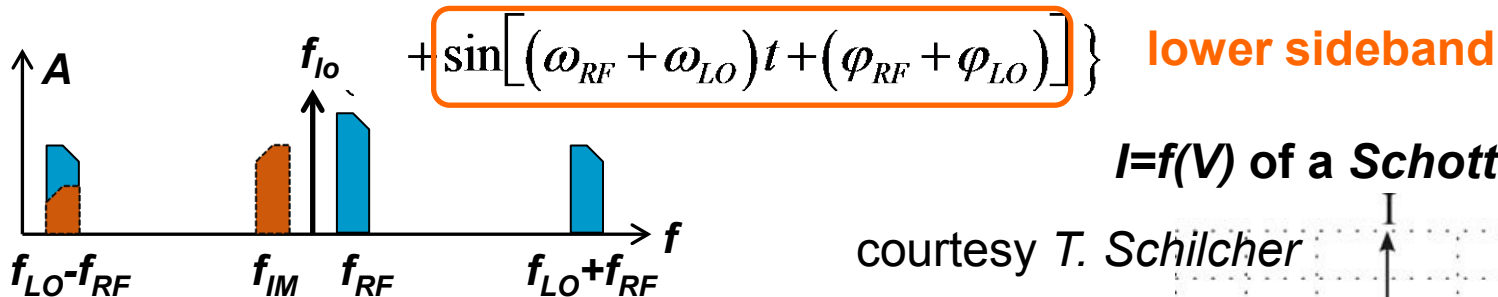


The RF Mixer as Downconverter

$$y_{RF}(t) = A_{RF} \sin(\omega_{RF}t + \varphi_{RF}) \quad \text{RF} \rightarrow \text{Mixer} \rightarrow \text{IF} \quad y_{IF}(t) = y_{RF}(t) y_{LO}(t)$$

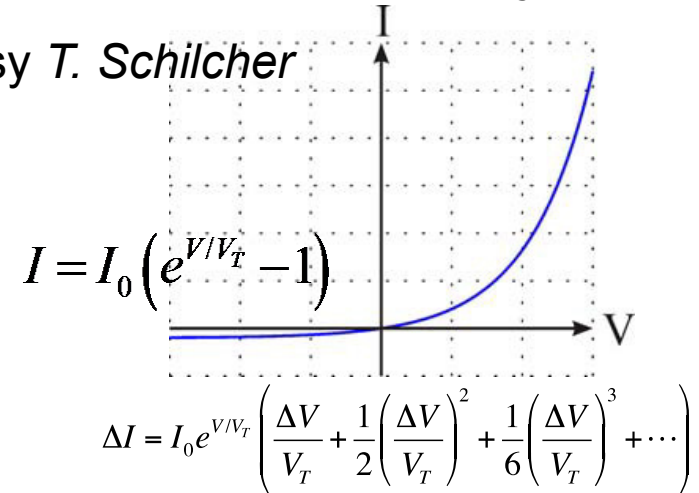
- Ideal mixer:** $f_{IF} = f_{RF} \pm f_{LO}$ $y_{LO}(t) = A_{LO} \sin(\omega_{LO}t + \varphi_{LO})$

$$y_{IF}(t) = \frac{1}{2} A_{LO} A_{RF} \left\{ \sin[(\omega_{RF} - \omega_{LO})t + (\varphi_{RF} - \varphi_{LO})] \right\} \quad \text{upper sideband}$$



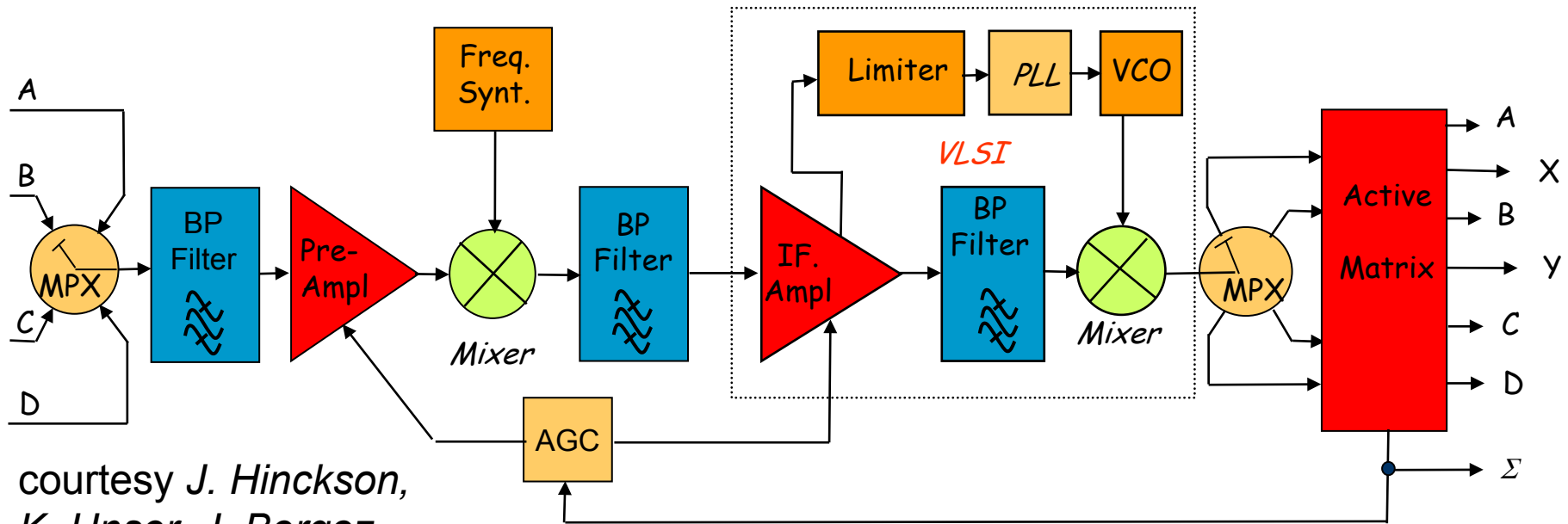
$I=f(V)$ of a Schottky diode

courtesy T. Schilcher



- Frequency conversion**
 - $f_{RF} > f_{LO}$: heterodyne receiver
 - $f_{RF} = f_{LO}$: homodyne, demodulator
- Real mixer:** $f_{IF} = m f_{RF} \pm n f_{LO}$
 - Image frequency:** $f_{IM} = f_{LO} - f_{IF}$

Example: MPX Heterodyne Receiver



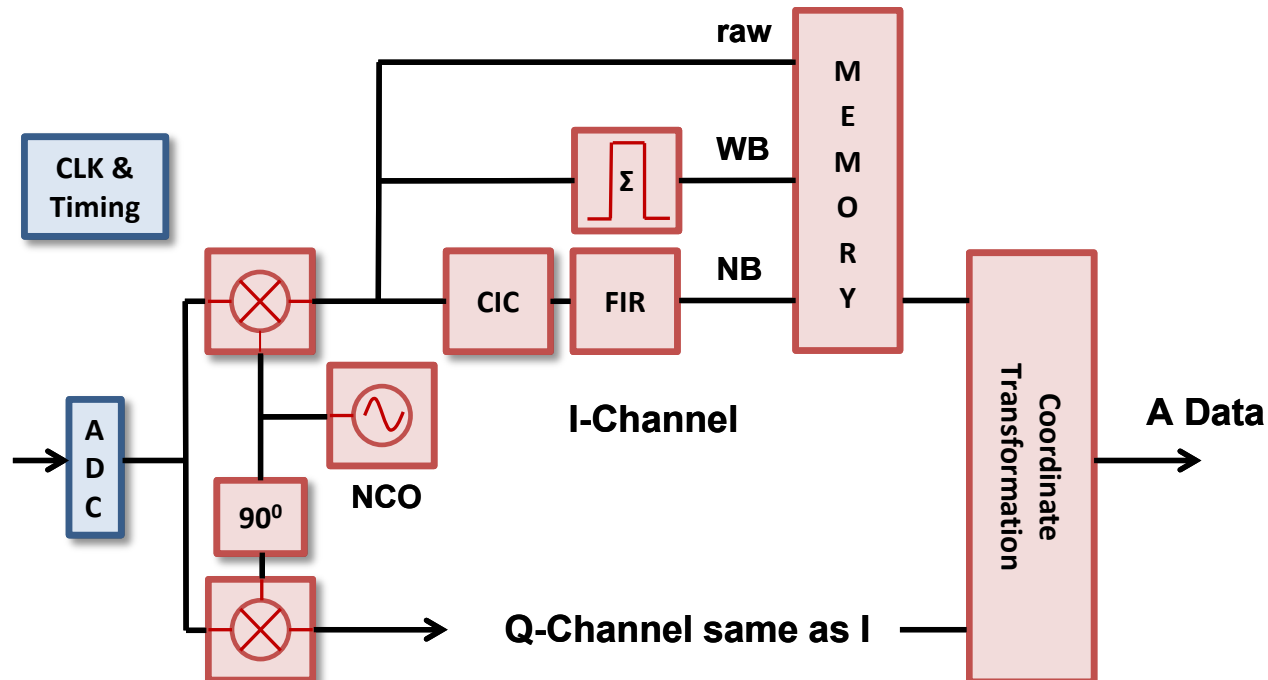
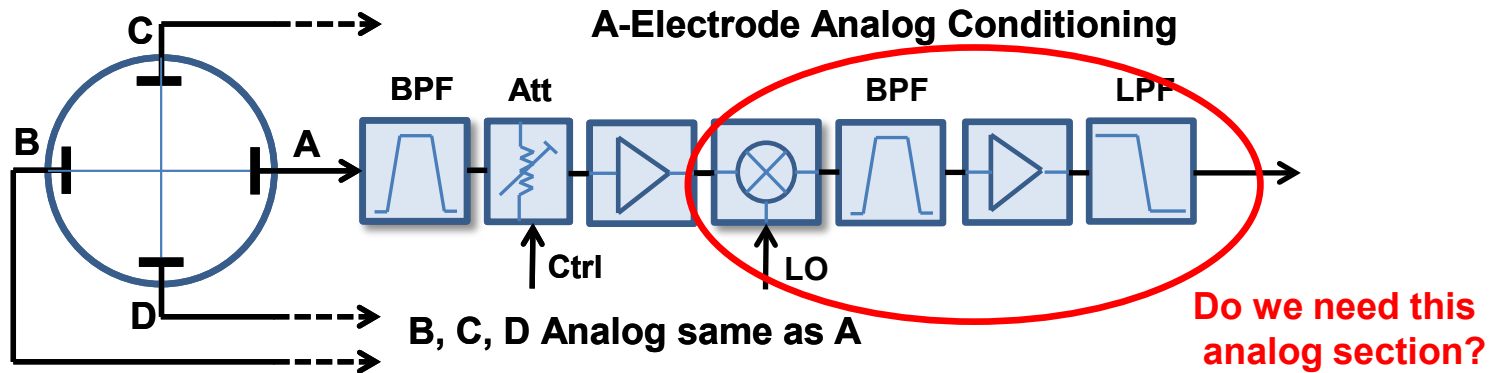
- **Analog BPM signal processing schemas typically:**
 - Normalize and demodulate (downconversion to baseband) the BPM signals **BEFORE** digitalization.
 - Substantially reduces the performance requirements for the ADC
 - **Resolution, dynamic range, bandwidth, sampling rate**

Digital BPM Signal Processing

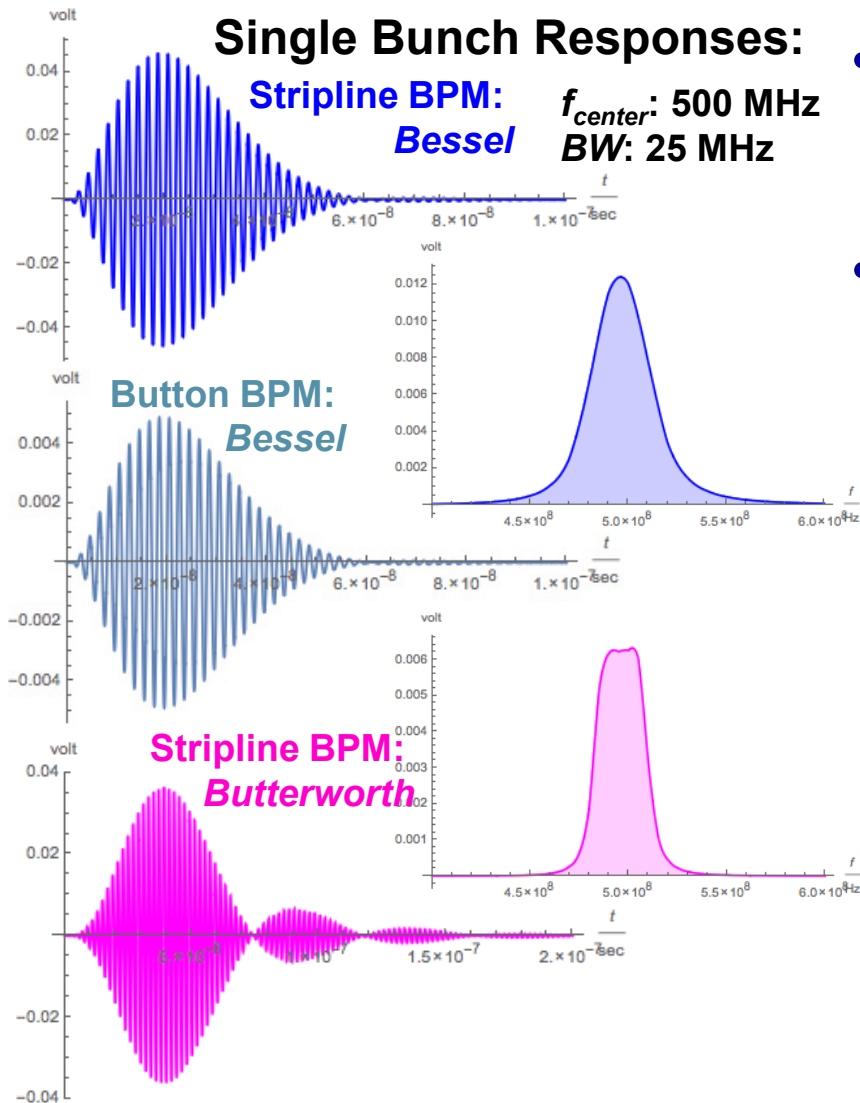


- **Why digital signal processing?**
 - Better reproducibility of the beam position measurement
 - Robust to environmental conditions, e.g. temperature, humidity, (radiation?)
 - No slow aging and/or drift effects of components
 - Deterministic, no noise or statistical effects on the position information
 - Flexibility
 - Modification of FPGA firmware, control registers or DAQ software to adapt to different beam conditions or operation requirements
 - Performance
 - Often better performance, e.g. higher resolution and stability compared to analog solutions
 - No analog equivalent of digital filters and signal processing elements.
- **BUT: Digital is not automatically better than analog!**
 - Latency of pipeline ADCs (FB applications)
 - Quantization and CLK jitter effects, dynamic range & bandwidth limits
 - Digital BPM solutions tend to be much more complex than some analog signal processing BPM systems
 - Manpower, costs, development time

BPM Read-out Electronics

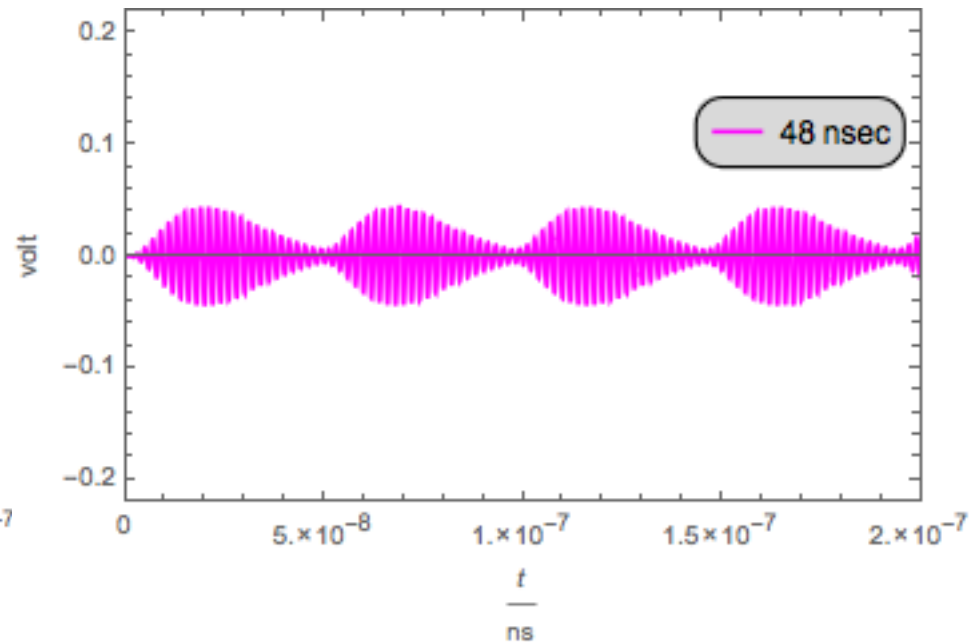
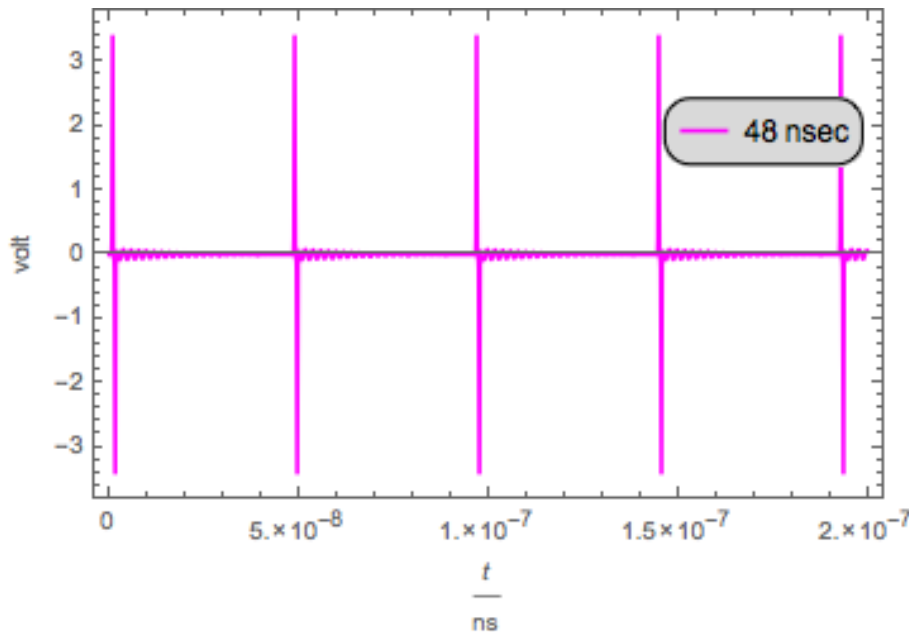


“Ringing” Bandpass-Filter (BPF)



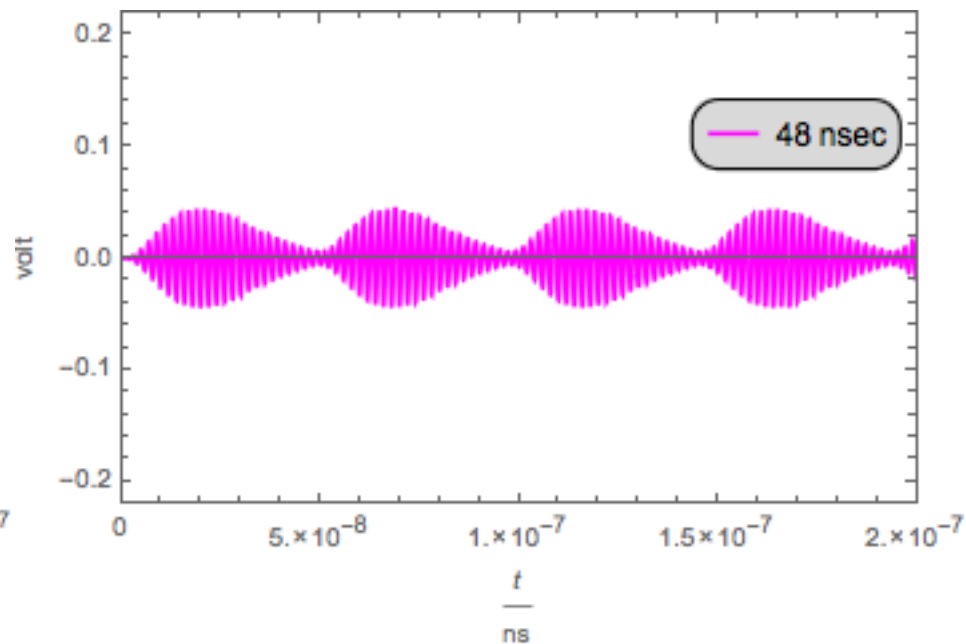
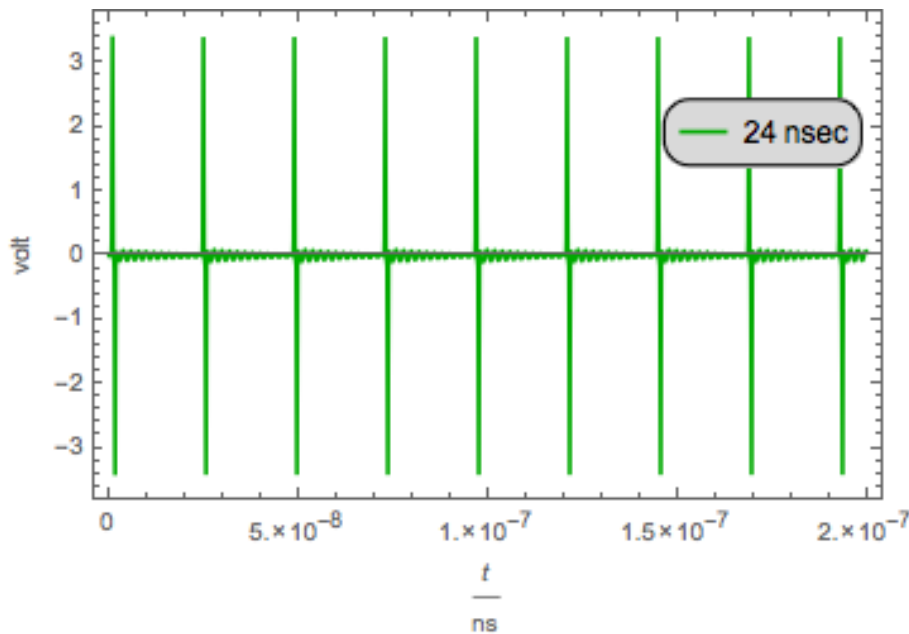
- BPM electrode signal energy is highly time compressed
 - **Most of the time: “0 volt”!**
- A “ringing” bandpass filter “stretches” the signal
 - Passive RF BPF
 - Matched pairs!
 - f_{center} matched to f_{rev} or f_{bunch}
 - Quasi sinusoidal waveform
 - Reduces output signal level
 - Narrow BW: longer ringing, lower signal level
 - Linear group delay designs
 - Minimize envelope ringing
 - *Bessel, Gaussian*, time domain designs

“Ringing” BPF & Multi-Bunches



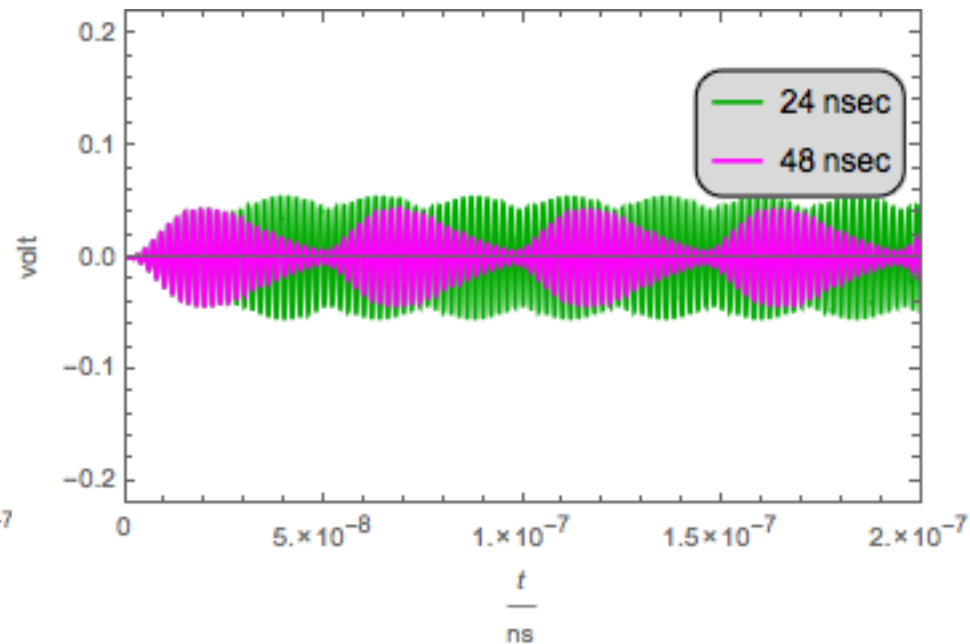
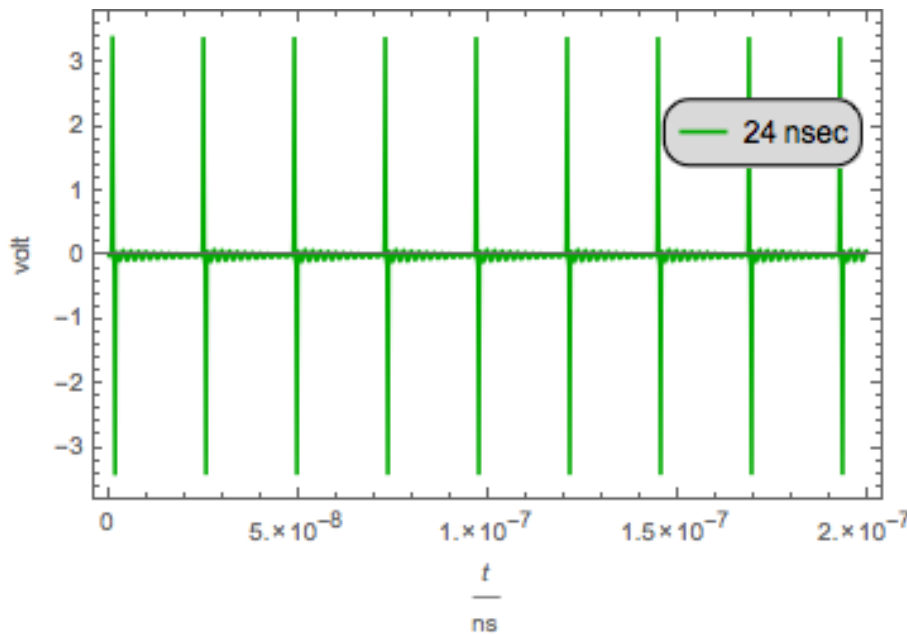
- **Bunch spacing < BPF ringing time:**
 - Superposition of single bunch BPF responses
 - More continuous “ringing”, smearing of SB responses
- **Bunch spacing < BPF rise time**
 - Constructive signal pile-up effect
 - **Output signal level increases linear with decreasing bunch spacing**

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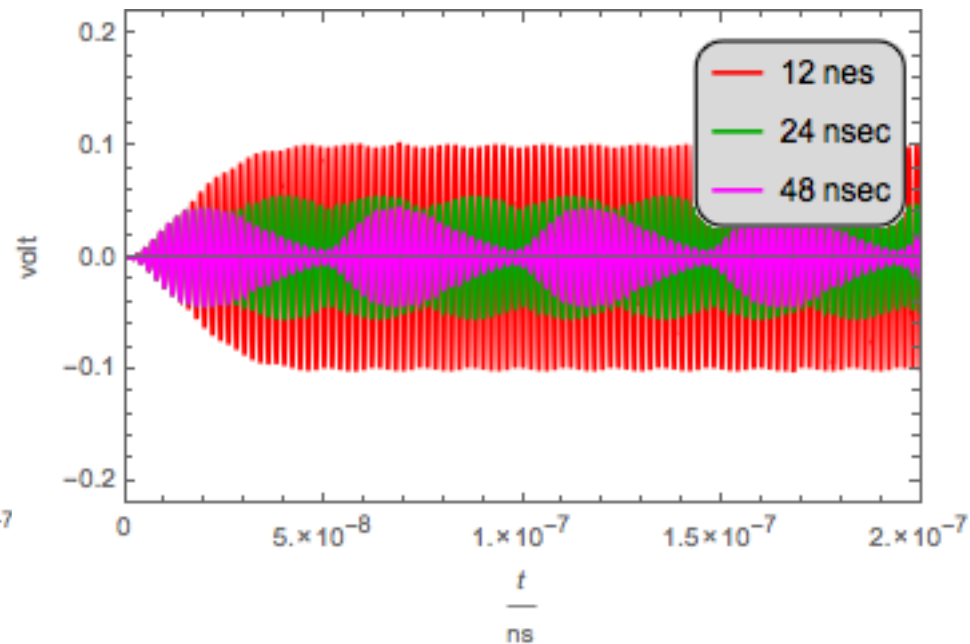
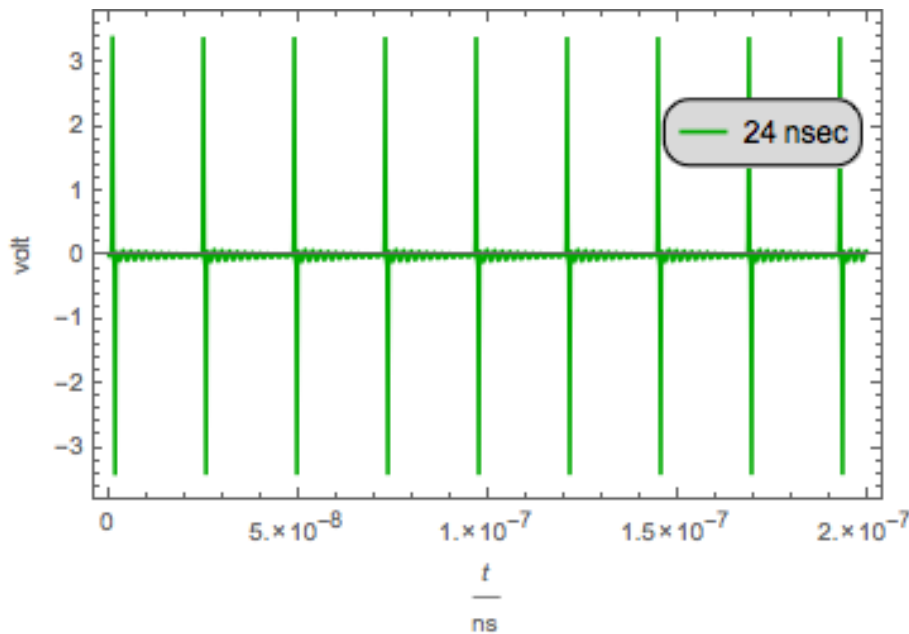
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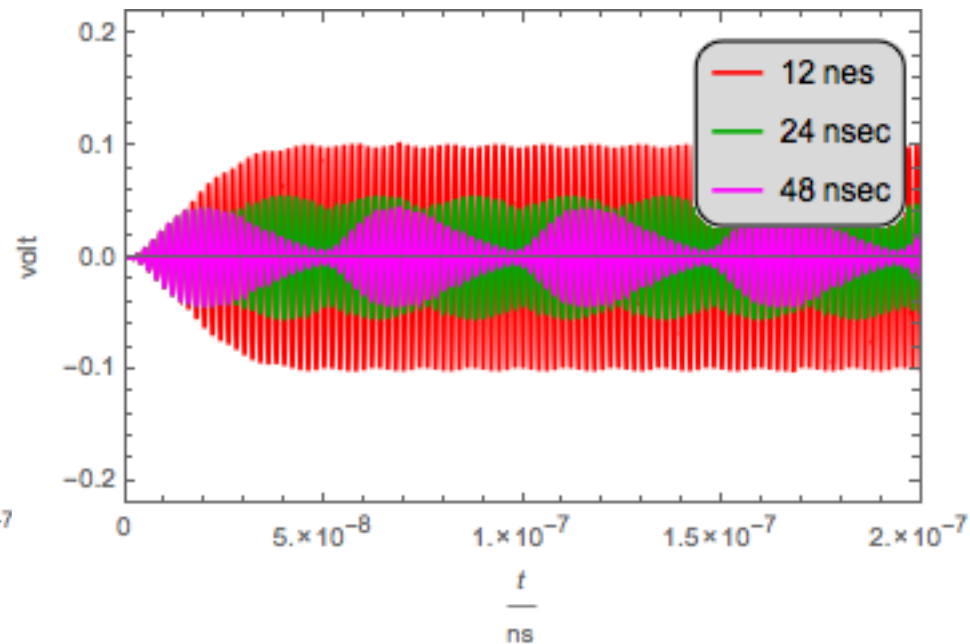
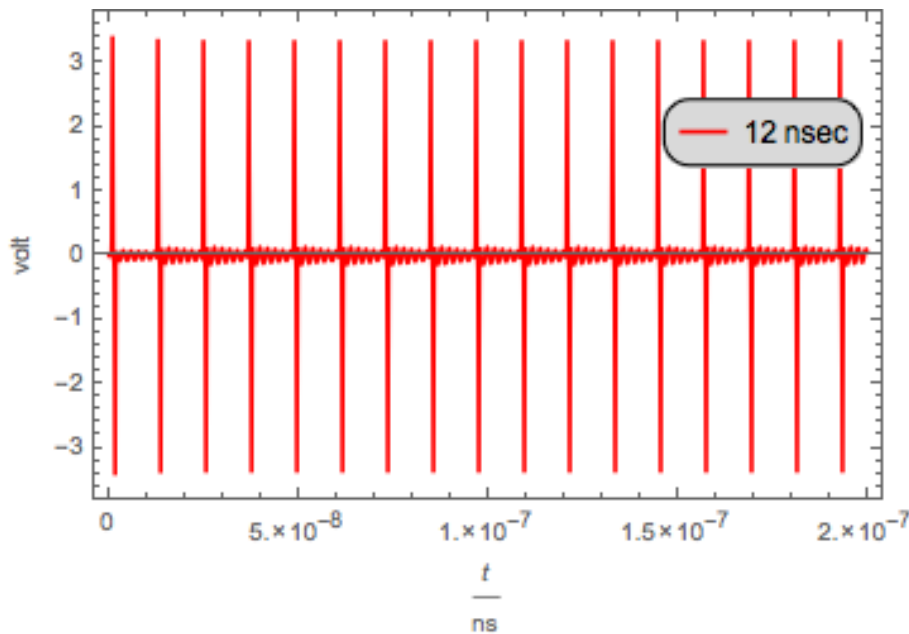
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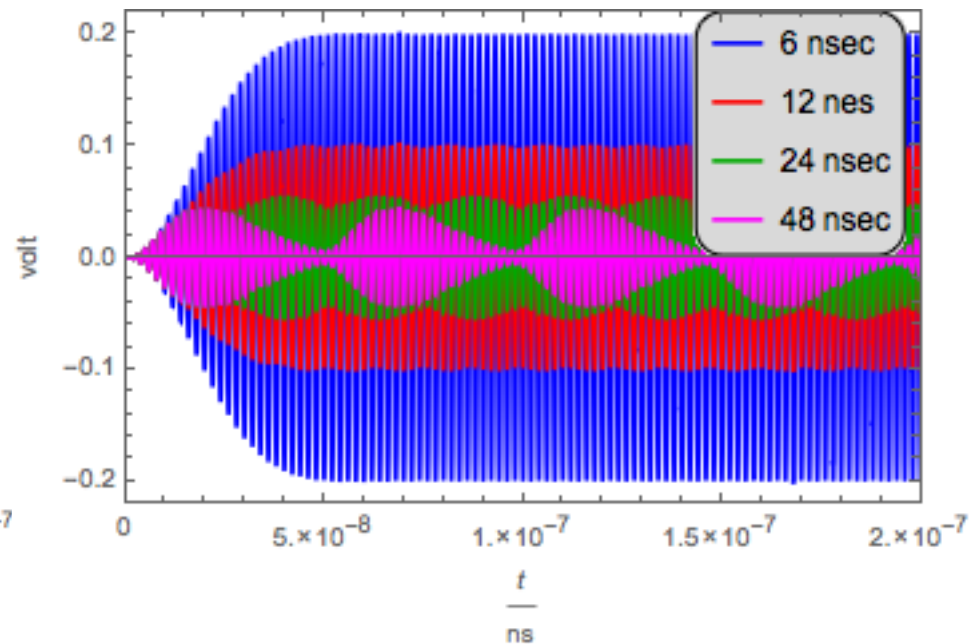
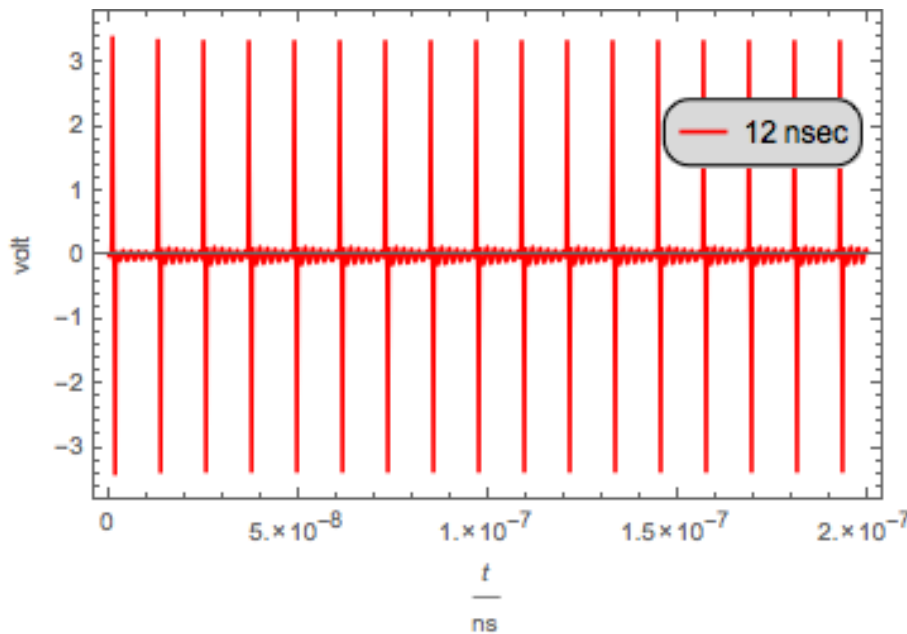
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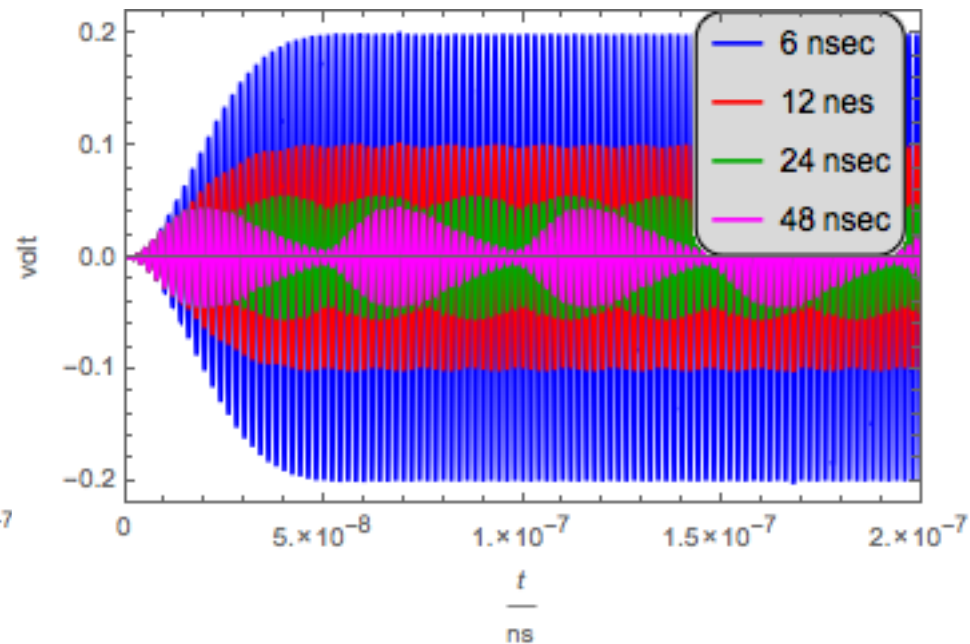
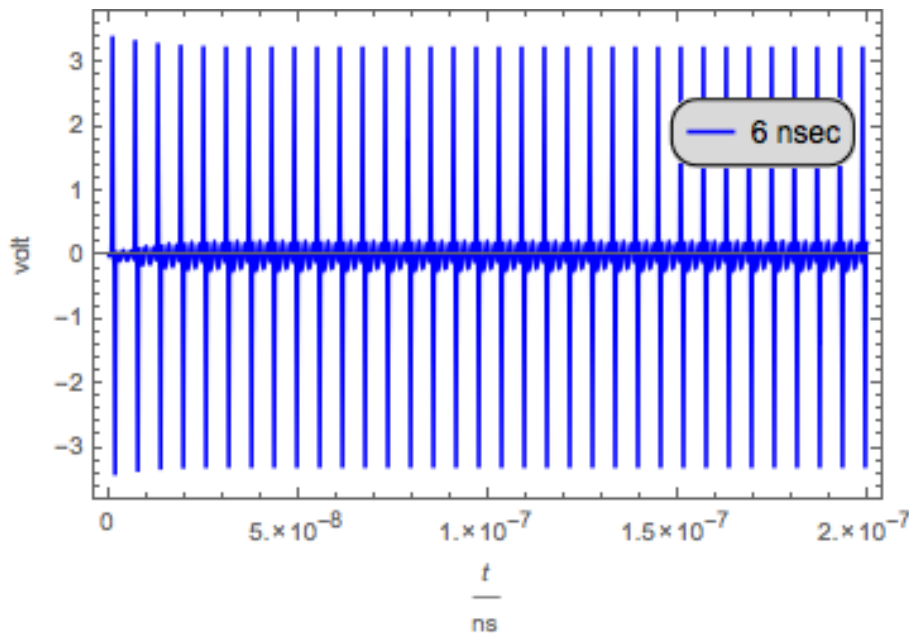
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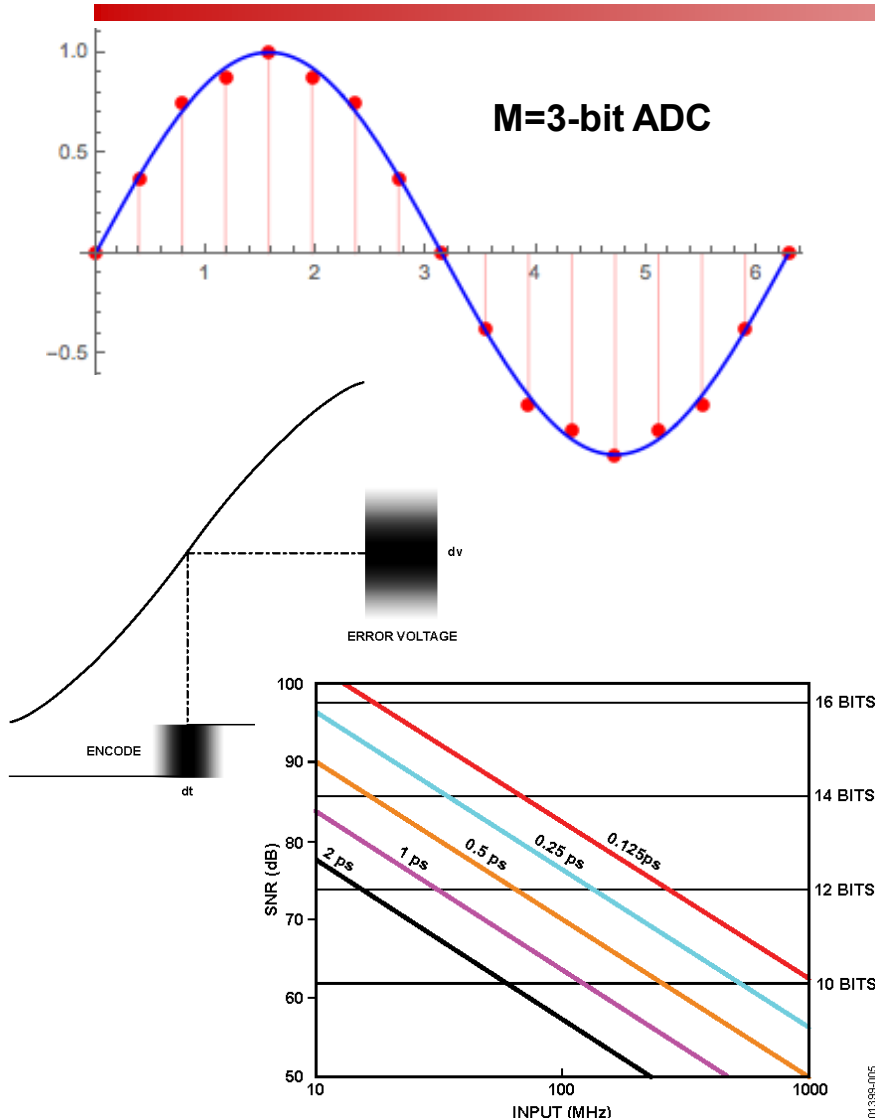
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Analog Digital Converter



- **Quantization of the continuous input waveform at equidistant spaced time samples**
 - Digital data is discrete in amplitude and time
- **LSB voltage (resolution)** $Q = \frac{V_{FSR}}{2^M}$
 - E.g. 61 μV (14-bit), 15 μV (16-bit) @ 1 volt V_{FSR}
- **Quantization error (dynamic range)** $SQNR = 20 \log_{10}(2^M)$
 - E.g. 84 dB (14-bit), 96 dB (16 bit)
- **SNR limit due to aperture jitter** $SNR = -20 \log_{10}(2\pi f t_a)$
 - E.g. 62 dB@500 MHz, 0.25 psec (equivalent to EOB=10.3)

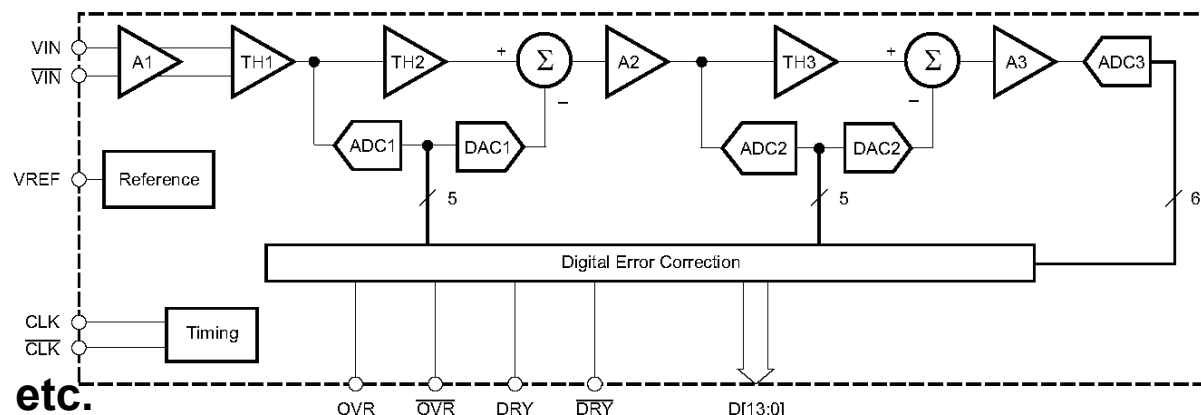
ADC Technology



	Type	Res. [bit]	Ch.	Power [W]	f_s (max) [MSPS]	BW [MHz]	SNR @ f_{in} [dB @ MHz]
AD	AD9652	16	2	2.2	310	485	72 @170
AD	AD9680	14	2	3.3	1000	2000	67 @ 170
LT	LTM9013*	14	2	2.6	310	300*	62 @ 150
TI	ADC16DX370	16	2	1.8	370	800	69 @ 150
TI	ADS5474-SP	14	1	2.5	400	1280	70 @ 230

* has an analog I-Q mixer integrated, $0.7 \text{ GHz} < f_{in} < 4 \text{ GHz}$

- **Dual Channel**
 - I-Q sampling with separate ADCs
- **Pipeline architecture**
 - Continuous CLK
 - Data latency
- **A-D mixed designs**
 - Mixers, gain, filters, etc.



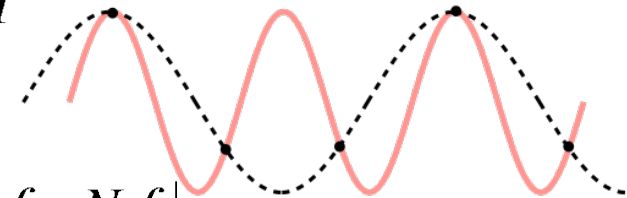
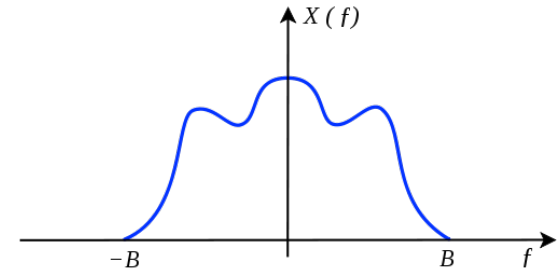
Sampling Theory

- A band limited signal $x(t)$ with $B=f_{\max}$ can be reconstructed if

$$f_s \geq 2 f_{\max}$$

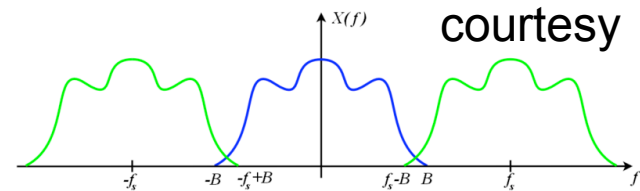
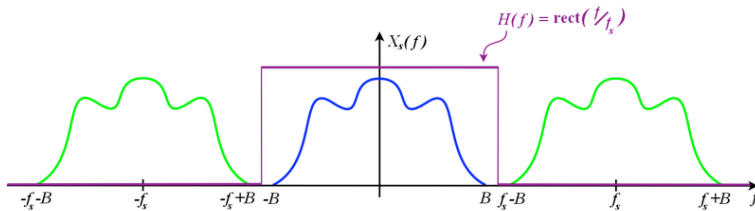
- Nyquist-Shannon theorem
- The exact reconstruction of $x(t)$ by $x_n=x(nT)$:

$$x(t) = \sum_{n=-\infty}^{+\infty} x_n \frac{\sin \pi(2f_{\max}t - n)}{\pi(2f_{\max}t - n)} = \sum_{n=-\infty}^{+\infty} x_n \operatorname{sinc} \frac{t - nT}{T}$$

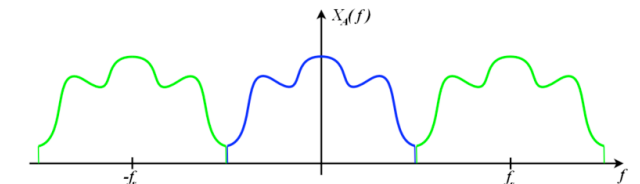
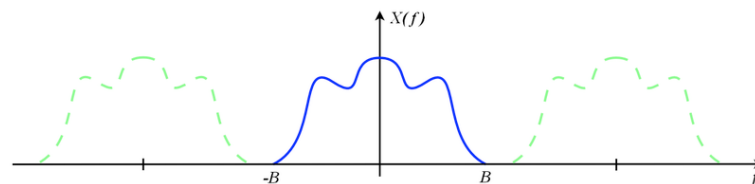


- Aliasing of a sampled sin-function

- Samples can be interpreted by $f_{\text{alias}}(N) = |f - N f_s|$



courtesy Wikipedia



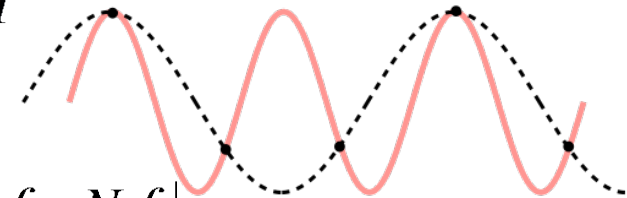
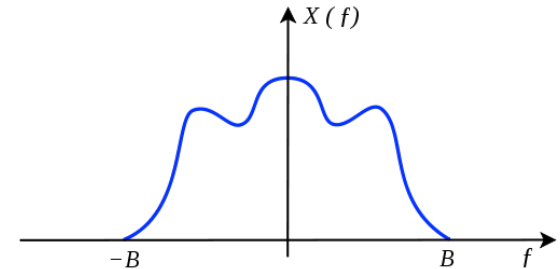
Sampling Theory

- A band limited signal $x(t)$ with $B=f_{\max}$ can be reconstructed if

$$f_s \geq 2 f_{\max}$$

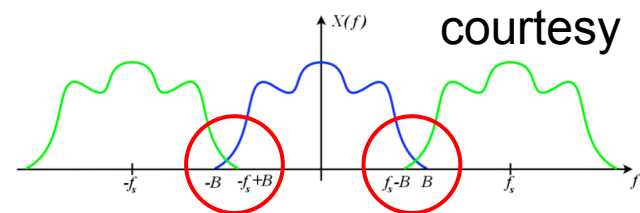
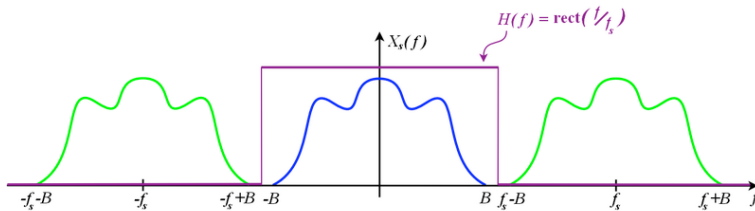
- Nyquist-Shannon theorem
- The exact reconstruction of $x(t)$ by $x_n=x(nT)$:

$$x(t) = \sum_{n=-\infty}^{+\infty} x_n \frac{\sin \pi(2f_{\max}t - n)}{\pi(2f_{\max}t - n)} = \sum_{n=-\infty}^{+\infty} x_n \operatorname{sinc} \frac{t - nT}{T}$$

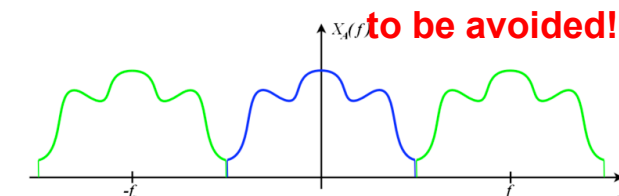
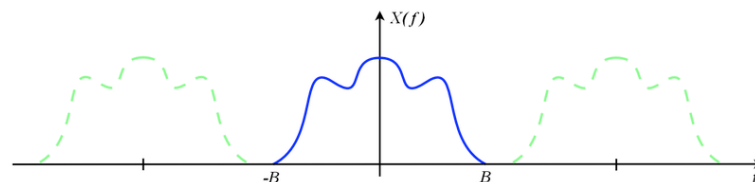


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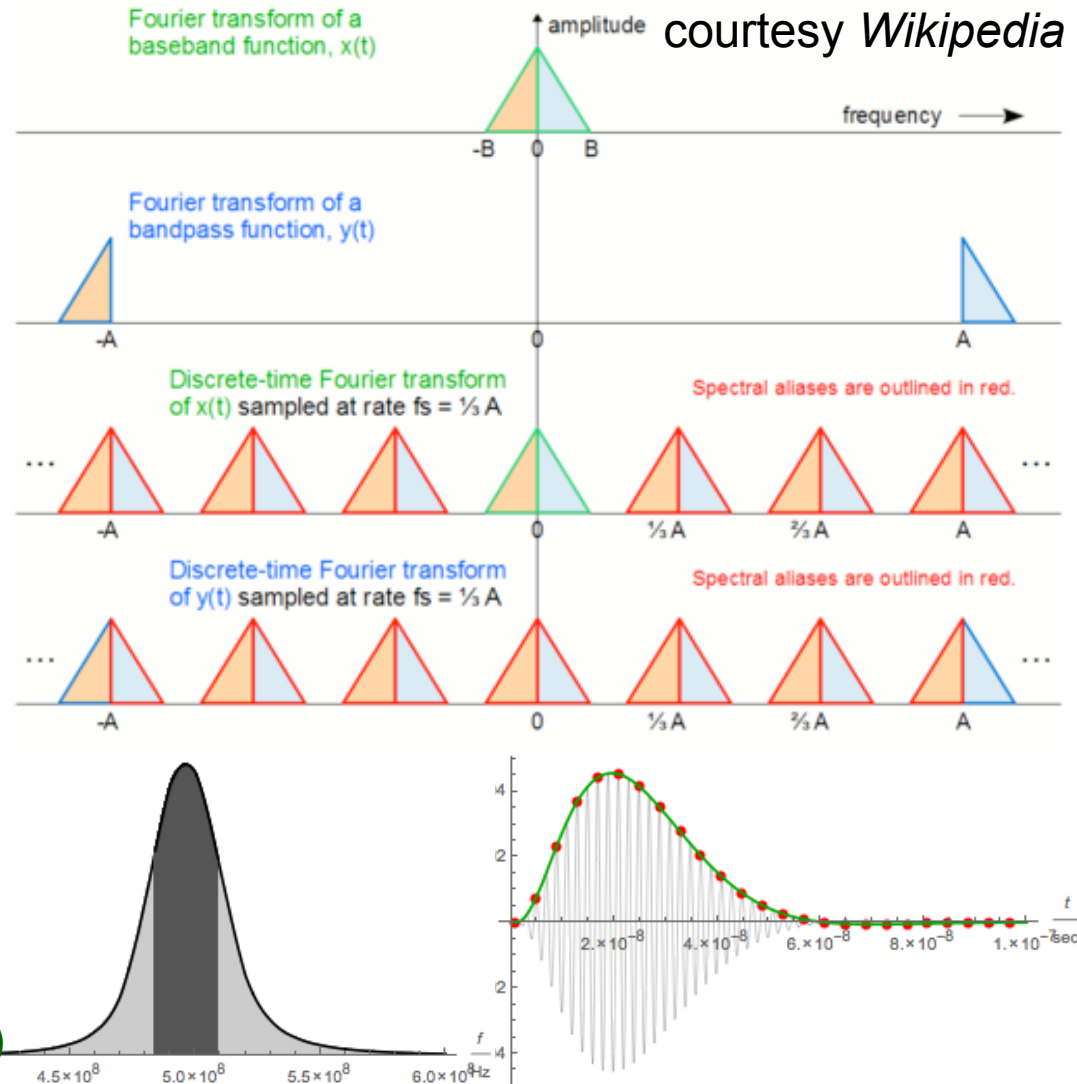
courtesy Wikipedia



Bandpass or Undersampling

- A bandpass signal $f_{lo}=A$, $f_{hi}=A+B$ is down-converted to baseband
 - The sampling frequency has to satisfy:

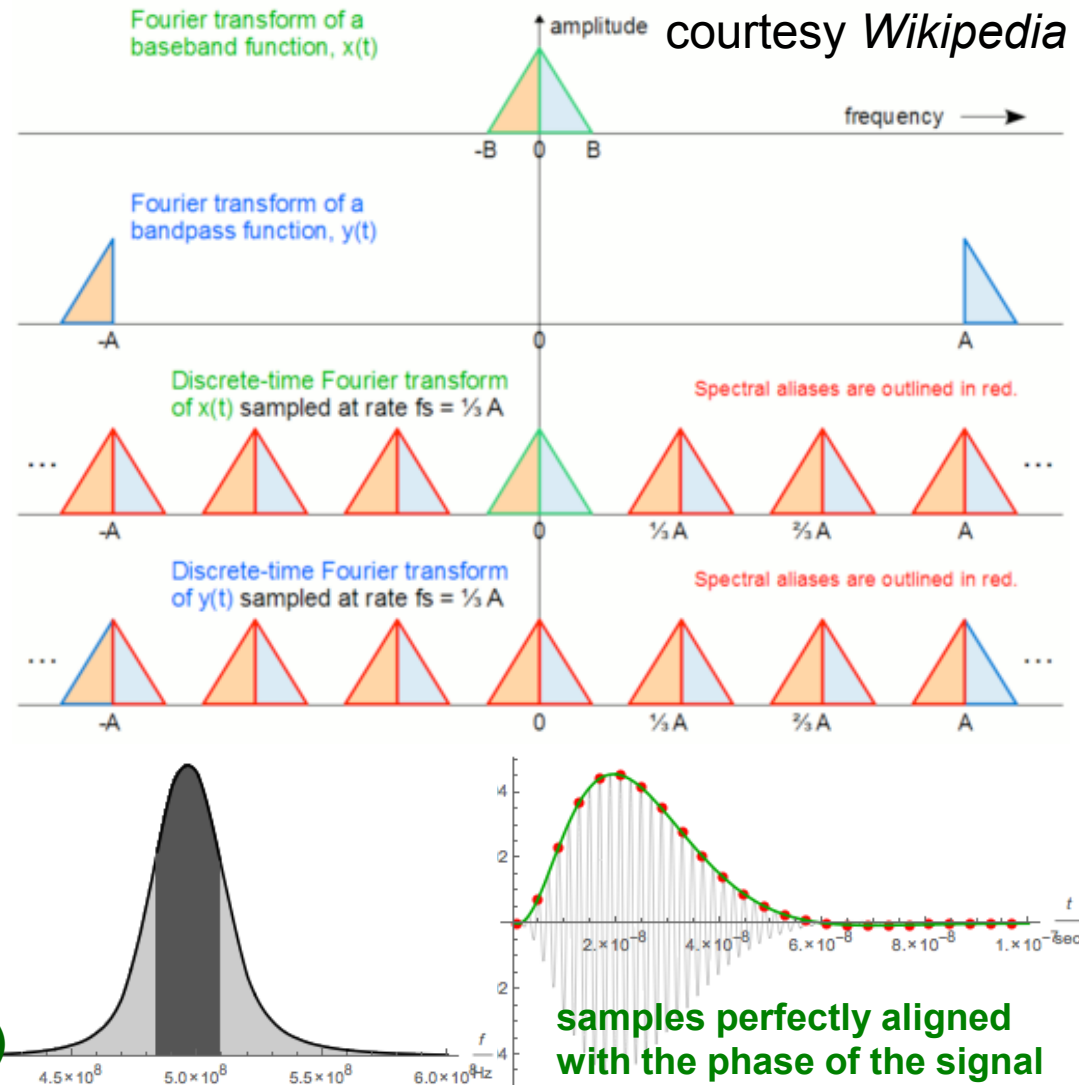
$$\frac{2f_{hi}}{n} \leq f_s \leq \frac{2f_{lo}}{n-1}$$
 with: $1 \leq n \leq \left\lfloor \frac{f_{hi}}{f_{hi} - f_{lo}} \right\rfloor$
- Digital down-conversion (DDC) of BPM signals
 - BPM -> BPF (Bessel)
 - f_{center} : ~500 MHz
 - BW (3 dB): 25 MHz
 - $T=4$ ns, $f_s=200$ MHz
 - $(f_{hi}/f_{lo}=550/450$ MHz, $n=5.5)$



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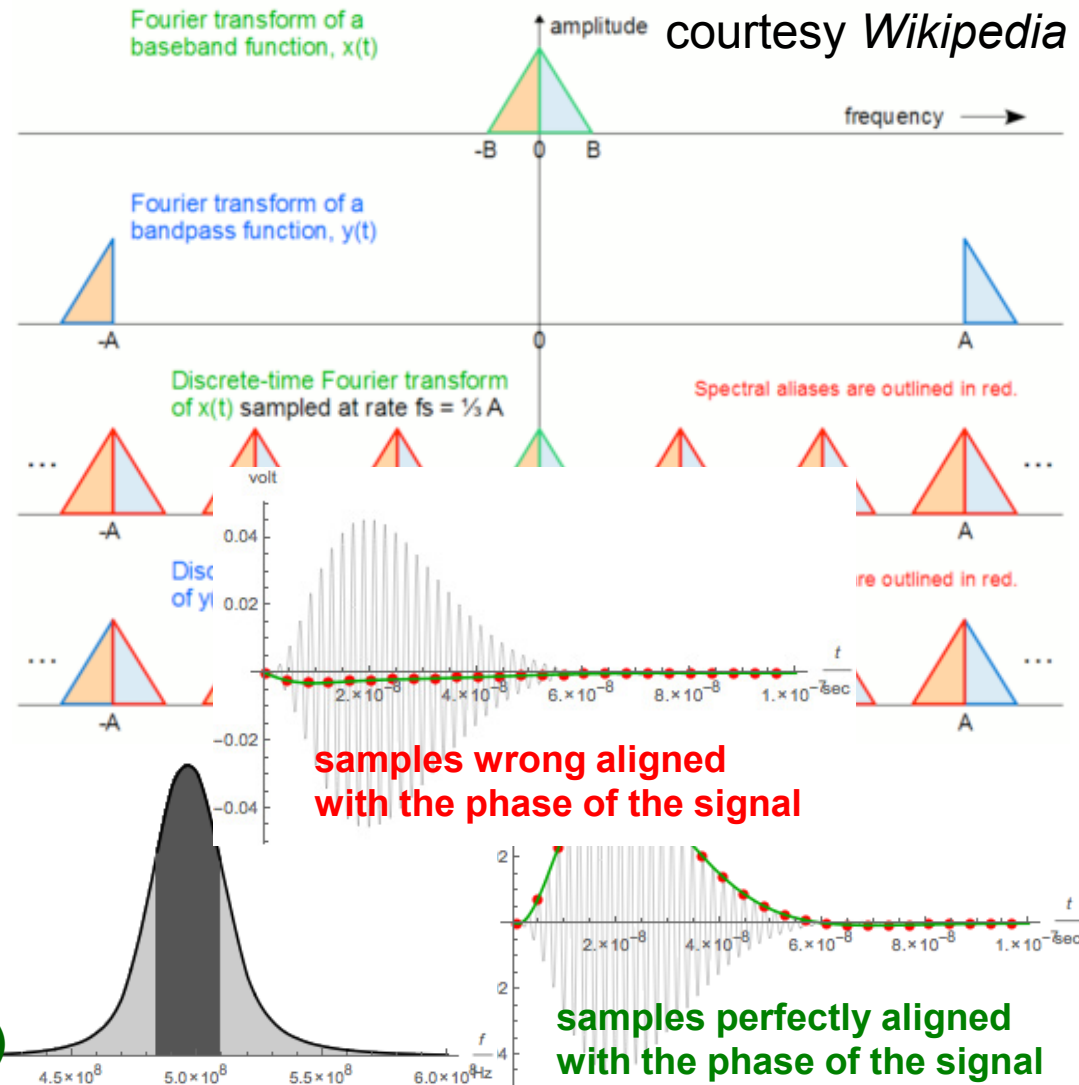
$$\frac{2f_{hi}}{n} \leq f_s \leq \frac{2f_{lo}}{n-1}$$

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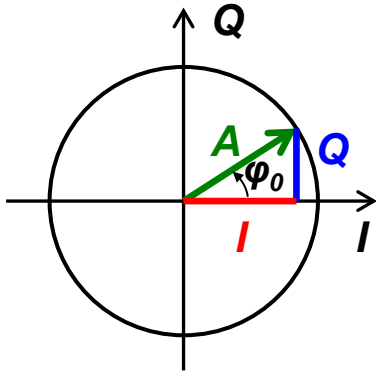
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- ($f_{hi}/f_{lo}=550/450$ MHz, $n=5.5$)



I-Q Sampling



- **Vector representation of sinusoidal signals:**
 - **Phasor rotating counter-clockwise (pos. freq.)**

$$y(t) = A \sin(\omega t + \varphi_0)$$

$$y(t) = \underbrace{A \cos \varphi_0}_{=: I} \sin \omega t + \underbrace{A \sin \varphi_0}_{=: Q} \cos \omega t$$

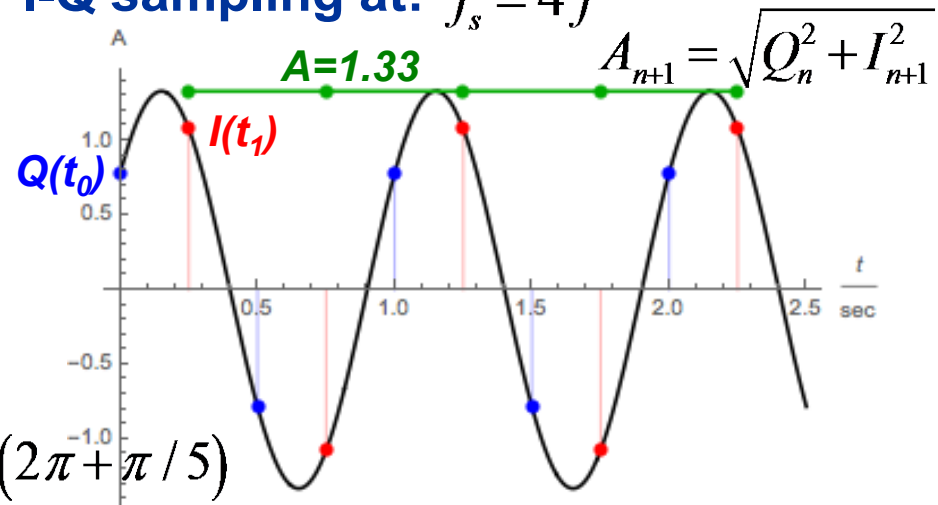
I: in-phase component **Q: quadrature-phase component**

$$y(t) = I \sin \omega t + Q \cos \omega t$$

$$I = A \cos \varphi_0 \quad A = \sqrt{I^2 + Q^2}$$

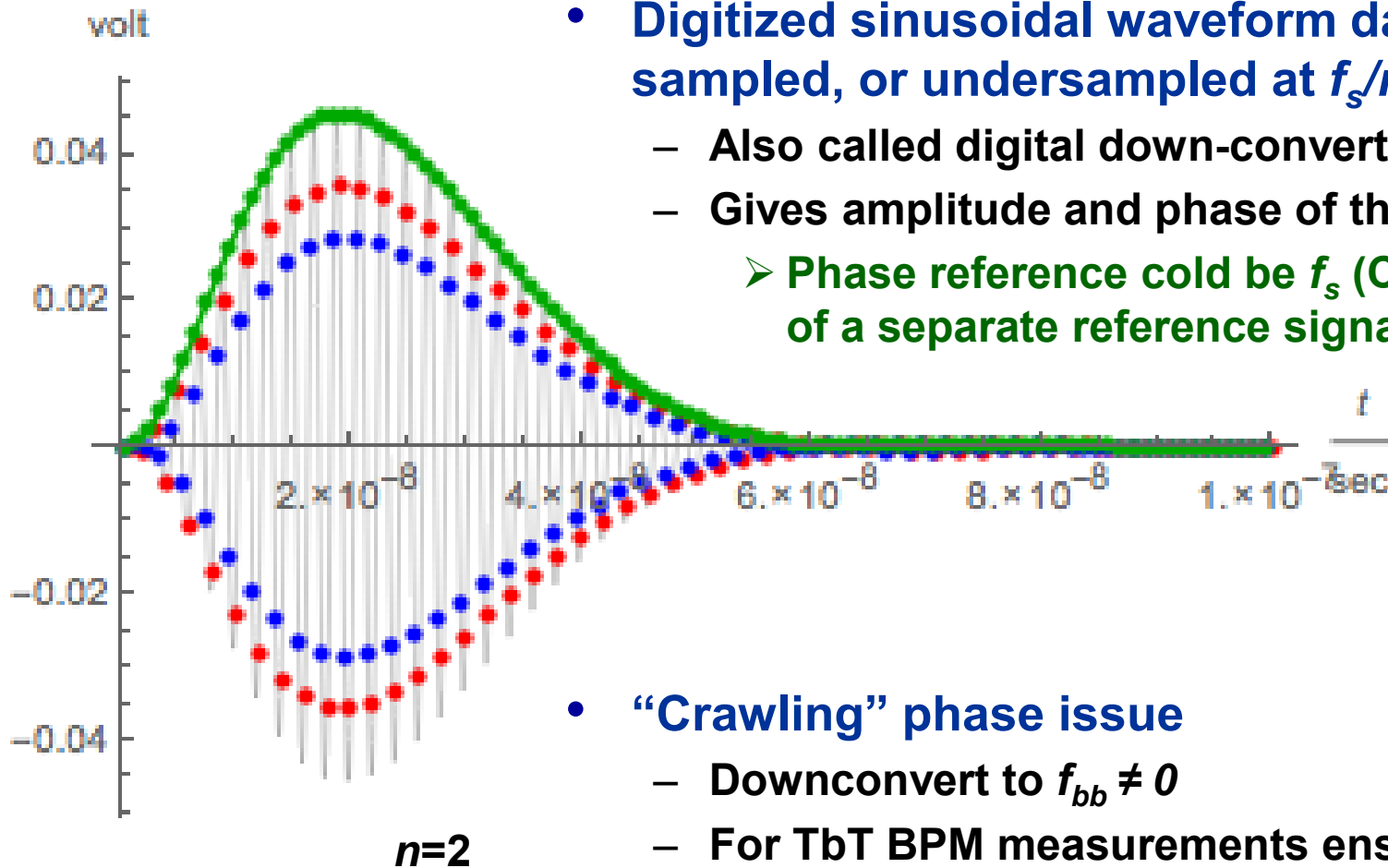
$$Q = A \sin \varphi_0 \quad \varphi_0 = \arctan\left(\frac{Q}{I}\right)$$

- **I-Q sampling at: $f_s = 4f$**



$$y(t) = 1.33 \sin(2\pi + \pi/5)$$

I-Q Demodulation of BPM Signals



- Digitized sinusoidal waveform data is sampled, or undersampled at $f_s/n = 4xf_{in}$
 - Also called digital down-converter (DDC)
 - Gives amplitude and phase of the input signal
 - Phase reference could be f_s (CLK), of a separate reference signal
- “Crawling” phase issue
 - Downconvert to $f_{bb} \neq 0$
 - For TbT BPM measurements ensure $f_{bb} = i f_{rev}$

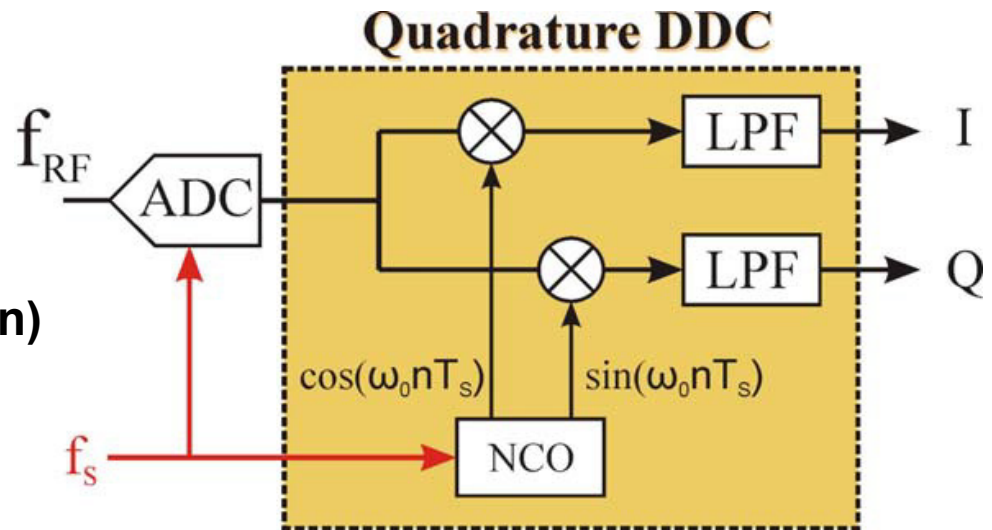
Digital Down-Converter

- **Goals**

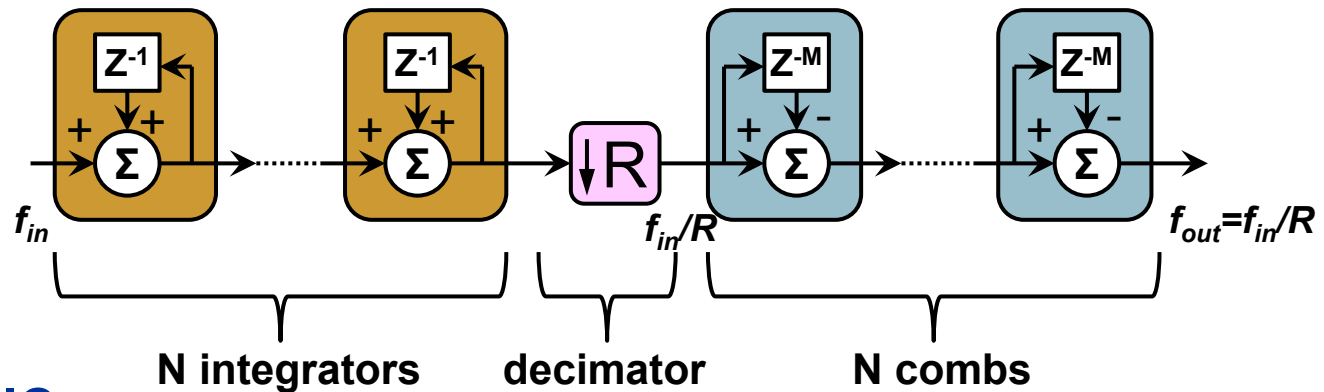
- Convert the band limited RF-signal to baseband (demodulation)
- Data reduction (decimation)

- **DDC Building blocks:**

- ADC
 - Single fast ADC (oversampling)
- Local oscillator
 - Numerically controlled oscillator (NCO) based on a direct digital frequency synthesizer (DDS)
- Digital mixers (“ideal” multipliers)
- Decimating low pass (anti alias) filters
 - Filtering and data decimation.
 - Implemented as CIC and/or FIR filters



Cascaded Integrator Comb Filter (CIC)



- **Decimating CIC**

- **Boxcar filter (anti aliasing)**
 - **non-recursive moving average filter**
- **Decimator**
 - **Data rate reduction**
- **Comb filter**
 - **Recursive running-sum**

$$\begin{aligned}
 H(z) &= H_I^N(z) H_C^N(z) \\
 &= \frac{1}{(1 - z^{-1})^N} (1 - z^{-RM})^N \\
 &= \left(\sum_{k=0}^{RM-1} z^{-k} \right)^N \quad \text{FIR filter (stable)}
 \end{aligned}$$

- **Economical implementation**

- **No multiplier, minimum storage requirements**

CIC Filter (cont.)

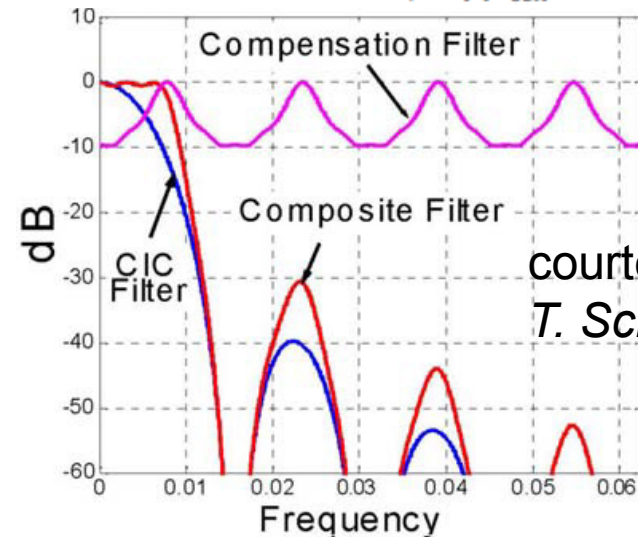
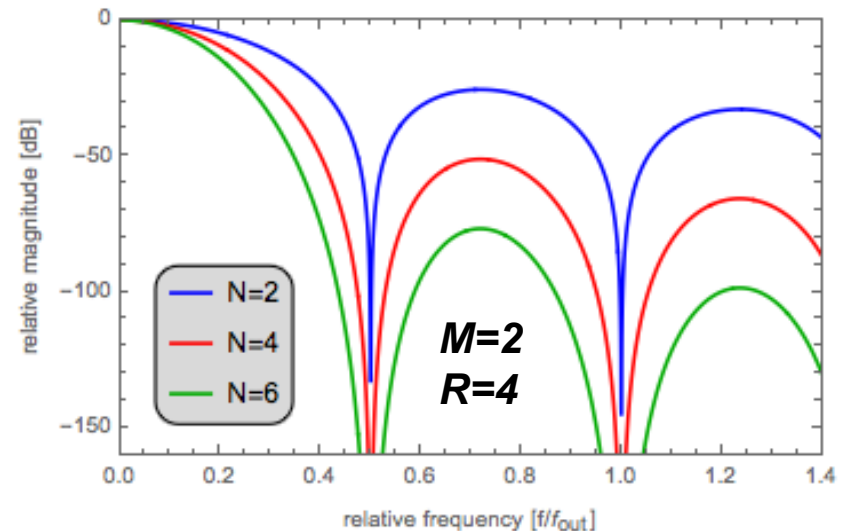
- CIC frequency response

$$H(f) = \left(\frac{\sin \pi M \frac{f}{f_{out}}}{\sin \frac{\pi}{R} \frac{f}{f_{out}}} \right)^N$$

- With respect to the output frequency: $f_{out} = \frac{f_{in}}{R}$
- M : differential delay, determines the location of the zeros: $f_0 = k \frac{f_{out}}{M}$
- N : number of CIC stages
- R : decimation ratio
 - Has little influence on the filter response

- CIC plus FIR compensation filter

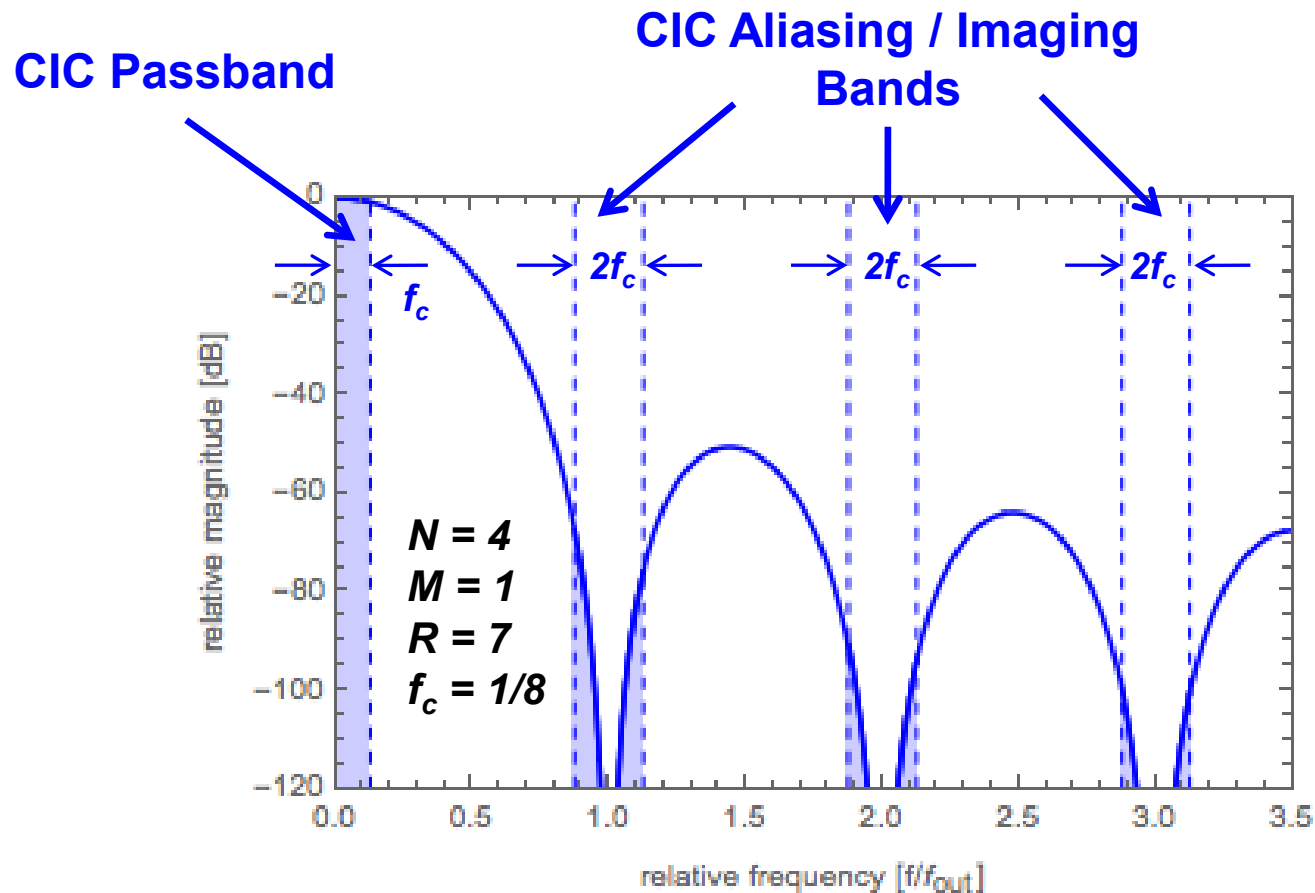
- Compensate CIC passband drop



CIC compensation filter for $M=3, R=64$.

courtesy
T. Schilcher

CIC Aliasing – Imaging



courtesy
E. Hogenauer

- **CIC aliasing / imaging bands are around:** $(i - f_c) \leq f \leq (i + f_c)$

Signal/Noise & Theoretical Resolution Limit



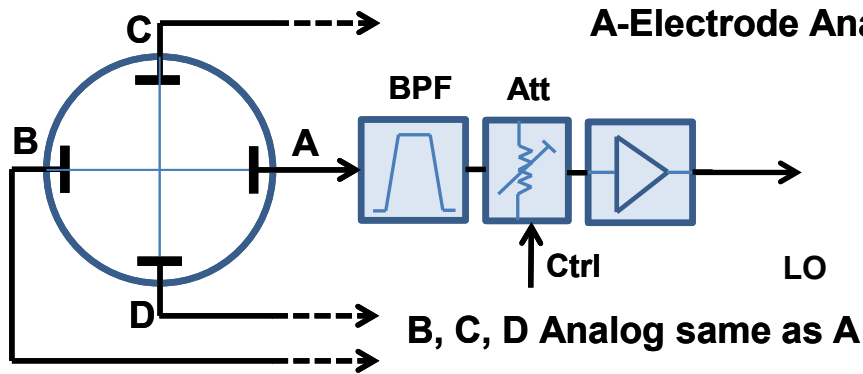
- **Minimum noise voltage at the 1st gain stage:** $v_{noise} = \sqrt{4k_B T R \Delta f}$
 - With the stripline BPM and Bessel BPF example:
 $R = 50 \Omega$, $\Delta f = 25 \text{ MHz} \rightarrow v_{noise} = 4.55 \mu\text{V}$ (-93.83 dBm)
- **Signal-to-noise ratio:** $S/N = \frac{\Delta v}{v_{noise}}$
 - Where Δv is the change of the voltage signal at the 1st gain stage due to the change of the beam position (Δx , Δy).
 - Consider a signal level $v \approx 22.3 \text{ mV}$ (-20 dBm)
 - **Bessel BPF output signal of the stripline BPM example**
 - $22.3 \text{ mV} / 4.55 \mu\text{V} \approx 4900$ (73.8 dB) would be the required dynamic range to resolve the theoretical resolution limit of the BPM
 - **Under the given beam conditions, e.g. $n=1e10$, $\sigma=25\text{mm}$, single bunch, etc.**
 - **The equivalent BPM resolution limit would be: $\Delta x=\Delta y=0.66\mu\text{m}$ (assuming a sensitivity of $\sim 2.7\text{dB/mm}$)**

S/N & BPM Resolution (cont.)



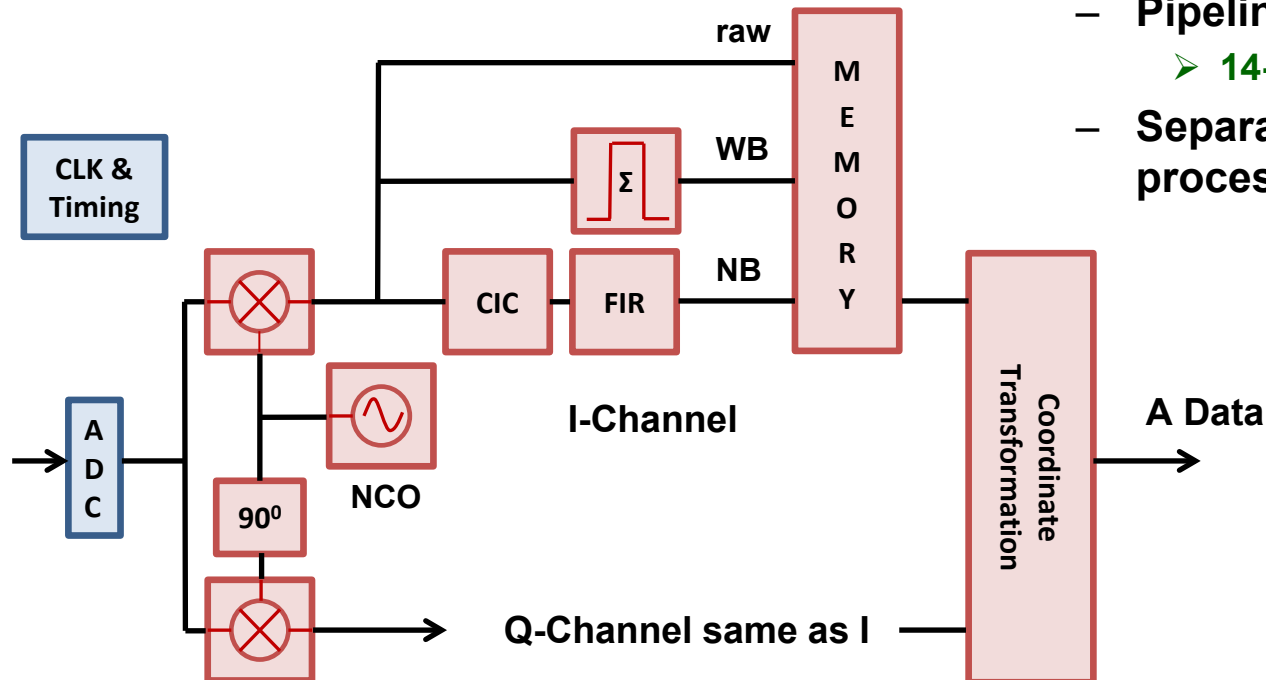
- **Factors which reduce the S/N**
 - Insertion losses of cables, connectors, filters, couplers, etc.
 - Typically sum to 3...6 dB
 - Noise figure of the 1st amplifier, typically 1...2 dB
 - The usable S/N needs to be >0 dB,
e.g. 2.3 dB is sometimes used as lowest limit. (*HP SA* definition)
 - For the given example the single bunch / single turn resolution limit reduces by ~10 dB (~3x): 2...3 μm
- **Factors to improve the BPM resolution**
 - Increase the signal level
 - Increase BPM electrode-to-beam coupling,
e.g. larger electrodes
 - Higher beam intensity
 - Increase the measurement time, apply statistics
 - Reduce the filter bandwidth (S/N improves with $1/\sqrt{\text{BW}}$)
 - Increase the number of samples (S/N improves with $\sqrt{n}$)

BPM Read-out Electronics

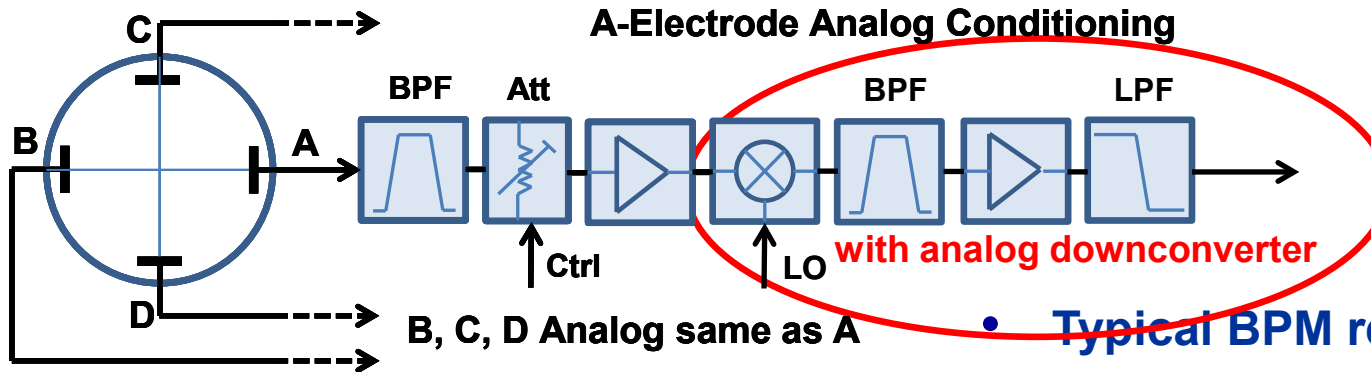


- **Typical BPM read-out scheme**

- Pipeline ADC & FPGA
 - 14-16 bit, >300 MSPS, ~70 dB S/N
- Separate analog signal processing for the channels



BPM Read-out Electronics

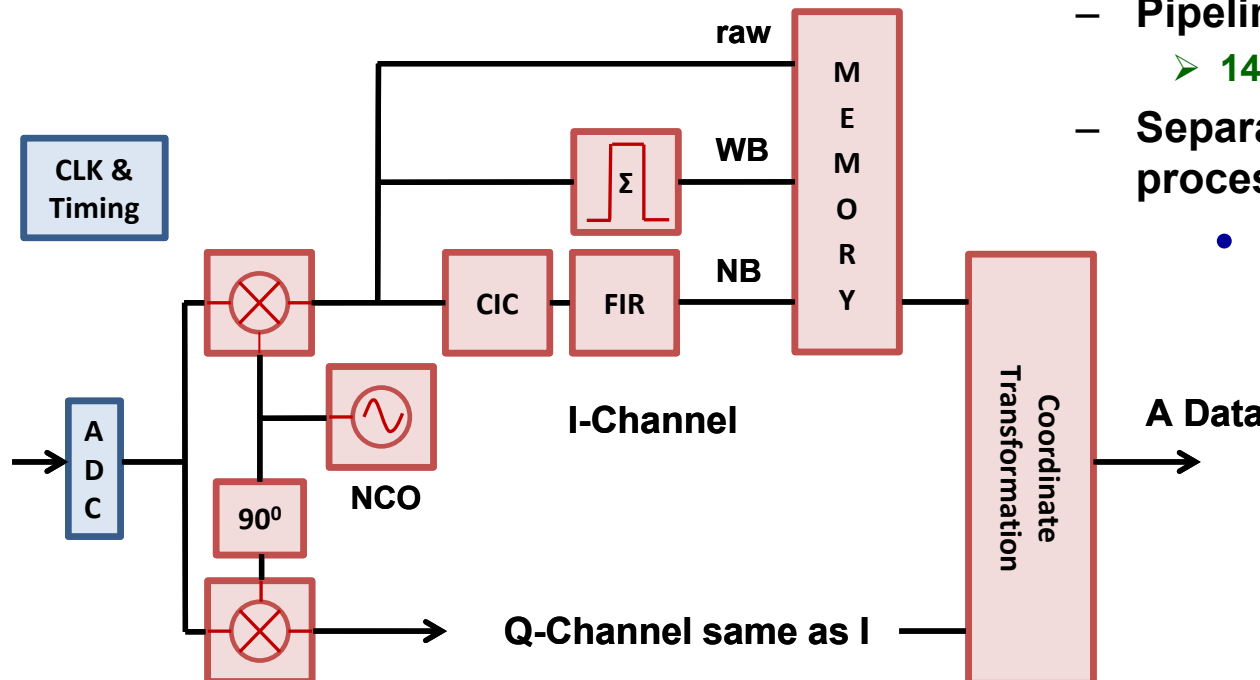


• Typical BPM read-out scheme

- Pipeline ADC & FPGA
 - 14-16 bit, >300 MSPS, ~70 dB S/N
- Separate analog signal processing for the channels

• Choices:

- Analog downconverter?!
- RF locked (sync) CLK & LO signals?!
 - No I-Q required

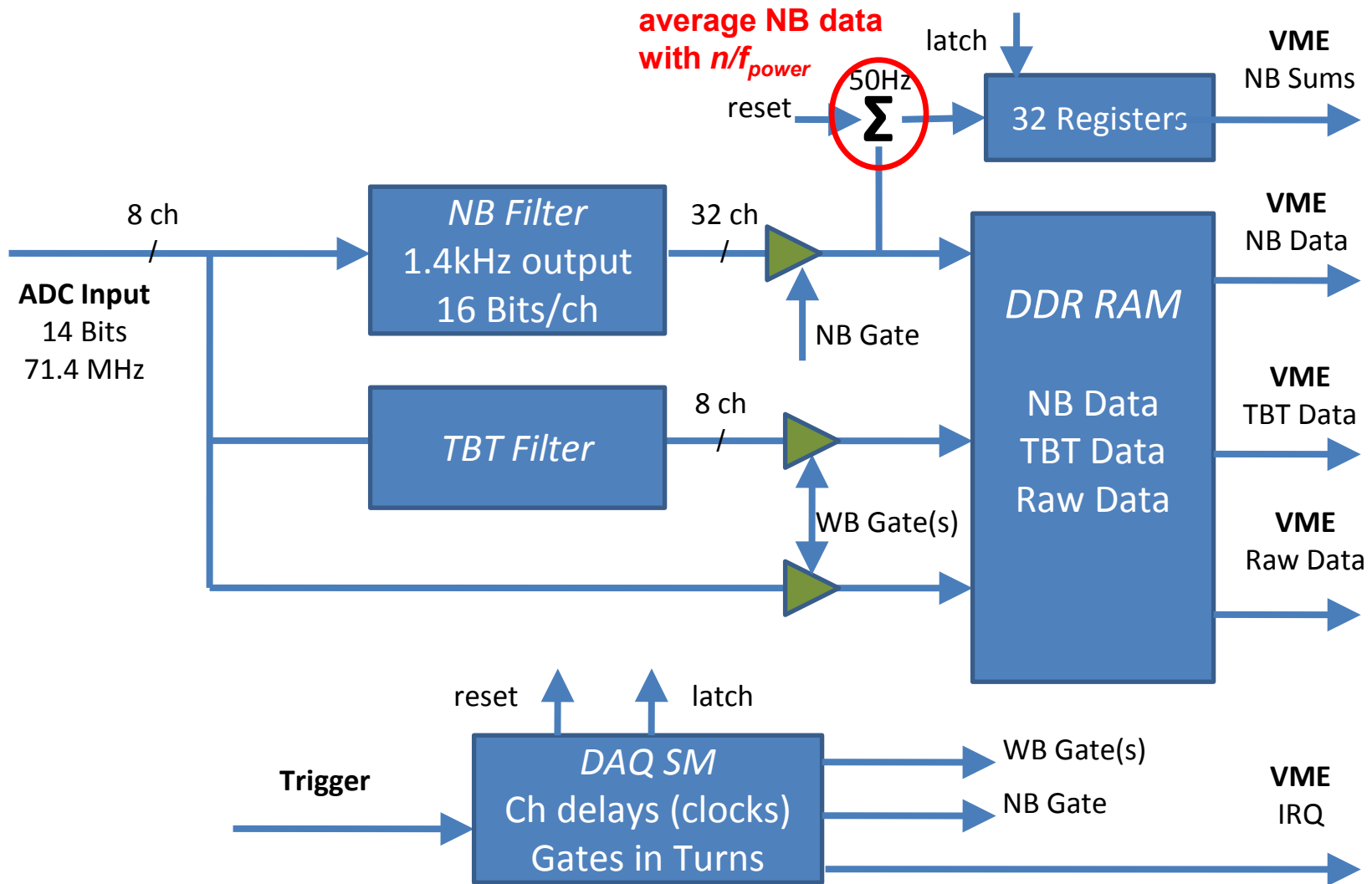


Analog Downconverter vs. Direct Digital Under-Sampling



- **ADC dynamic range is limited to ~70 dB**
 - Not sufficient for most BPM applications
 - Need for analog signal attenuator / gain stages
 - **Requires calibration signal to avoid “electronic offsets”**
- **Analog downconverter**
 - + Certainly necessary for the conditioning of cavity BPM signals
 - + Allows sampling in the 1st Nyquist passband (no undersampling)
 - + Relaxes input RF filter requirements
 - + Relaxed ADC and CLK requirements
 - + May relax cable requirements and improve S/N
 - **Analog hardware installation near the BPM pickup**
 - **Transfer analog IF signals out of the tunnel**
 - **Additional analog hardware required**
 - **Generates additional image frequencies**
 - **Consider image rejection analog mixer!**

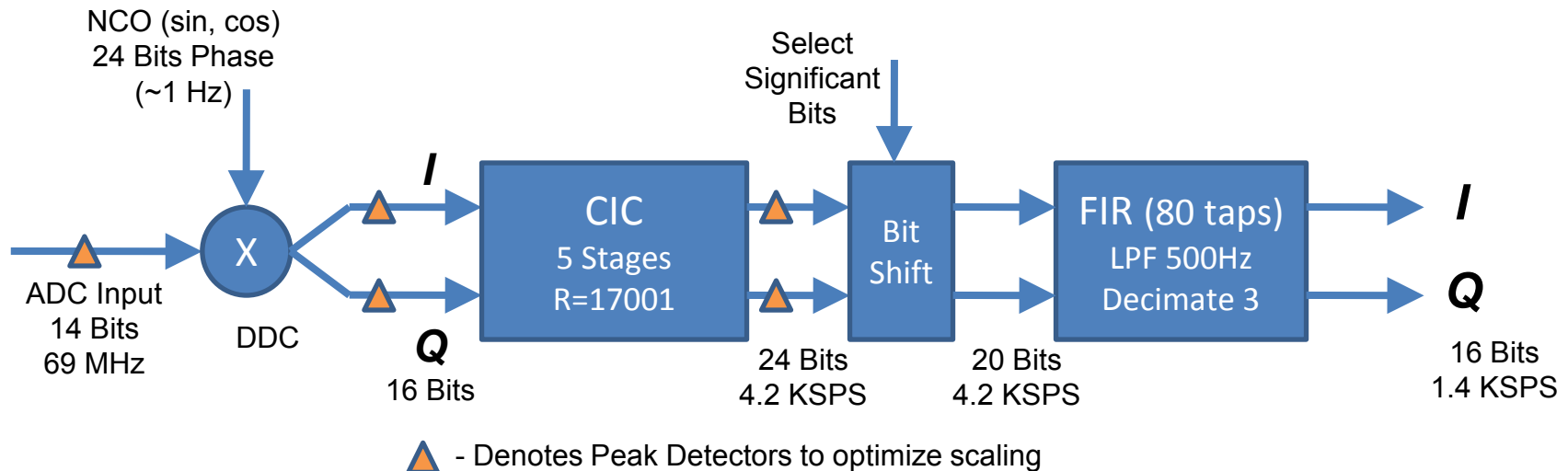
Example: ATF DR BPM Signal Processing



ATF BPM Narrowband Signal Processing



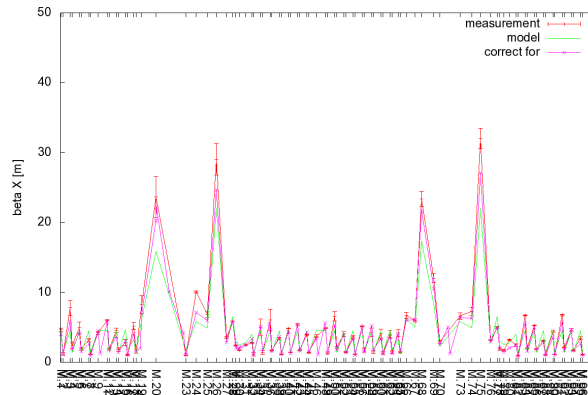
- **Process 8 ADC channels in parallel up to FIR filter**
 - Digitally downconvert each channel into I, Q then filter I, Q independently
 - CIC Filters operating in parallel at 71.4MHz
 - Decimate by 17KSPS to 4.2KSPS output rate
 - 1 Serial FIR Filter processes all 32 CIC Filter outputs
 - 80 tap FIR (400 Hz BW, 500 Hz Stop, -100 db stopband) -> 1KHz effective BW
 - Decimate by 3 to 1.4 KSPS output rate -> ability to easily filter 50Hz
 - Calculate Magnitude from I, Q at 1.4KHz
 - Both Magnitude and I, Q are written to RAM
 - Also able to write I, Q output from CIC to RAM upon request



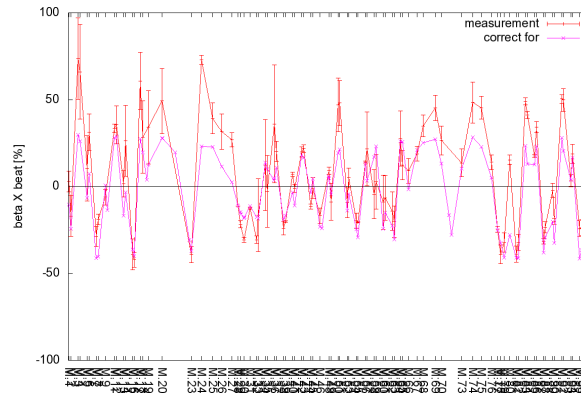
ATF DR Turn-by-Turn Beam Studies

- Beam optics studies with 96 BPMs in the ATF damping ring

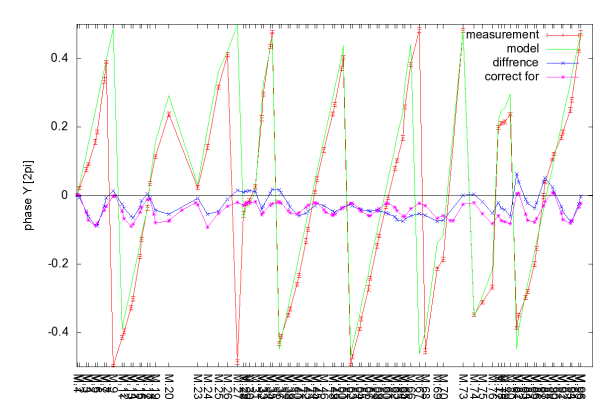
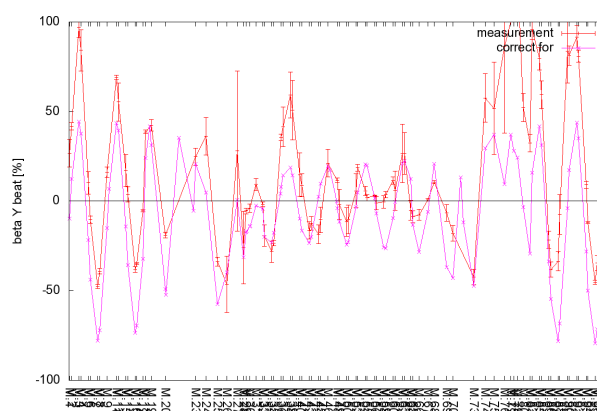
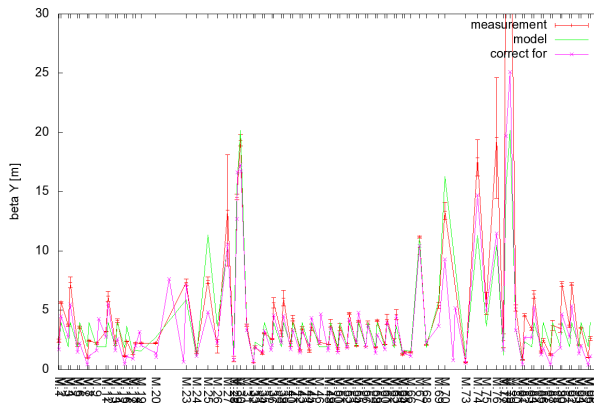
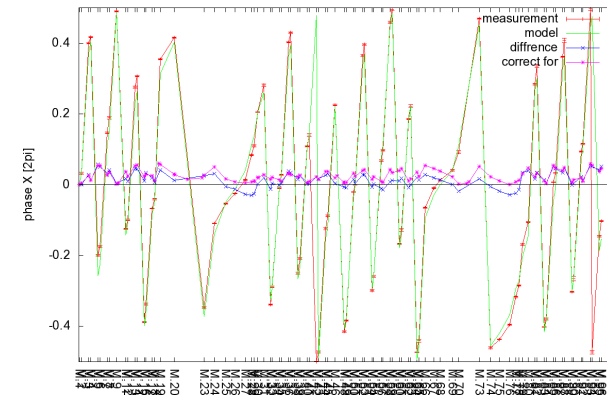
β function measurement



β beating measurement

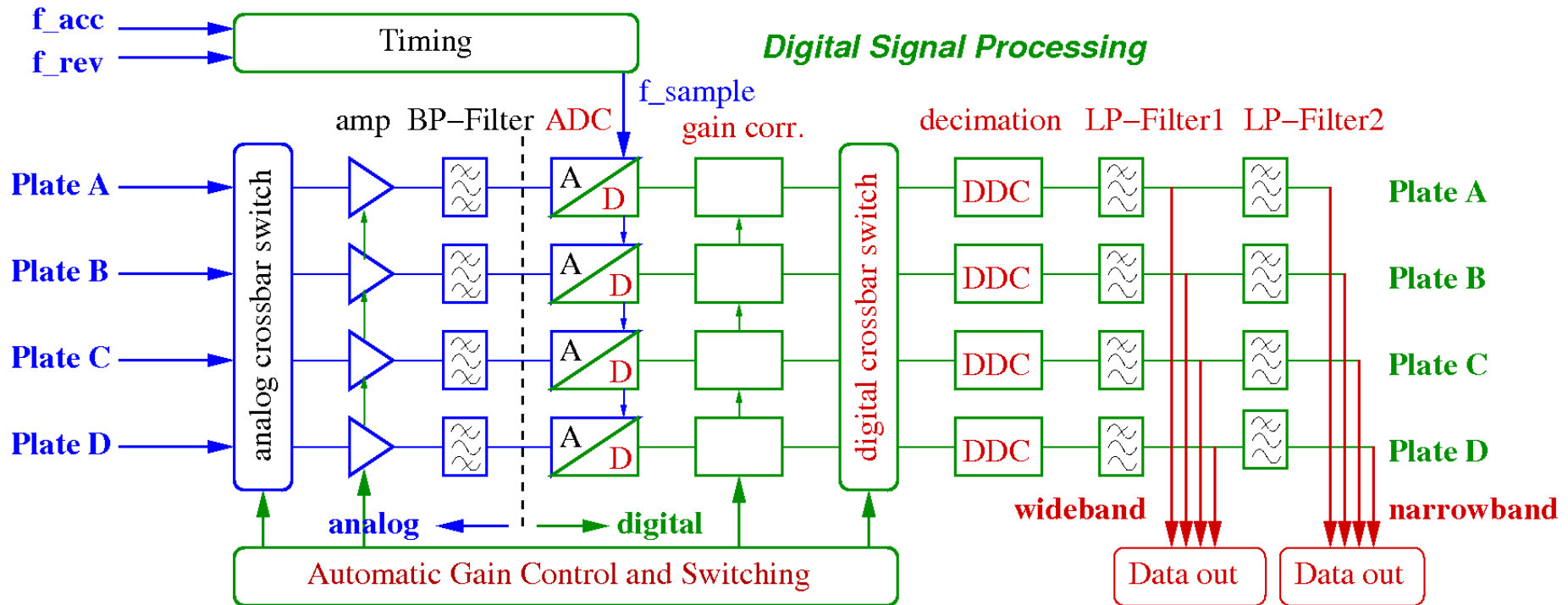


ϕ measurement



courtesy Y. Renier

Libera BPM Electronics

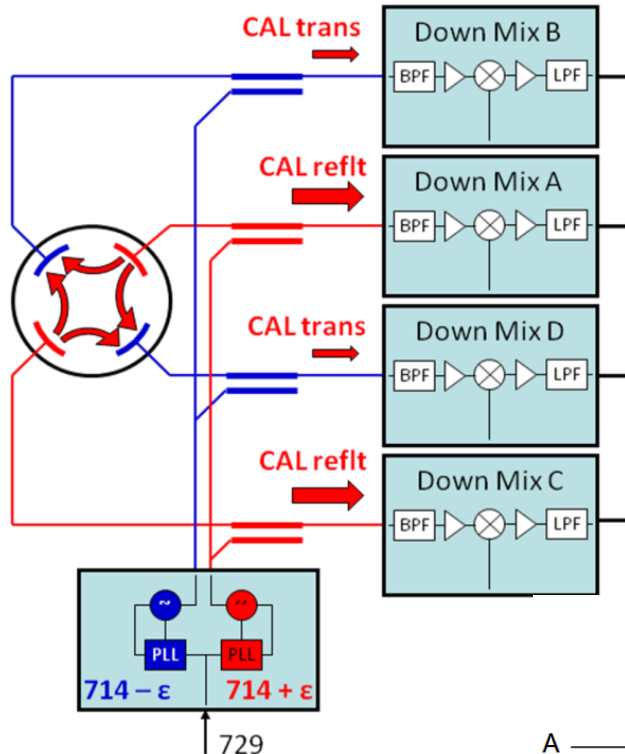


- **Analog & digital crossbar switch**

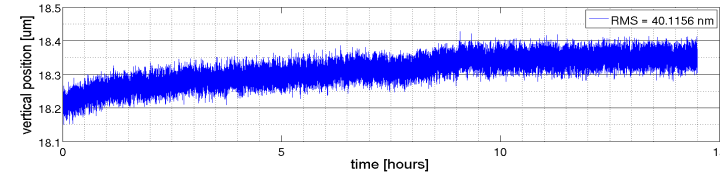
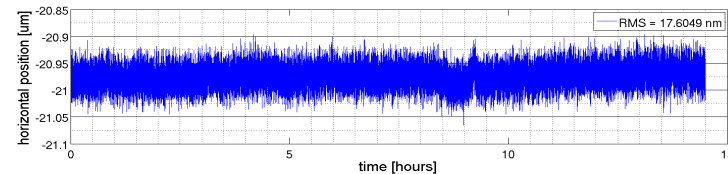
- Compensation of long term drift effects in the analog sections

courtesy
Instrumentation
Technologies

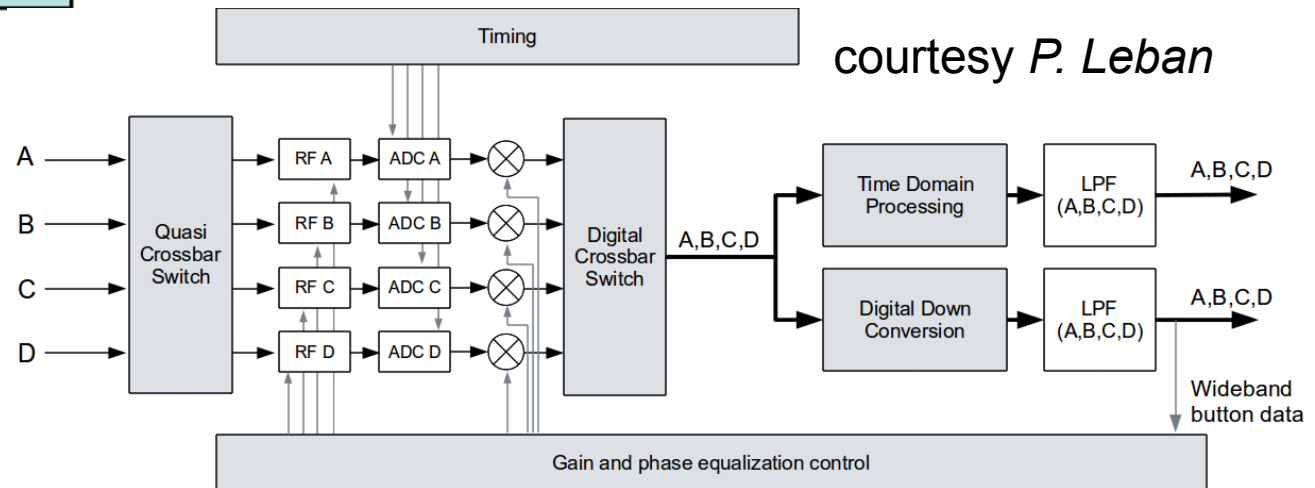
Long-Term Drift Compensation



- ***Libera* crossbar switching technique**
 - **<100 nm stability over 14 hours**



courtesy *P. Leban*

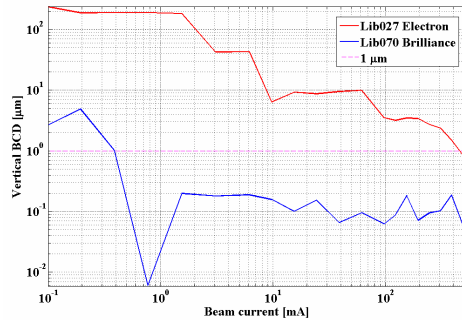


courtesy *N. Eddy*

- **Calibration tone technique (only in narrowband operation)**
ATF (KEK)

Libera BPM Performance

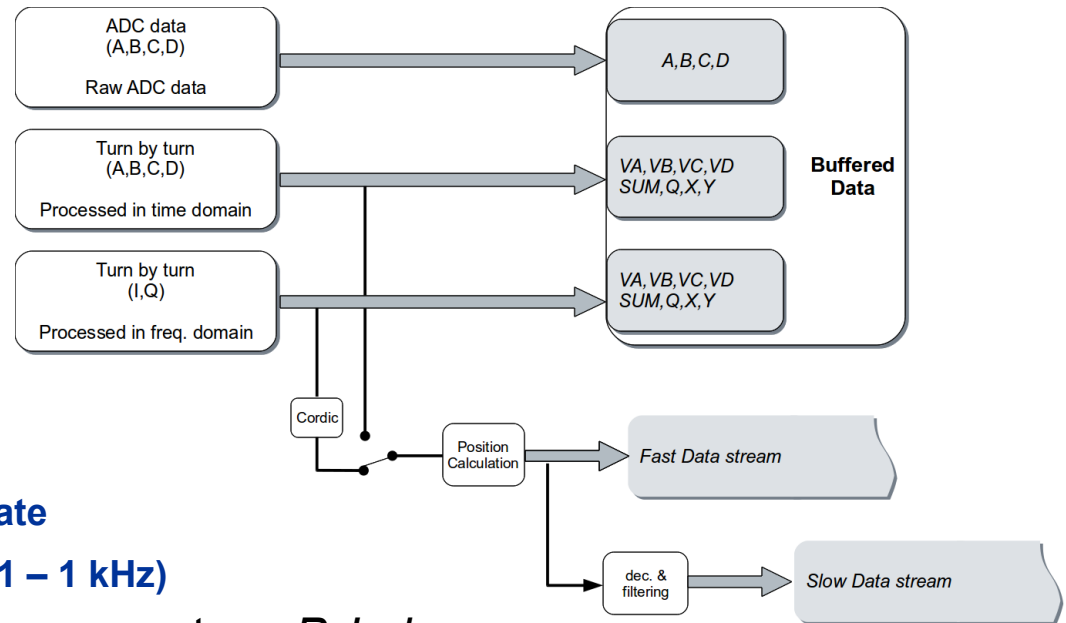
Beam Current Dependence



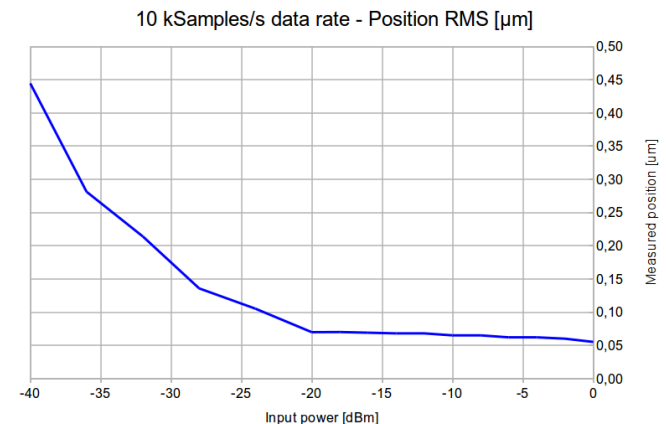
Libera Brilliance +

Electron beam position measurements

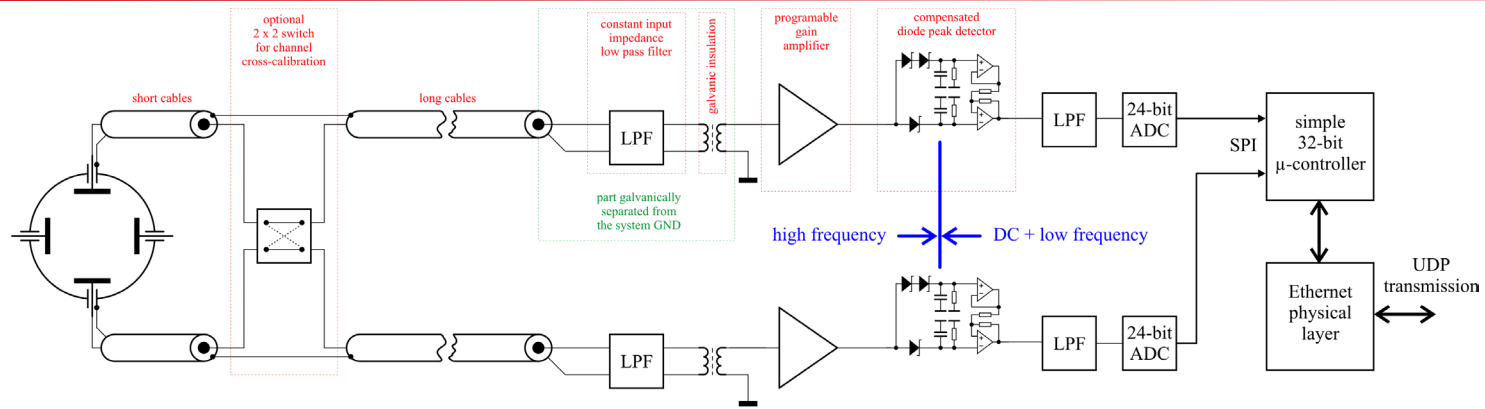
- < 0.5 μm RMS at turn-by-turn data rate
- 40 nm RMS at 10 kS/s data rate (0.01 – 1 kHz)
- 10 nm RMS for slow monitoring
- sub-micron longterm stability
- Temperature drift < 200 nm / °C
- Full Fast Orbit Feedback implementation with magnet output
- Fast Interlock detection (< 100 μs)
- Clean turn to turn measurement using Time-Domain Processing



courtesy *P. Leban*



Compensated Diode Detector for BOM



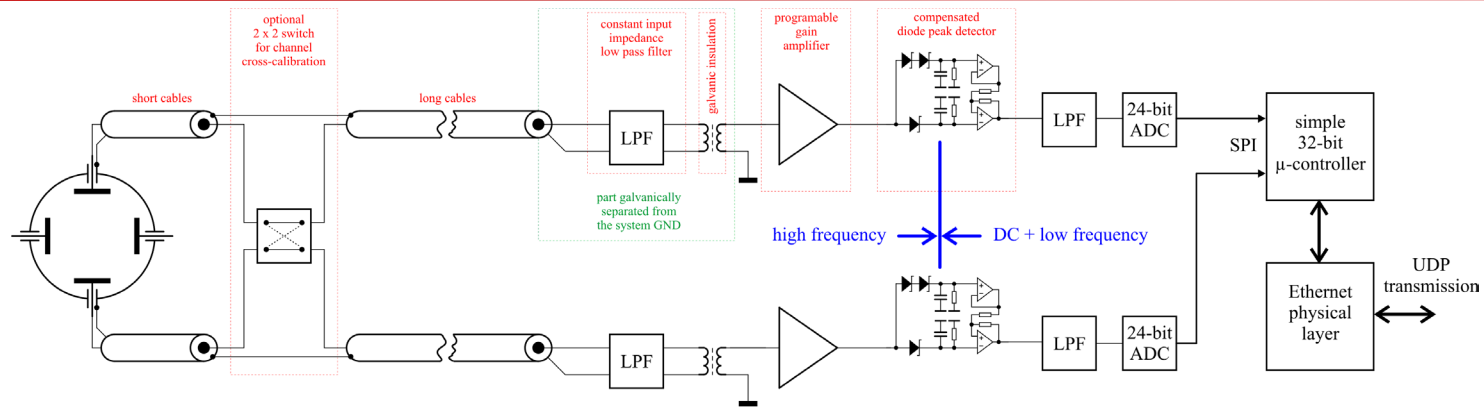
Diode ORbit (DOR) Measurement

2 channels shown for one pick-up plane,
one 19" 1U unit accommodates 8 channels

courtesy M. Gasior

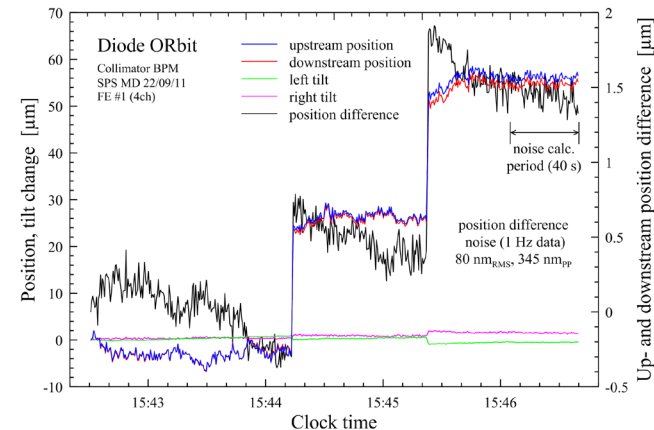
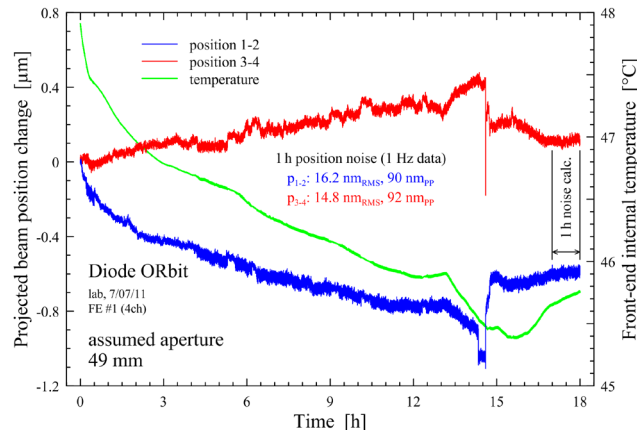
- Sub-micrometre resolution can be achieved with relatively simple hardware and signals from any position pick-up.
- To be used for the future LHC collimators with embedded BPMs.

Compensated Diode Detector for BOM



Diode ORbit (DOR) Measurement

courtesy M. Gasior



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- To be used for the future LHC collimators with embedded BPMs.

Summary & Final Remarks



- **Analog vs. digital**
 - Both technologies require the same great engineering care and well thought through design efforts!
 - Digital signal processing has many advantages, however
 - **Digital is not always and automatically better!**
 - **Digital often tends to be more complex and manpower consuming.**
- **The greater picture**
 - Today, the BPM read-out technology is very advanced, and in most cases not the limiting factor of the BPM system performance.
 - **Sub- μm resolution and stability**
 - **Self offset error correction techniques**
 - **Calibration of pickup non-linearities**
 - Analog, RF and EM issues are still the dominating performance limits
 - **Cables, connectors, RF & analog components, etc.**
 - **Wakefield and impedance effects of the BPM pickup!**

Thanks!



- **Further reading:**

- ***P. Forck*, et.al.: Beam Position Monitors (DITANET School 2011)**
- ***E.B. Hogenauer*: An Economical Class of Digital Filters for Decimation and Interpolation (IEEE Trans. 1981)**
- ***T. Schilcher*: Digital Signal Processing in RF Applications (CAS 2007)**
- ***R.H. Siemann*: Spectral Analysis of Relativistic Bunched Beams (BIW 1996)**
- ***G. Vismara*: Comparison Among Signals Processing for BPM (BIW 2000)**
- ***M. Wendt*: Overview of Recent Trends and Developments for BPM Systems (DIPAC 2011)**
- ***M. Wendt*: Trends in High Precision , High Stability BPMs (ALERT 2014)**
- **Internet: Wikipedia, many articles from companies, etc.**

Typical Performance

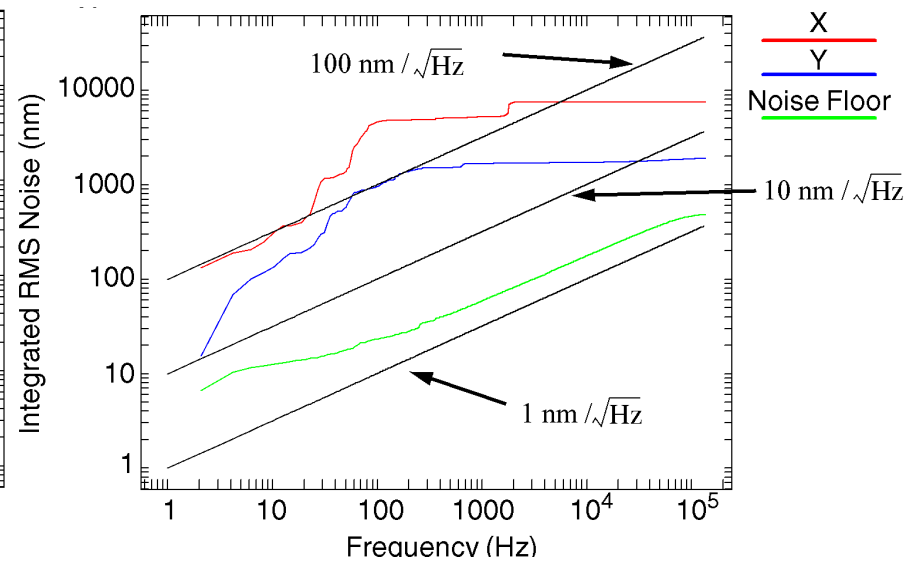
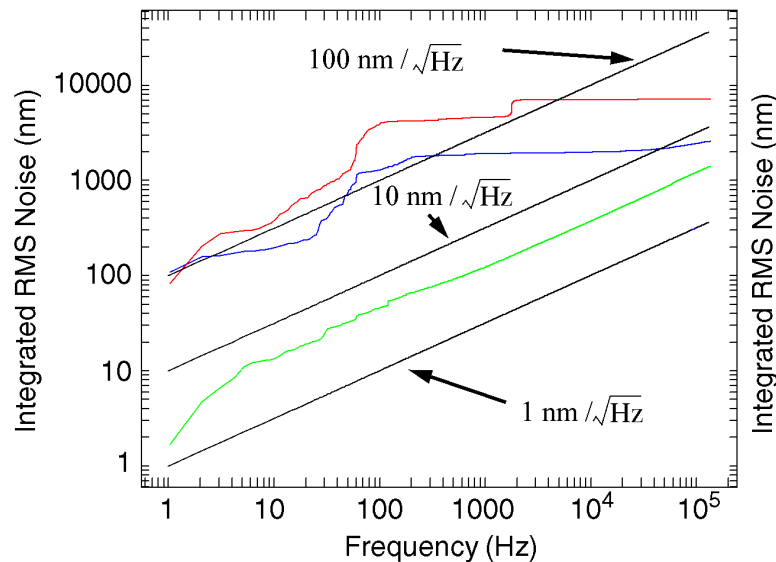


courtesy
G. Decker

**BSP-100 module
(APS ANL)**



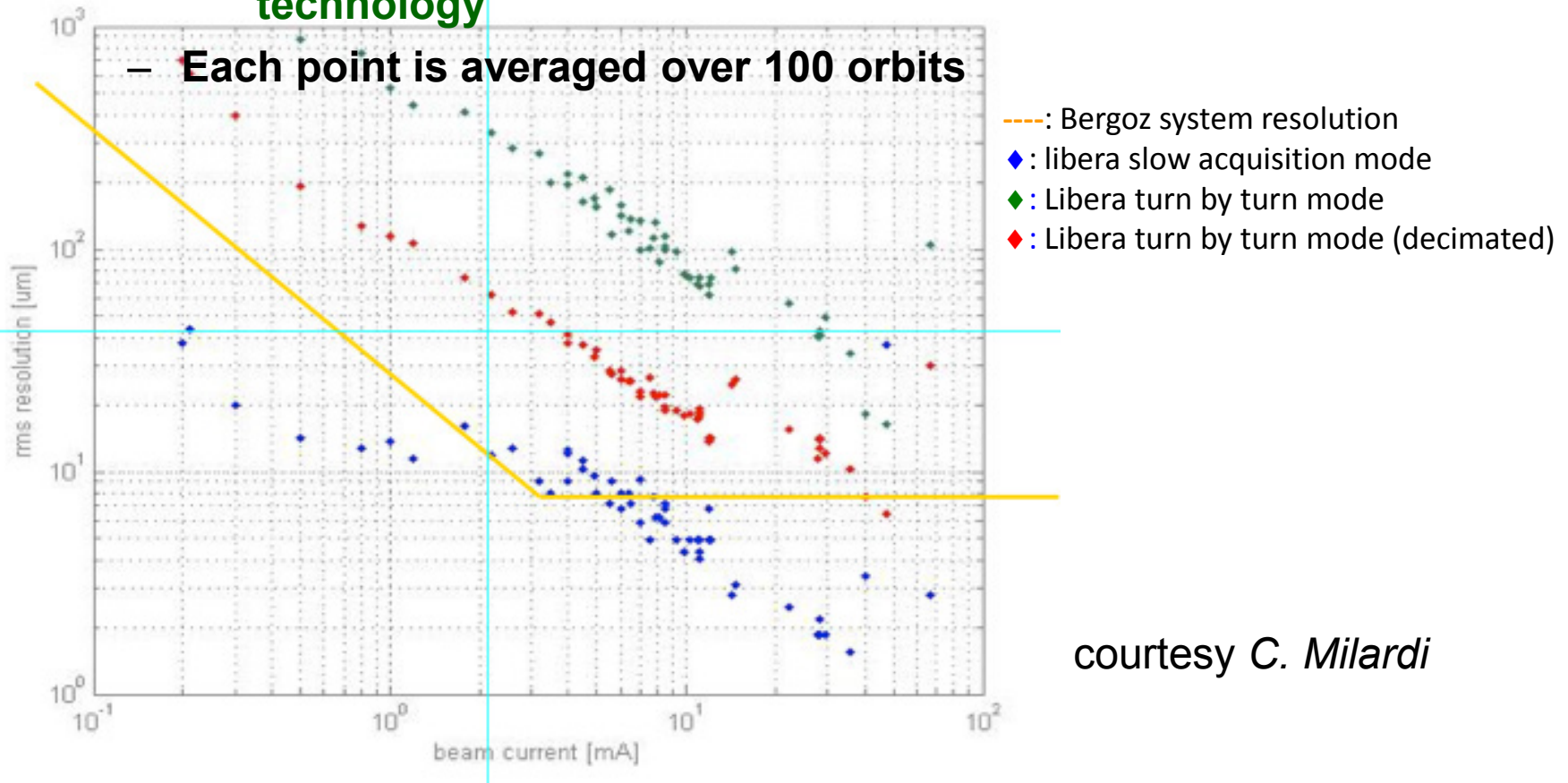
**Libera Brilliance
(@APS ANL)**



BPM Resolution vs. Beam Intensity



- Observed at DAΦNE (INFN-LNF)
 - *Libera* (digital) and *Bergoz* (analog) BPM read-out electronics
 - This study was made some years ago, not with the actual *Libera* technology



courtesy C. Milardi