

A Calibration Method For The RF Front-end Asymmetry Of The DBPM Processor



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- Abstract

Digital Beam Position Monitor (DBPM) processor, designed to measure the beam positions in the LINAC, the booster and the storage ring of a particle accelerator, has been used in many synchrotron radiation facilities. Channels asymmetry, which deteriorates the performance of the DBPM, is inevitable since the RF front-end needs four exactly same blocks. Recently, an RF Front-end board for DBPM has been made with calibration circuit which clears the switching noise. The calibration method will be described in detail, including an overview of the RF board. The beam current dependence, which is sensitive to channels asymmetry, decreases from 160um to 25um after the calibration in the lab test

Introduction

Four exactly same process blocks are needed in DBPM, therefore channels asymmetry which deteriorate the performance of the **DBPM** is unavoidable. There are two widely used ways to suppress the influence. One is channels share technology or channels switch technology, such as MX-BPM and Libera. They both suppress the effect of the channels asymmetry. However, the resolution of the wide-band beam position information (Turn by Turn, for example) will be decrease due to the switching noise which cannot avoid, (Libera, for example). Figure shows the switch noise of TBT data captured from Libera in storage ring of SSRF.



Switches on (blue)and off(red)

RF board





Schematic and picture of the RF board

RF signals process part

• Filter part:

- Central frequency: 499.654 MHz
- **Band width: 694KHz**
- •Gain control : Adjusted rang is **60dBm.**
- •Control logic: Control RF switches and dynamic rang



Frequency response

>Ch1: Start 477.440 MHz -

Gain control test

Calibration part

The calibration circuit consist of a signal generator, a RF switch and control logic. Waveform of the standard signal is sine whose frequency is 499.600 MHz. Amplitude rang of the output from 0 to -50dBm. The standard signal divided into four almost same signals by power splitter.



Waveform of standard signal

Calibration method

The calibration method is to fit the amplitude response function of the four channels. Then the coefficients of function is used to correct the

The amplitude response function are :

 $Y_a = F_a(X_a)$ (1) $Y_b = F_b(X_b)$ (2) $Y_c = F_c(X_c)$ (3)

Figure shows the amplitude response of four	The target function is :
channels.	$\left\{k_1 X + m_1, x_0 \le X \le x_1\right\}$
5.5 x 10 ¹⁰	$F(X) = \begin{cases} k_2 X + m_2, \ x_1 \le X \le x_2 \end{cases}$
Δccording to the sharp of	- ()



Calibration test



Step 1. fitting the correction factors

The input of the RF processing circuit is switched to be the calibration signals(right figure, blue parts). The amplitude of four output are acquired from PC. Target function is used to calculate the correct factors.

Step 2. Correct the asymmetry

RF switches are set to normal mode (right figure, green parts). Input signal of the RF processor comes from a signal generator which is used to simulation the output of the BPM pick-up. In this mode, the input signal will calibrate with the factors.



Calibration results

current dependence Beam evaluate the used to calibration method. Figure shows the calibration results. In this test linear function used as the fitting function. Beam current dependence decreases from 160um to 75um after calibration.



the fitting To increase precision piecewise linear function was used as the target function. As shows in figure the curve will smoother than the left one. Beam current independence decreases from 160um to 25um.





◆ The calibration is valid.

◆ The more sections the curve divided, the better was calibration precision the calibration would get.

•Next goal is to implement the calibration method in the FPGA.